



100W 1/32th Brick Non-isolated DC/DC Power Modules



Non-isolated 1/32 Brick DC/DC Converter

Input voltage: 9~53Vdc

Single output: 3.3~16.5Vdc

Output power: 100W

T31SN12008, 1/32 Brick, 9~53V input, single output, non-isolated DC/DC converters, are the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. This product family provides up to 100 watts of power or 8A of output current. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. Peak efficiency of the 18Vin/15Vout/6A module is up to 98.0%.

FEATURES

Electrical

- High efficiency: 98.0% @ 18Vin/15Vo/6A
- Industry standard 1/32nd brick form factor
- Fixed frequency operation
- Thermal limit, Input UVLO
- · Output OCP Hiccup mode
- Output voltage trim range: 3.3V~16.5V
- Output Remote sense
- Monotonic startup into normal
- No minimum load requirement
- Working altitude to 5000m

Mechanical

Size: Open frame (through hole)

 19.1mm x 23.4 mm x 9.6 mm (0.75 in. x 0.92 in. x 0.38 in.)

Size: Open frame (surface mount)

 19.1mm x 23.4 mm x 10.1mm (0.75 in. x 0.92 in. x 0.40 in.)

Size: Potting (standard case)

 23.1mm x 27.6 mm x 12.7 mm (0.91 in. x 1.09 in. x 0.50 in.)

Size: Potting (flanged case)

 23.1mm x 38.9 mm x 12.7 mm (0.91 in. x 1.53 in. x 0.50 in.)

Soldering Methods

- Wave soldering
- Hand soldering
- Reflow soldering (MSL of rating 3)

Safety & Certificate

- IEC/EN/UL/CSA 62368-1,2nd edition
- IEC/EN/UL/CSA 60950-1,2nd edition+A2
- ISO 9001, TL 9000, ISO 14001, QS 9000,
- OHSAS18001 certified manufacturing facility

OPTIONS

- Negative or Positive Remote On/Off
- Power Good
- Through hole or SMD pins
- Open frame or Potting
- · Potting with Standard case or Flanged case

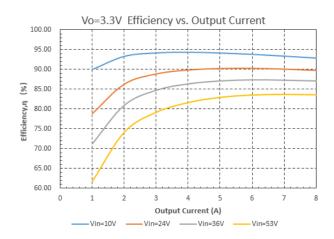


ELECTRICAL SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS Input Voltage Continuous NA	PARAMETER	RAMETER NOTES and CONDITIONS T31SN12008						
Input Voltage Continuous			Min.	Тур.	Max.	Units		
Continuous Transient (100ms) NA								
Transient (100ms) Operating Ambient Temperature Storage Temp			0.05			Vdc		
Operating Ambient Temperature Storage Te		NΙΔ	-0.25		55	Vdc Vdc		
Storage Temperature		IVA	-40		85	°C		
Input/Output Isolation Voltage None						°C		
INPUT CHARACTERISTICS		None				Vdc		
Operating Input Voltage Vin>Vo 9 53 Vin								
Turn-On Voltage Threshold 1		Vin>Vo	9		53	Vdc		
Turn-Off Voltage Threshold Cuckout Hysteresis Voltage Maximum Input Current Vin=24V,Vo=12V, Io=0A 75 m Vin=18V ,Vo=12V, Io=0A 75 m Vin=24V,Vo=12V, Io=0A Vin=24V,Vo=12V,Vo								
Lockout Hysteresis Voltage Maximum Input Current Vin=18V ,Vo=15; Io=Io,max 10						Vdc		
Maximum Input Current No-Load Input Current No-Load Input Current Vin=18V,Vo=15V, Io=DA 75 mm Vin=24V,Vo=12V, Io=DA 0.4 mm Vin=24V,Vo=12V, Io=DA 0.5 mm Vin=24V,Vo=12V, Io=DA 0.5 mm Vin=24V,Vo=12V, Io=DA 0.5 mm Vin=24V,Vo=12V, Io=DA 0.5 Vin=24V,Vo=12V, Io=DA 0.5 Vin=24V,Vo=12V, Io=DA 0.5 Vin=24V,Vo=12V,Vo=10,Vo=DA 0.5 Vin=24V,Vo=12V,Vo=10,Vo=DA 0.5 Vin=24V,Vo=12V,Vo=10,Vo=DA 0.5 Vin=24V,Vo=12V,Vo=10,Vo=DA 0.5 Vin=24V,Vo=12V,Vo=10,Vo=DA 0.5 Vin=24V,Vo=DA 0.5 0.5 Vin=24V,Vo=DA 0.5 0					9	Vdc		
No-Load Input Current Vin=24V/vo=12V, lo=0A 75 mm Off Converter Input Current Vin=24V/vo=12V, lo=0A 0.4 mm Vin=24V/vo=12V, Vin=0A 0.4 mm Vin=24V/vo=12V, Vin=0A 0.4 vin=24V/vo=12V, Vin		\fig. 40\\ \\-45\\ \-15\\ \\		1	40	Vdc		
Off Converter Input Current Input Parter In				75	10	Α		
Inrush Current (1)						mA		
Input Veltage Ripple Current 24Vin,Vo=3.3V,P-P thru 33µH inductor, 5Hz to 20MHz 94 mt 120 Hz 50 dt 50		VIII-24V, VO-12V, 10-0A		0.4	1	A ² s		
Input Voltage Ripple Rejection 120 Hz 50 di		24Vin.Vo=3.3V.P-P thru 33uH inductor, 5Hz to 20MHz		94		mA		
OUTPUT CHARACTERISTICS Output Voltage Set Point V _m =48V, Vo=12V, I _o =I _{0,max} , T _c =25°C -2 +2 9 Output Regulation Io=I _{0,min} to I _{0,max} 0.5 %V Line Regulation V _m =9V to 53V 0.2 %V Total Output Voltage Range Over sample load, ine and temperature -4 +1 %V Total Output Voltage Ripple and Noise SHz to 20MHz bandwidth -4 +4 %V Peak-to-Peak Vo=48V, Vo=12V, Full Load, 0.1µF ceramic, 22µF ceramic 56 m RMS Operating Output Current Range Vo=6.5V 0 8 A Operating Output Current Range Vo=6.5V 0 6 A Output Over Current Protection(hiccup mode) Vo=6.5V 0 6 A DYNAMIC CHARACTERISTICS 48V _{in} , Vo=12V, 0.1µF ceramic, 22µF ceramic load cap, 1 A/µs 360 m Output Over Current Protection(hiccup mode) 48V _{in} , Vo=12V, 0.1µF ceramic, 22µF ceramic load cap, 1 A/µs 360 m Torribut Voltage Step Change in Output Current Negative Step Change in Output Current Negative Step Change in Output Current Negative Step						dB		
Output Voltage Set Point		· · ·						
Output Negulation Load Regulation Load Regulation Io=I _{0, min} to I _{0, max} 0.5 %V Line Regulation Temperature Regulation T _x =40°C to 85°C -1 +1 41 %V Total Output Voltage Range Over sample load, line and temperature -4 +4 %V Start to 20MHz bandwidth -4 5Hz to 20MHz bandwidth -5 56 mr Start-Up Ceramic Qutput Current Range Quertaing Output Current A8V _m , Vo=12V, 0.1μF ceramic, 22μF ceramic load cap, 1 A/μs Positive Step Change in Output Current A8V _m , Vo=12V, 0.1μF ceramic, 22μF ceramic load cap, 1 A/μs Positive Step Change in Output Current A8V _m , Vo=12V, 0.1μF ceramic, 22μF ceramic load cap, 1 A/μs A8V _m , Vo=12V, 0.1μF ceramic load cap, 1 A/μs A8V _m , Vo=12V, 0.1μF ceramic load cap, 1 A/μs A8V _m , Vo=12V, 0.1μF ceramic load cap, 1 A/μs A8V _m , Vo=12V, 0.1μF ceramic load cap, 1 A/μs A8V _m , Vo=12V, Vo=12V, Vo=10V load Vo=10V Vo, Vo, vom A mm Available Time A8V _m , Vo=12V, Vo=10V load Vo=10V Vo, vom A mm Available Time A		V _{in} =48V, Vo=12V, I _o =I _{o.max} , T _c =25°C	-2		+2	%		
Line Regulation V _m =9V to 53V	Output Regulation	. ,						
Temperăture Regulation T _c =-40°C to 85°C -1 +1 %V						$%V_{o,set}$		
Total Output Voltage Range Over sample load, line and temperature -4				0.2		$%V_{o,set}$		
Output Voltage Ripple and Noise Peak-to-Peak Vin=48V, Vo=12V, Full Load, 0.1 μF ceramic, 22μF ceramic 20μ Se						$%V_{o,set}$		
Peak-to-Peak Vin=48V, Vo=12V, Full Load, 0.1μF ceramic, 22μF ceramic, 20M BW Nin=48V, Vo=12V, Full Load, 0.1μF ceramic, 20M BW Vin=48V, Vo=12V, Full Load, 0.1μF ceramic, 22μF ceramic 20 mm Vo=6.5V Vin=48V, Vo=6.5V Vin=6.5V V			-4		+4	%V _{o,set}		
RMS	Output Voltage Ripple and Noise							
RMS	Peak-to-Peak			56		mV		
Operating Output Current Range	D140					.,		
Operating Output Current Range			•	20		mV		
Output Over Current Protection(hiccup mode) 14 A DYNAMIC CHARACTERISTICS Output Voltage Current Transient 48V _{In} , Vo=12V, 0.1µF ceramic, 22µF ceramic load cap, 1 A/µs 360 m Positive Step Change in Output Current Negative Step Change in Output Current Settling Time (within 1% nominal Vout) 25% lo.max to 25% lo.max 360 m Settling Time (within 1% nominal Vout) 000 300 µ Turn-On Delay and Rise Time Start-Up Delay Time From Input Voltage Start-Up Delay Time From On/Off Control Output Voltage Rise Time Output Voltage Rise Time 0n/Off=On, from Vin=Turn-On Threshold to Vo=10% Vo,nom 4 m Output Voltage Rise Time Output Capacitance (note1) Full load; 5% overshoot of Vout at startup 22 1200 µ EFFICIENCY 100% Load Vin=12V,Vo=5V,lo=lo,max 95.0 9 100% Load Vin=18V,Vo=15V,lo=lo,max 97.5 9 100% Load Vin=18V,Vo=5V,lo=lo,max 92.7 9 100% Load Vin=24V,Vo=5V,lo=lo,max 96.5 9 100% Load Vin=48V,Vo=12V,lo=lo,max 93.0 9 100% Load Vin=48V,Vo=10,lo=lo,max 93.0 9						A A		
DYNAMIC CHARACTERISTICS		VO/0.5V	U	1/	O	A		
Output Voltage Current Transient Positive Step Change in Output Current Negative Step Change in Output Current Settling Time (within 1% nominal V _{out}) Turn-On Delay and Rise Time Start-Up Delay Time From Input Voltage Start-Up Delay Time From On/Off Control Output Voltage Rise Time V _i =10% to 90% V _{0,nom} 7 mm Output Capacitance (note1) Full load; 5% overshoot of V _{out} at startup 22 1200 µ EFFICIENCY Output Voltage Rise Time V _i =12V,Vo=5V,Io=Io,max 97.5 % Output Voltage Rise Time V _i =12V,Vo=5V,Io=Io,max 97.5 % Output Voltage Rise Time V _i =12V,Vo=5V,Io=Io,max 97.5 % Output Voltage Rise Time V _i =12V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max 98.0 % Output Voltage Rise Time V _i =24V,Vo=5V,Io=Io,max V _i =24V,Vo=5				14		^		
Positive Step Change in Output Current Negative Step Change in Output Current Settling Time (within 1% nominal Vout) Turn-On Delay and Rise Time Start-Up Delay Time From Input Voltage Start-Up Delay Time From On/Off Control Output Voltage Rise Time Output Voltage Rise Time Output Voltage Rise Time Output Capacitance (note1) Full load; 5% overshoot of Vout at startup Ful		48V: Vo=12V 0 1uE ceramic 22uE ceramic load can 1 A/us						
Negative Step Change in Output Current Settling Time (within 1% nominal Vout) 25% Io.max to 75% Io.max 360 mm 30 mm				360		mV		
Turn-On Delay and Rise Time Start-Up Delay Time From Input Voltage On/Off=On, from V _{in} =Turn-On Threshold to V₀=10% V₀,nom 4 m Start-Up Delay Time From On/Off Control Output Voltage Rise Time Vin=Vin,nom, from On/Off=On to V₀=10% V₀,nom 4 m Output Capacitance (note1) Full load; 5% overshoot of V₀ut at startup 22 1200 μ EFFICIENCY 100% Load Vin=12V,Vo=5V,lo=lo,max 95.0 % 100% Load Vin=12V,Vo=9V,lo=lo,max 97.5 % 100% Load Vin=18V,Vo=15V,lo=lo,max 98.0 % 100% Load Vin=24V,Vo=5V,lo=lo,max 96.5 % 100% Load Vin=24V,Vo=12V,lo=lo,max 96.5 % 100% Load Vin=48V,Vo=5V,lo=lo,max 89.0 % 100% Load Vin=48V,Vo=5V,lo=lo,max 93.0 % FEATURE CHARACTERISTICS Switching Frequency 300 KH On/Off Control, Negative Remote On/Off logic Logic Low (Module On) Von/off 0 0.5 N						mV		
Start-Up Delay Time From Input Voltage Start-Up Delay Time From On/Off Control Output Voltage Rise Time	Settling Time (within 1% nominal Vout)			30		μs		
Start-Up Delay Time From On/Off Control Output Voltage Rise Time								
Output Voltage Rise Time V₀=10% to 90% V₀,nom 7 m Output Capacitance (note1) Full load; 5% overshoot of V₀ut at startup 22 1200 μ EFFICIENCY 100% Load Vᵢn=12V,Vo=5V,Io=Io,max 95.0 % 100% Load Vᵢn=12V,Vo=9V,Io=Io,max 97.5 % 100% Load Vᵢn=18V,Vo=15V,Io=Io,max 98.0 % 100% Load Vᵢn=24V,Vo=5V,Io=Io,max 92.7 % 100% Load Vᵢn=24V,Vo=12V,Io=Io,max 96.5 % 100% Load Vin=48V,Vo=5V,Io=Io,max 89.0 % FEATURE CHARACTERISTICS Switching Frequency 300 KH On/Off Control, Negative Remote On/Off logic Von/off 0 0.5 N						mS		
Output Capacitance (note1) Full load; 5% overshoot of V _{out} at startup 22 1200 μ EFFICIENCY 100% Load V _{in} =12V,Vo=5V,lo=lo,max 95.0 % 100% Load V _{in} =12V,Vo=9V,lo=lo,max 97.5 % 100% Load V _{in} =18V,Vo=15V,lo=lo,max 98.0 % 100% Load V _{in} =24V,Vo=5V,lo=lo,max 92.7 % 100% Load V _{in} =24V,Vo=12V,lo=lo,max 96.5 % 100% Load Vin=48V,Vo=5V,lo=lo,max 89.0 % 100% Load Vin=48V,Vo=12V,lo=lo,max 93.0 % FEATURE CHARACTERISTICS Switching Frequency 300 KH On/Off Control, Negative Remote On/Off logic Von/off 0 0.5 N						mS		
### EFFICIENCY 100% Load 100% Load 100% Load 100% Load 100% Load 100% Load Vin=12V,Vo=9V,lo=lo,max 97.5 98.0 98.0 99.0 99.0 90.0 9			00	7	4000	mS		
100% Load		ruii ioau, 5% oversnoot of V _{out} at startup	22		1200	μF		
100% Load		\/: =12\/\/o=5\/ o=lo may		OF O		%		
100% Load						% %		
100% Load						%		
100% Load						%		
100% Load						%		
FEATURE CHARACTERISTICS Switching Frequency On/Off Control, Negative Remote On/Off logic Logic Low (Module On) Von/off 0 0.5						%		
Switching Frequency On/Off Control, Negative Remote On/Off logic Logic Low (Module On) Von/off 0 0.5		Vin=48V,Vo=12V,Io=Io,max		93.0		%		
On/Off Control, Negative Remote On/Off logic Logic Low (Module On) Von/off 0 0.5								
Logic Low (Module On) V _{on/off} 0 0.5 \				300		KHz		
		V _{on/off}				V		
3 3 ()		V _{on/off}	3.1		13.2	V		
On/Off Control, Positive Remote On/Off logic	On/Off Control, Positive Remote On/Off logic							
		V _{on/off}				V		
99(0 0 0		3.1		13.2	V		
	` , ,	I _{on/off} at V _{on/off} =0.0V		0.4		mA		
Output Voltage Adjustment Range 3.3 16.5	Output Voltage Adjustment Range		3.3		16.5	V		
Output Voltage Remote Sense Range 0 +5 %Vo	Output Voltage Remote Sense Range		0		+5	$%V_{o,nom}$		
GENERAL SPECIFICATIONS	GENERAL SPECIFICATIONS							
MTBF I _o =80% of I _o max; T _a =40°C, airflow rate=300LFM 14 Mhc	MTBF	I _o =80% of I _{o, max} ; T _a =40°C, airflow rate=300LFM		14		Mhours		
		Open frame				grams		
	9	Potting		20		grams		

Note: For applications with higher output capacitive load, please contact $\ensuremath{\mathsf{Delta}}$.

T_A=25°C



Vo=5V Efficiency vs. Output Current

95.00

90.00

85.00

75.00

70.00

1 2 3 4 5 6 7 8

Output Current (A)

Vin=10V Vin=24V Vin=36V Vin=53V

Figure 1: Efficiency vs. Output Current (Vo=3.3V)

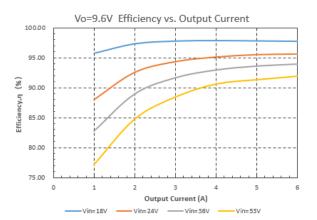


Figure 2: Efficiency vs. Output Current (Vo=5V)

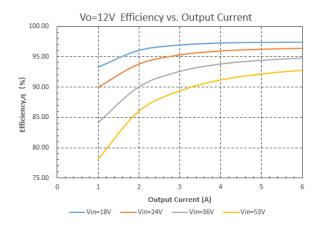


Figure 3: Efficiency vs. Output Current (Vo=9.6V)

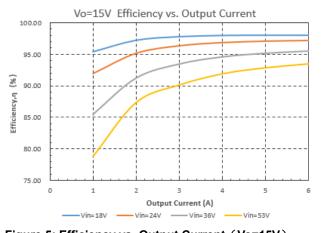


Figure 4: Efficiency vs. Output Current (Vo=12V)

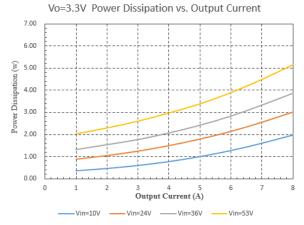
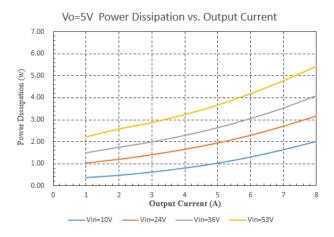


Figure 5: Efficiency vs. Output Current (Vo=15V)

Figure 6: Power Dissipation vs. Output Current(Vo=3.3V)





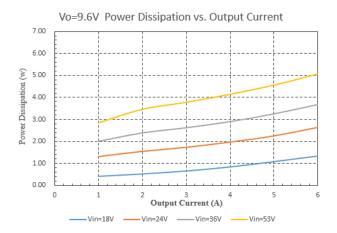
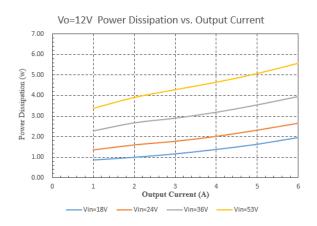


Figure 7: Power Dissipation vs. Output Current(Vo=5V)

Figure 8: Power Dissipation vs. Output Current(Vo=9.6V)



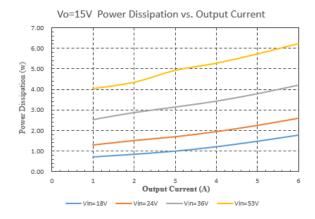
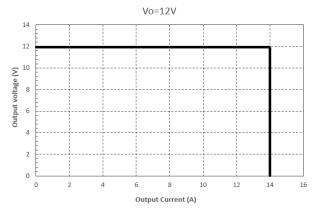


Figure 9: Power Dissipation vs. Output Current(Vo=12V)

Figure 10: Power Dissipation vs. Output Current(Vo=15V)



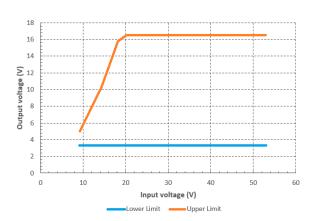
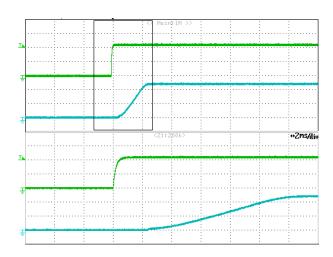


Figure 11: Output Voltage vs. Output current showing typical current limit curves and converter shutdown points.

Figure 12: Output Voltage vs. Input Voltage Operating Range



TA=25°C, Vin=48Vdc, Vo=12V



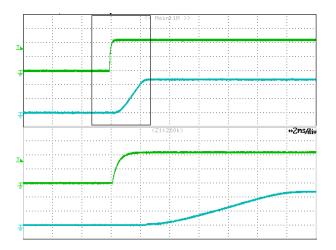


Figure 13: Remote On/Off Start-up at open load

Time: 10ms/div.

V_{remote On/Off signal}(top trace): 2V/div; V_{out} (bottom trace): 5V/div.

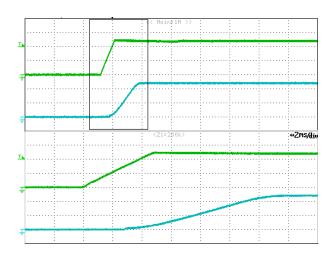


Figure 14: Remote On/Off Start-up at full load

Time: 10ms/div.

V_{remote On/Off signal}(top trace): 2V/div; V_{out} (bottom trace): 5V/div.

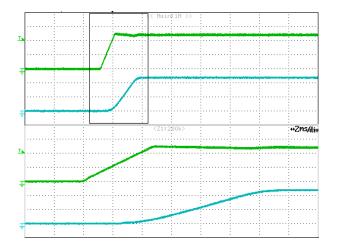


Figure 15: Input Voltage Start-up at open load

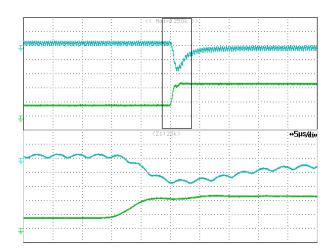
Time: 10ms/div. Vin (top trace): 20V/div; Vout (bottom trace): 5V/div.

Figure 16: Input Voltage Start-up at full load Time: 10ms/div.

V_{in} (top trace): 20V/div; V_{out} (bottom trace): 5V/div.



TA=25°C, Vin=48Vdc, Vo=12V



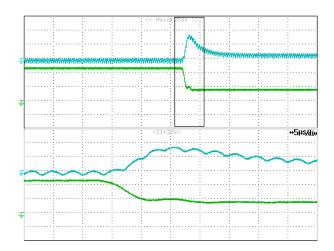


Figure 17: Transient Response

(1A/ μ s step change in load from 25% to 75% of I_{o, max}) V_{out} (top trace): 0.2 V/div, 50us/div;

lout (bottom trace): 2A/div.

Load cap: 22µF ceramic capacitor and 1µF ceramic capacitor. Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module

Figure 18: Transient Response

(1A/µs step change in load from 75% to 25% of I_{o, max})

 $V_{out} \ (top \ trace) \hbox{:} 0.2 V/\hbox{div}, \ 50 \hbox{us}/\hbox{div};$

lout (bottom trace): 2A/div.

Load cap: $22\mu F$ ceramic capacitor and $1\mu F$ ceramic capacitor. Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module



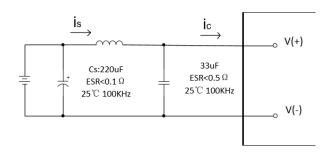


Figure 19: Test Setup Diagram for Input Ripple Current Note: Measured input reflected-ripple current with a simulated source Inductance (LTEST) of 12µH. Capacitor Cs offset possible battery impedance. Measure current as shown above.

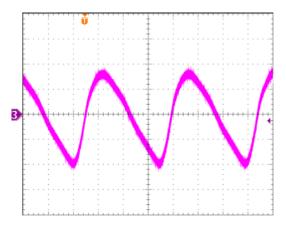


Figure 20: Input Terminal Ripple Current, ic, at max output current, 5Vdc output voltage and 48Vdc input voltage with $12\mu H$ source impedance and $33\mu F$ electrolytic capacitor (100 mA/div, 1us/div).

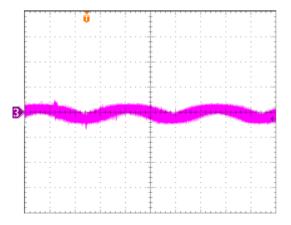


Figure 21: Input Reflected Ripple Current, is, through a $12\mu H$ source inductor at 48Vdc input voltage, 5Vdc output voltage and max load current (100mA/div, 1us/div).

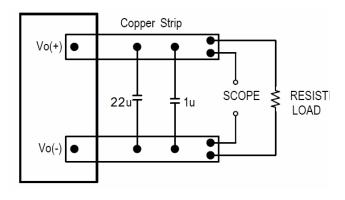


Figure 22: Test Setup for Output Voltage Noise and Ripple

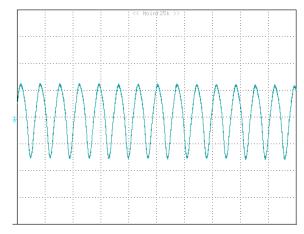
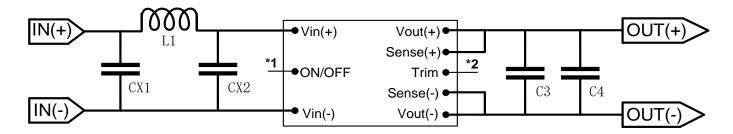


Figure 23: Output Voltage Ripple and Noise at 48Vdc input voltage, 12Vdc output voltage and max load current (20 mV/div, 5us/div)

Load cap: $1\mu F$ ceramic capacitor and $22\mu F$ ceramic capacitor. Bandwidth: 20MHz



Typical EMI filter circuit for EN55032 Class B Conducted Emission



*1,2: Please refer to page10 for the On/Off (pin2) and Trim (pin6) implementation.

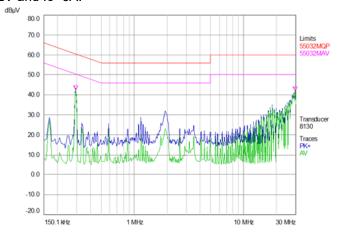
Location	Vendor P/N	Description	Qty	Vendor	Purpose
CX1	PCR1K470MCL1GS	CAP AL SP 80V 47uF M 10*10 SMD TP	1	NICHICON	EMC
L1	CMLS136E-3R3MS	INDUCTOR MPT 3.3uH +/-20% 22A SMD	1	CYNTEC	EMC
CX2	UVY2A101MPD1TD	CAP AL 100V 100uF M 10*16 TP P5	1	NICHICON	EMC
C3	GRM32ER71E226KE1 5L	CAP MC SMD 25V 22uF K X7R 1210 2.5	1	MURATA	RIPPLE
C4	C1206X104K101TX	CAP MC SMD 100V 0.1uF K X7R 1206 EPOXY 0.85	2	HOLY STONE	RIPPLE

Layout and EMI considerations

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team.

Below is the EN55032 Class B Conducted Emission test result using typical EMI filter circuit.

At T = +25°C. Vin=32V, Vo=12V and Io=6A.



T31SN12008 EN55032 Class B Conducted Emission Test Result

Recommended PCB Layout

It is suggested to use multiple layers PCB and large size copper on system board which connects to pins of module, that can achieve better thermal performance.



Input Source Impedance

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few μH , we advise $33\mu F\text{-}100\mu F$ electrolytic capacitor (ESR < 0.7Ω at 100kHz) mounted close to the input of the module to improve the stability.

Safety Considerations

The power module must be installed in compliance with the spacing and separation requirements of the end-user's safety agency standard, i.e., IEC 62368-1, UL60950-1, CSA C22.2 NO. 60950-1 2nd and IEC 60950-1 2nd: 2005 and EN 60950-1 2nd: 2006+A11+A1: 2010, if the system in which the power module is to be used must meet safety agency requirements.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a fast-acting fuse with 20A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

Soldering and Cleaning Considerations

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.



Over-Current Protection

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will shut down (hiccup mode).

The modules will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

Remote On/Off

The remote On/Off feature on the module can be either negative or positive logic depend on the part number options on the last page. Remote On/Off can be controlled by an external switch between the On/Off terminal and the Vi(-) terminal. The switch can be an open collector or open drain. The maximum allowable leakage current of the switch is 10uA. The switch must be capable of maintaining a low signal Vo/off<0.25V while sinking 1mA.

- For Negative logic version, turns the module on during an external switch is on, it will be off during an external switch is off and floating. If the remote on/off feature is not used, please short the On/Off pin to Vi(-).
- For Positive logic version, turns the modules off during an external switch is on, it will be on during an external switch is off and the on/off pin is floating. If the remote On/Off feature is not used, please leave the On/Off pin to floating.

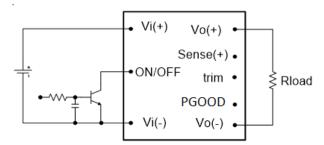


Figure 24: Remote On/Off Implementation

Remote Sense

Remote sense compensates for voltage drops in the power distribution path by sensing the voltage at the load point. The output voltage sense range defines the maximum voltage allowed between the sense and the output power, and it is shown on the electrical data page. If remote sense feature is not used, the sense pin should be connected to the Vo pin.

The output voltage at the Vo(+) can be increased by either remote sense or trim, the maximum voltage increase allowed is the larger of the remote sense range or the output voltage adjustment range; it is not the sum of both. As the output voltage increases with the maximum output current due to the use of remote sense, please ensure the output power of the module does not exceed the maximum rated power.

Power Good

The power module provides an optional open-drain PGOOD signal which indicates if the output voltage is being regulated. When the module is power on, but output voltage is more than +/-5 from the expect voltage set point due to input under voltage, over temperature, over load, or out of control, the power good signal will be pulling low. A 10 k Ω pull-up resistor is recommended to 3.3V source. If the power good feature is not used, this pin should be left open.

Thermal Limit

The modules include an internal thermal shutdown function, which provides protection from thermal damage. If the junction temperature of the controller IC reaches the over-temperature threshold, the module will shut down.

The modules will try to restart after shutdown. If the over-temperature condition still exists, the module will shut down again. The module restarts repeat until the temperature of the device has fallen below the thermal reset level(135°C typ).

Output Voltage Adjustment (TRIM)

To decrease the output voltage set point, connect an external resistor between the TRIM pin and the Vo(-) pin. The TRIM pin should be left open if this feature is not used.

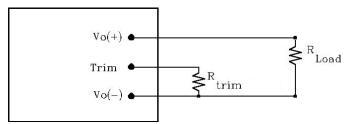


Figure 25: Circuit Configuration for Trim (decrease output voltage)

If the external resistor is connected between the TRIM and Vo(-) pins, the output voltage can be set (Fig.26). To adjust the output voltage, the trim resistor is defined as:

$$Rtrim = \left[\frac{\text{Vref * F}}{\text{Vo } - 2.59} - 0.511 \right] (K\Omega)$$

The values of Vref is 0.6, and F is 36.5.

Ex. When Vo=5V,

$$Rtrim = \left[\frac{0.6 * 36.5}{5 - 2.59} - 0.511 \right] (K\Omega) = 8.57 (K\Omega)$$

Rtrim
30.3K/F
8.57K/F
2.61K/F
1.82K/F
1.25K/F



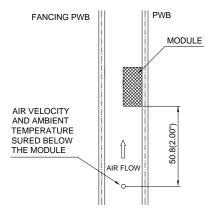
Thermal Testing Setup (Airflow Cooling)

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a 120mmX180mm,70µm (2Oz),4 layers test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 26: Wind Tunnel Test Setup

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.

Thermal Curves (Open Frame)

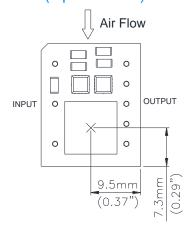


Figure 27: * Hot spot temperature measured point. The allowed maximum hot spot temperature is defined at 120°C.

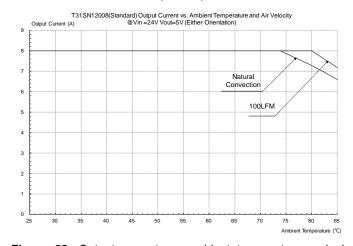


Figure 28: Output current vs. ambient temperature and air velocity @V_{in}=24V, V_{out}=5V (Either Orientation, Open Frame)

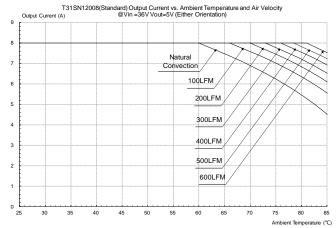


Figure 29: Output current vs. ambient temperature and air velocity @V_{in}=36V, V_{out}=5V (Either Orientation, Open Frame)



Thermal Curves (Open Frame)

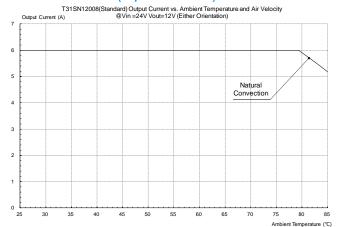


Figure 30: Output current vs. ambient temperature and air velocity @V_{in}=24V, V_{out}=12V (Either Orientation, Open Frame)

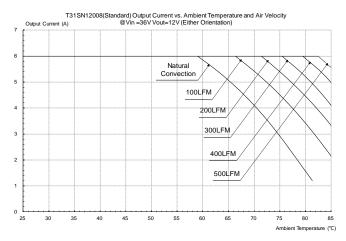


Figure 31: Output current vs. ambient temperature and air velocity @V_{in}=36V, V_{out}=12V (Either Orientation, Open Frame)

Thermal Curves (Potting)

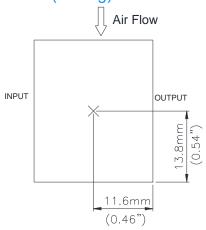


Figure 32: * Hot spot temperature measured point. The allowed maximum hot spot temperature is defined at 115°C.

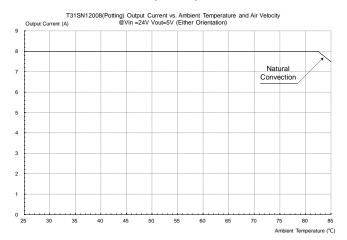


Figure 33: Output current vs. ambient temperature and air velocity @V_{in}=24V, V_{out}=5V (Either Orientation, Potting)

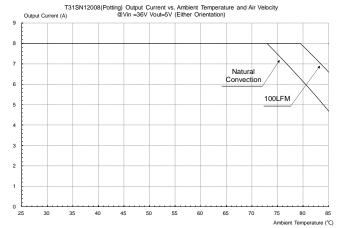


Figure 34: Output current vs. ambient temperature and air velocity $@V_{in}=36V$, $V_{out}=5V$ (Either Orientation, Potting)



Thermal Curves (Potting)

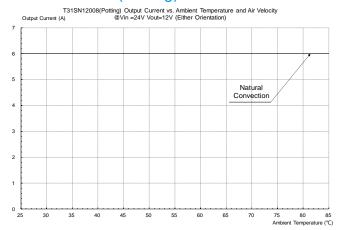


Figure 35: Output current vs. ambient temperature and air velocity $@V_{in}=24V$, $V_{out}=12V$ (Either Orientation, Potting)

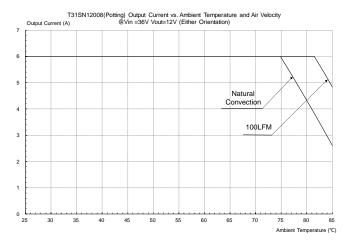


Figure 36: Output current vs. ambient temperature and air velocity @V_{in}=36V, V_{out}=12V (Either Orientation, Potting)



Thermal Testing Setup (Cold Plate Cooling)

The following figure shows cold plate cooling test setup. The power module is mounted on a 120mmX180mm, 70µm (2Oz),4 layers test PWB and attach to a cold plate with thermal interface material (TIM).

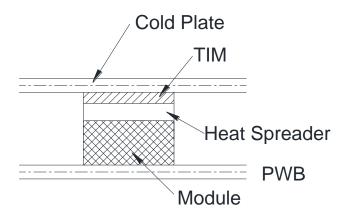


Figure 37: Cold Plate Cooling Test Setup

Thermal Curves (Potting, Attach to Cold Plate)

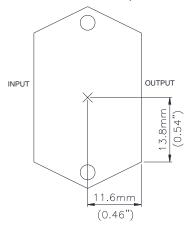


Figure 38: * Hot spot on metal case temperature measured point. The allowed maximum hot spot temperature is defined at 110°C.

Thermal Curves (Potting, Attach to Cold Plate)

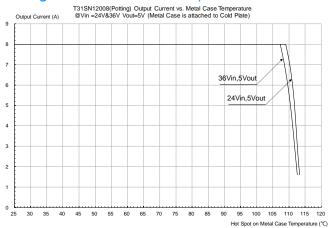


Figure 39: Output Current vs. Hot Spot on Metal Case Temperature @V_{in}=24V&36V, V_{out}=5V (Potting, Attach to Cold Plate)

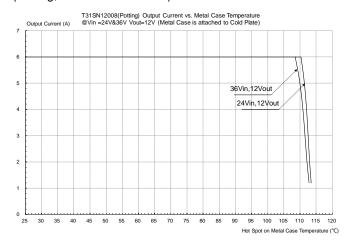


Figure 40: Output Current vs. Hot Spot on Metal Case Temperature @V_{in}=24V&36V, V_{out}=12V (Potting, Attach to Cold Plate)



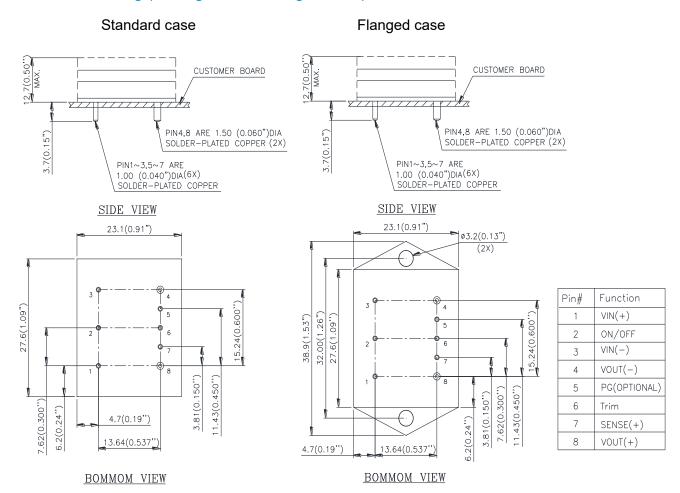
Mechanical Drawing (Open Frame Through Hole and Surface Mount)

Through hole Surface mount 19.1(0.75") 19.1(0.75") 160") 4.06(0.160") 2.66(0.105") 2.66(0.105") 11.43(0.450"] 7.62(0.300") 11.43(0.450" 3.81(0.150") 3.81(0.150") 1.3*45*(0.05''*45*) 7.62(0.300" 1.3*45*(0.05"*45*) 13.64(0.537'') 13.64(0.537'') 15.24(0.600" 23.4(0.92") 23.4(0.92") TOP VIEW TOP VIEW Function Pin# VIN(+) 10.1(0.40") MAX. 9.6(0.38'') MAX. CUSTOMER BOARD 2 ON/OFF VIN(-)3 VOUT(-)CUSTOMER BOARD 5 PG(OPTIONAL) 0.25(0.100") 0.25(0.100") MIN. PIN4,8 ARE 1.50 (0.060")DIA SOLDER-PLATED COPPER (2X) 3.7(0.15") ž 0.120 6 WITH 2.3(0.09")SHOULDER 7 SENSE(+) ø1.57(0.062")DIA(8X) PIN1~3,5~7 ARE 1.00 (0.040")DIA(6X) SOLDER-PLATED COPPER SOLDER-PLATED BRASS 8 VOUT(+)WITH 1.8(0.07")SHOULDER SIDE VIEW SIDE VIEW NOTES: DIMENSIONS ARE IN MILLIMETERS AND (INCHES) TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.) X.XXmm±0.25mm(X.XXX in.±0.010 in.)

Note: All pins are copper alloy with matte Tin (Pb free) plated over Nickel under plating.



Mechanical Drawing (Through Hole Potting Module)



NOTES

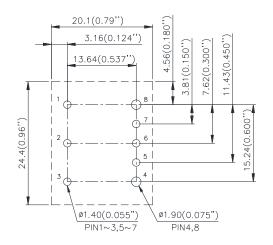
DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

Note: All pins are copper alloy with matte Tin (Pb free) plated over Nickel under plating.



Recommended Pad Layout (Open Frame Through-Hole Module)

RECOMENDED P.W.B. PAD LAYOUT

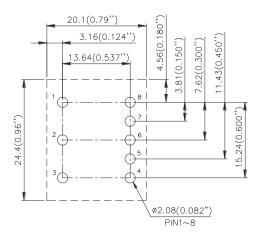


Pin#	Function
1	VIN(+)
2	ON/OFF
3	VIN(-)
4	VOUT(-)
5	PG(OPTIONAL)
6	Trim
7	SENSE(+)
8	VOUT(+)

NOTES:

Recommended Pad Layout (Open Frame Surface Mount Module)

RECOMENDED P.W.B. PAD LAYOUT



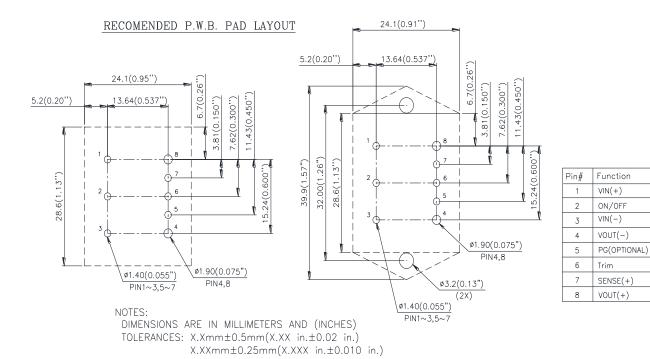
Pin#	Function
1	VIN(+)
2	ON/OFF
3	VIN(-)
4	VOUT(-)
5	PG(OPTIONAL)
6	Trim
7	SENSE(+)
8	VOUT(+)

NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES) TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.) X.XXmm±0.25mm(X.XXX in.±0.010 in.)



Recommended Pad Layout (Through Hole Potting Module)





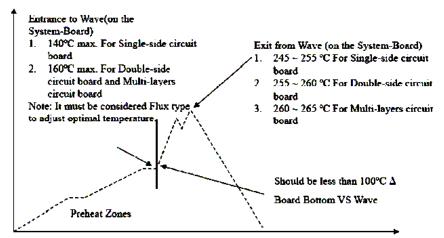
Soldering Method

Generally, as the most common mass soldering method for the solder attachment, wave soldering is used for through-hole power modules and reflow soldering is used for surface-mount ones. Delta recommended soldering methods and process parameters are provided in this document for solder attachment of power modules onto system board. SAC305 is the suggested lead-free solder alloy for all soldering methods. The soldering temperature profile presented in this document is based on SAC305 solder alloy.

Reflow soldering is not a suggested method for through-hole power modules due to many process and reliability concerns, and reflow is prohibited for potting model.

Wave Soldering (Lead-free)

Delta's power modules are designed to be compatible with single-wave or dual wave soldering. The suggested soldering process must keep the power module's internal temperature below the critical temperature of 217C continuously. The recommended wave-soldering profile is shown below:



Note: The temperature is measured on solder joint of pins of power module.

The typical recommended (for double-side circuit board) preheat temperature is 115+/-10°C on the top side (component side) of the circuit board. The circuit-board bottom-side preheat temperature is typically recommended to be greater than 135°C and preferably within 100°C of the solder-wave temperature. A maximum recommended preheat up rate is 3C/s. A maximum recommended solder pot temperature is 255+/-5°C with solder-wave dwell time of 3~6 seconds. The cooling down rate is typically recommended to be 6°C/s maximum.

Hand Soldering (Lead Free)

Hand soldering is the least preferred method because the amount of solder applied, the time the soldering iron is held on the joint, the temperature of the iron, and the temperature of the solder joint are variable. The recommended hand soldering guideline is listed in Table below. The suggested soldering process must keep the power module's internal temperature below the critical temperature of 217C continuously.

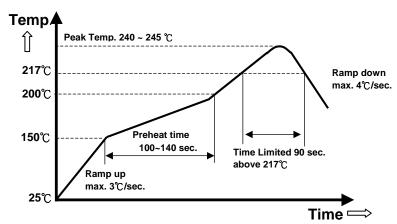
Parameter	Single-side	Double-side	Multi-layers
	Circuit Board	Circuit Board	Circuit Board
Soldering Iron Wattage	90	90	90
Tip Temperature	385+/-10°C	420+/-10℃	420+/-10℃
Soldering Time	$2 \sim 6$ seconds	$4 \sim 10$ seconds	$4\sim 10 \ seconds$



Reflow Soldering (Lead-free)

High temperature and long soldering time will result in IMC layer increasing in thickness and thereby shorten the solder joint lifetime. Therefore the peak temperature over 245°C is not suggested due to the potential reliability risk of components under continuous high-temperature. In the meanwhile, the soldering time of temperature above 217 °C should be less than 90 seconds. Please refer to below figure for recommended temperature profile parameters.

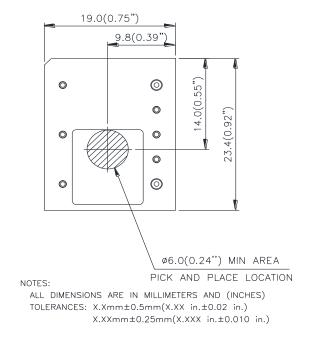
Shielding cap is requested to mount on DCDC module if with heat-spreader/heat-sink, to prevent the customer side high temperature of reflow to re-melt the DCDC module's internal component's soldering joint.



Note: The temperature is measured on solder joint of pins of power module.



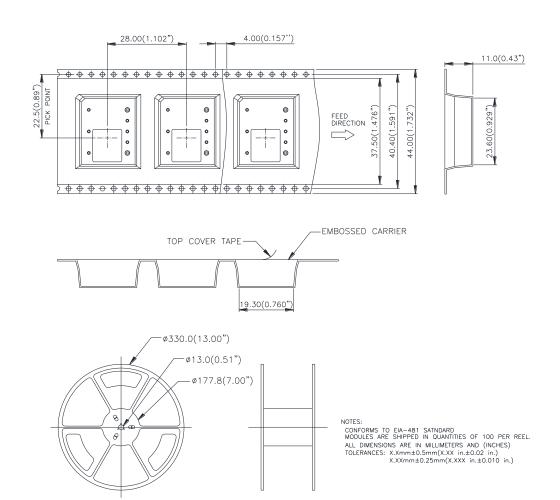
Pick and Place Location (For Open Frame Surface Mount Only)



Surface-Mount Tape & Reel (For Open Frame Surface Mount Only)



MANUFACTURE CONSIDERATIONS





PART NUMBERING SYSTEM												
Т	31	S	N	120	08	N	N	F			Α	
Form Factor	Input Voltage	Number Of Outputs	Product Series	Output Voltage	Max Output Current	ON/OFF Logic	Pin Length	RoHS			Option Code	
T - 1/32 Brick	31 - 9V~53V	S - Single	N - Series Number	120- 3.3V~16.5	08 - 8A	P - Positive	N - 0.145" M - SMD	F - RoHS 6/6 (Lead Free)		Power Good	Standard Case (Potting)	Flanged Case (Potting)
									Α	No	No	No
									В	Yes	No	No
						N – Negative			С	Yes	Yes	No
									D	No	Yes	No
									E	Yes	No	Yes
									F	No	No	Yes

RECOMMENDED PART NUMBER							
Model Name	Input	Out	put	Eff. @ 100% Load			
T31SN12008NNFA	9V~53V	3.3~16.5V	8A	96.5% @ 24Vin/12Vo			
T31SN12008NMFA	9V~53V	3.3~16.5V	8A	96.5% @ 24Vin/12Vo			
T31SN12008NNFC	9V~53V	3.3~16.5V	8A	96.5% @ 24Vin/12Vo			

Default remote On/Off logic is negative and pin length is 0.145"

For different remote On/Off logic and pin length, please refer to part numbering system above or contact your local sales office. For modules with through-hole pins and the optional heat-spreader, they are intended for wave soldering assembly onto system boards; please do not subject such modules through reflow temperature profile.

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WARRANTY

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

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