

## 1/6W, 0402, Low Resistance Chip Resistor (Lead / Halogen Free)

### 1. Scope

This specification applies to1.0mm x 0.5mm size 1/6W, fixed thick film low resistance value chip resistors rectangular type.

### 2. Type Designation

RLT0510 - <u>7</u> - <u>□</u> <u>□</u>

(1) (2) (3) (4)

Where (1) Size No.

(2) Power Rating:

7 = 1/6W

(3) Resistance value:

For example --

 $R075 = 0.075\Omega$ 

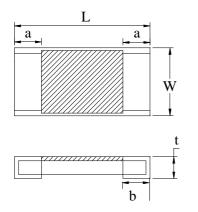
 $R100 = 0.100\Omega$ 

The "R" shall be used as a decimal point

(4) Resistance tolerance:

 $F = \pm 1.0\%, G = \pm 2\%, J = \pm 5\%$ 

3. Outline Dimensions



Code Letter	Dimension
L	$1.00\pm0.10$
W	$0.50\pm0.10$
t	0.35 +0.15/-0.10
a	$0.25\pm0.10$
b	$0.30\pm0.10$

Unit : mm

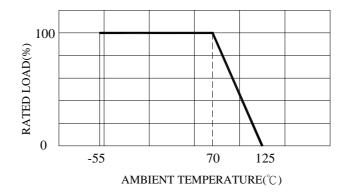


### 4. Ratings

Power Rating*	1/6	1/6W	
Resistance Tolerance	1%(F), 2%(	1%(F), 2%(G), 5%(J)	
Resistance Range	$0.065 \sim < 0.60 \Omega$	0.60 ~ 1.0 Ω	
Temperature Coefficient of Resistance(ppm/°C)	±300	±200	
Operating Temperature Range	-55°C to	-55℃ to 125℃	

Note\*:

Power Rating is based on continuous full load operation at rated ambient temperature of 70  $^{\circ}$ C. For resistor operated at ambient temperature in excess of 70  $^{\circ}$ C, the maximum load shall be derated in accordance with the following curve.



### 4-2 Rated Voltage

The d.c. or a.c. r.m.s. voltage shall be calculated from the following expression

Р

 $V = \sqrt{P \times R}$ 

Where V : Rated voltage (V)

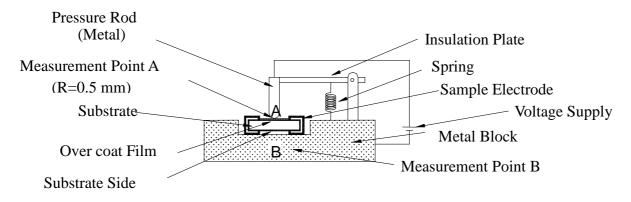
- : Rated power (W)
- R : Nominal resistance  $(\Omega)$
- 4-3 Operating and Storage Temperature Range -55 to +125 °C

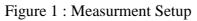


### 5. Characteristics

5-1 Electrical

Item	Specification and Requirement	Test Method (JIS 5201)
Temperature Coefficient	As follow table 1.	Room temperature
of Resistance (TCR)		Room temperature+100°C
Short Time Overload	∧ R:±1.0%	(1) Applied voltage: 2.5 x rated
	Without damage by flashover, spark,	voltage
	arcing, burning or breakdown	(2) Test time: 5 seconds
	Over 100 M $\Omega$ on Overcoat layer	(1) Setup as figure 1
	face up	(2) Test voltage: $100V_{DC} \pm 15V_{DC}$
	Over 1,000 M $\Omega$ on Substrate side	(3) Test time: $60 + 10 / - 0$ seconds
	face up	
	Resistance range:±1.0%	(1) Setup as figure 1
	Without damage by flashover, spark,	(2) Test voltage: $100V_{AC}(rms.)$
	arcing, burning or breakdown	(3) Test time: $60 + 10 / - 0$ seconds







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#### 5-2 Mechanical Specification and Requirement Test Method (JIS 5201) Item Solderability The surface of terminal immersed Solder bath: After immersing in flux, dip in shall be minimum of 95% covered with a new coating of solder $245 \pm 5^{\circ}$ C molten solder bath for $2 \pm$ 0.5 seconds (1) Pre-heat: 100~110°C Resistance to Solder $\triangle$ R: ± 1.0% Without distinct deformation in Heat for 30 seconds appearance (2) Immersed at solder bath of $270 \pm 5^{\circ}$ C for $10 \pm 1$ seconds (3) Measuring resistance 1 hour after test Bending value: 3 mm for $30 \pm 1$ Bending Test $\triangle$ R: ± 1.0% Without mechanical damage such as seconds break



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# SPECIFICATION FOR APPROVAL

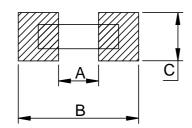
Item	Specification and Requirement	Test Method (JIS 5201)
Rapid Change of Temperature	△ R:±1.0% Without distinct damage in appearance	<ul> <li>(1) Repeat 5 cycle as follow: (-55 ± 3°C, 30minutes) →(Room temperature, 2~3 minutes) →(+125 ± 2°C, 30minutes) →(Room temperature 2~3 minutes)</li> <li>(2) Measuring resistance 1 hour after test</li> </ul>
Moisture with Load	$\triangle$ R: ±5.0% Without distinct damage in appearance	<ul> <li>(1) Environment condition: 40 ± 2°C,90~95% RH</li> <li>(2) Applied Voltage: rated voltage</li> <li>(3) Test period: (1.5 hour ON) →(0.5 hour OFF) cycled for total 1,000 + 48 / - 0 hours</li> <li>(4) Measuring resistance 1 hour after test</li> </ul>
Load Life	<ul> <li>△ R: ±5.0%</li> <li>Without distinct damage in appearance</li> </ul>	<ul> <li>(1) Test temperature: 70 ± 3°C</li> <li>(2) Applied Voltage: rated voltage</li> <li>(3) Test period: (1.5 hour ON)</li> <li>→(0.5 hour OFF) cycled for total</li> <li>1,000 + 48 / - 0 hours</li> <li>(4) Measuring resistance</li> <li>1 hour after test</li> </ul>
Low Temperature Store	$\triangle$ R: ± 5.0% Without distinct damage in appearance	<ul> <li>(1) Store temperature: -55 ± 3°C for total 1,000 + 48 / - 0 hours</li> <li>(2) Measuring resistance 1 hour after test</li> </ul>
High Temperature Store	$\triangle$ R: ± 5.0% Without distinct damage in appearance	<ul> <li>(1) Store temperature: +125 ± 2°C for total 1,000 + 48 / - 0 hours</li> <li>(2) Measuring resistance 1 hour after test</li> </ul>

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### 6. Recommend Land Pattern Dimensions



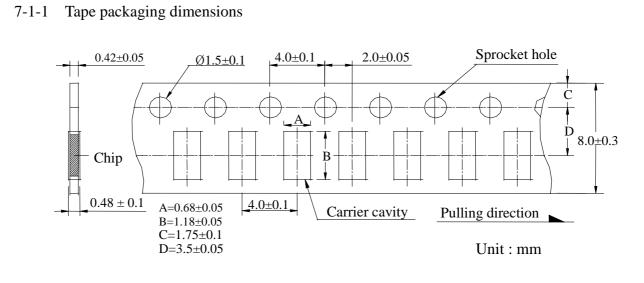
А	0.6~1.0
В	2.0~2.4
С	0.6~1.0
	11.1

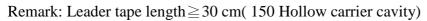




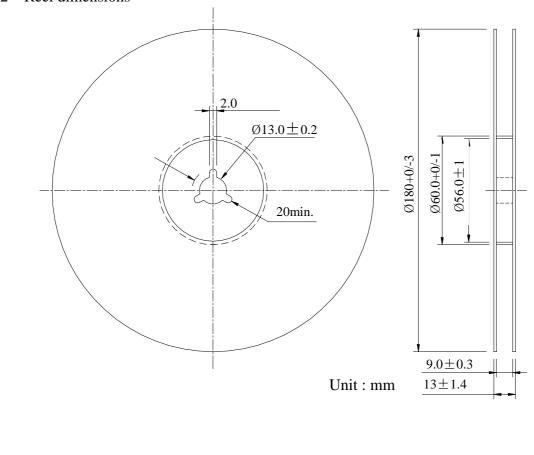
### 7. Packaging

7-1 Dimensions





7-1-2 Reel dimensions



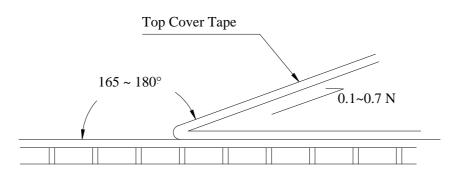
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### 7-2 Peel force of top cover tape

The peel speed shall be about 300 mm/min.

The peel force of top cover tape shall be between 0.1 to 0.7 N.



7-3 Numbers of taping 10,000 pieces /reel

### 7-4 Label making

The following items shall be marked on the reel.

- (1) Type designation.
- (2) Quantity
- (3) Manufacturing date code
- (4) Manufacturer's name



### 8. Carenote

- 8-1 Care note for storage
  - Chip resistor shall be stored in a room where temperature and humidity must be controlled. (temperature 5 to 35°C, humidity 45 to 85% RH) However, a humidity keep it low, as it is possible.
  - (2) Chip resistor shall be stored as direct sunshine doesn't hit on it.
  - (3) Chip resistor shall be stored with no moisture, dust, a material that will make solderability inferior, and a harmful gas (Chloridation hydrogen, sulfurous acid gas, and sulfuration hydrogen)
- 8-2 Carenote for operating and handling
  - (1) It is necessary to protect the edge and protection coat of resistors from mechanical stress.
  - (2) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
  - (3) Resistors shall be used with in rated range shown in specification. Especially, if voltage more than specified value will be loaded to resistor, there is a case it will make damage for machine because of temperature rise depending on generating of heat, and increase resistance value or breaks.
  - (4) In case that resistor is loaded a rated voltage, it is necessary to confirms temperature of a resistor and to reduce a load power according to load reduction curve, because a temperature rise of a resistor depends on influence of heat from mounting density and neighboring element.
  - (5) Observe Limiting element voltage and maximum overload voltage specified in each specification
  - (6) If there is possibility that a large voltage (pulse voltage, shock voltage) charge to resistor, it is necessary that operating condition shall be set up before use.