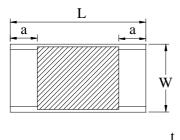
# 1/4W, 0603, Low Resistance Chip Resistor (Lead / Halogen Free)

1. Scope

This specification applies to1.6mm x 0.8mm size 1/4W, fixed thick film low resistance value chip resistors rectangular type.

- 2. Type Designation
  - RLT0816 3 (1)(2) (3) (4) Where (1) Size No. (2) Power Rating: 3 = 1/4W(3) Resistance value: Refer to paragraph 4-1 For example --Four digits of number  $R100 = 0.1\Omega$  $1R00 = 1.0\Omega$ The "R" shall be used as a decimal point (4) Resistance tolerance:
    - $F = \pm 1\%, G = \pm 2\%, J = \pm 5\%$
- 3. Outline Dimensions



		t
	b	

Code Letter	Dimension
L	$1.60\pm0.15$
W	$0.80\pm0.15$
t	$0.45\pm0.10$
a	$0.30\pm0.20$
b	$0.30\pm0.20$

Unit : mm

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TITLE : The Engineering Specification for RLT0816 1/4W Low Resistance Chip Resistor	NO.	SKK550000INH	A0	

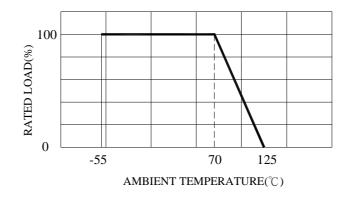
Ratings		
4-1 Specification		
Table 1		
Power Rating*	1/4	4W
Resistance Tolerance	1%(F), 2%	o(G), 5%(J)
Resistance Range	$0.05\Omega \sim < 0.1\Omega$	$0.1\Omega \sim < 10\Omega$
Temperature Coefficient of Resistance(ppm/°C)	±300	±200

Note\*:

Power Rating is based on continuous full load operation at rated ambient temperature of 70°C.

For resistor operated at ambient temperature in excess of 70°C, the maximum load

shall be derated in accordance with the following curve.



### 4-2 Rated Voltage

The d.c. or a.c. r.m.s. voltage shall be calculated from the following expression

 $V = \sqrt{P \times R}$ 

Where V : Rated voltage (V)

- Р : Rated power (W)
- R : Nominal resistance  $(\Omega)$

## 4-3 Operating and Storage Temperature Range

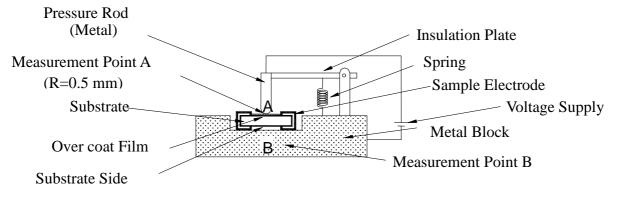
-55 to +125°C

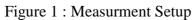
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### 5. Characteristics

#### 5-1 Electrical

Item	Specification and Requirement	Test Method (JIS 5201)
Temperature Coefficient	As follow table 1.	Room temperature
of Resistance (TCR)		Room temperature+100°C
Short Time Overload	△ R:±1.0%	(1) Applied voltage: 2.5 x rated
	Without damage by flashover, spark,	voltage
	arcing, burning or breakdown	(2) Test time: 5 seconds
Insulation Resistance	Over 100 M $\Omega$ on Overcoat layer	(1) Setup as figure 1
	face up	(2) Test voltage: $100V_{DC} \pm 15V_{DC}$
	Over 1,000 M $\Omega$ on Substrate side	(3) Test time: $60 + 10 / - 0$ seconds
	face up	
Voltage Proof	Resistance range:±1.0%	(1) Setup as figure 1
	Without damage by flashover, spark,	(2) Test voltage: $100V_{AC}$ (rms.)
	arcing, burning or breakdown	(3) Test time: $60 + 10 / - 0$ seconds



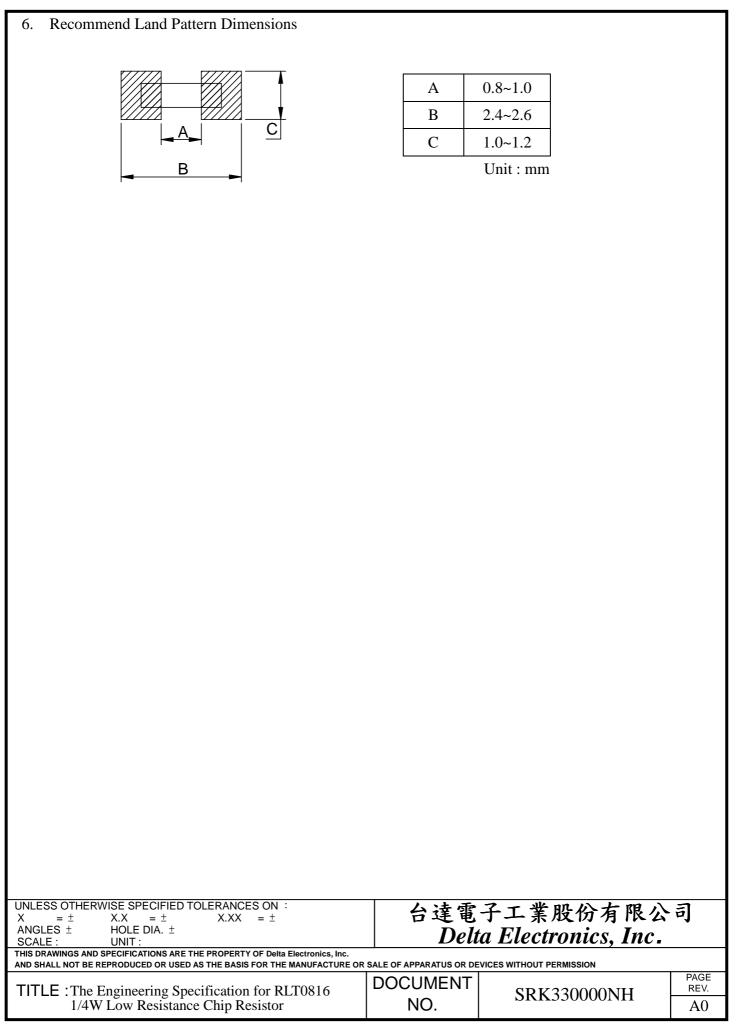


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Item	Specification and Requirement	Test Method (JIS 5201)
Solderability	The surface of terminal immersed shall be minimum of 95% covered with a new coating of solder	Solder bath: After immersing in flux, dip in 245 ± 5°C molten solder bath for 2 ± 0.5 seconds
Resistance to Solder Heat	$\triangle$ R: ± 1.0% Without distinct deformation in appearance	<ol> <li>Pre-heat: 100~110°C for 30 seconds</li> <li>Immersed at solder bath of 270 ± 5°C for 10 ± 1 seconds</li> <li>Measuring resistance 1 hour after test</li> </ol>
Bending Test		Bending value: 3 mm for $30 \pm 1$ seconds
Solvent Resistance	Without mechanical and distinct damage in appearance	<ol> <li>(1) Solvent: Trichloroethane or Isopropyl alcohol</li> <li>(2) Immersed in solvent at room temperature for 300 seconds</li> </ol>

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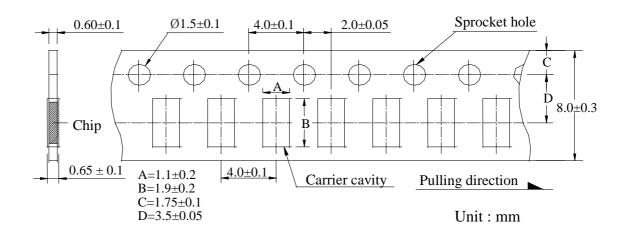
Item	Specification and Require	nent	Test Method (JIS 5201)
Rapid Change of Temperature		(( 	Repeat 5 cycle as follow: $(-55 \pm 3^{\circ}C, 30 \text{minutes})$ $\rightarrow$ (Room temperature, 2~3 minutes) $\rightarrow$ (+125 $\pm$ 2°C, 30 minutes) $\rightarrow$ (Room temperature 2~3 minutes) Measuring resistance hour after test
Moisture with Load	$\triangle$ R: ±5.0% Without distinct damage in appearance	(1) E 4 (2) A (3) T (4) N	Environment condition: $40 \pm 2^{\circ}C$ ,90~95% RH Applied Voltage: rated voltage Test period: (1.5 hour ON) $\rightarrow$ (0.5 hour OFF) cycled for total 1,000 + 48 / - 0 hours Measuring resistance hour after test
Load Life		(1) T (2) A (3) T (4) N	Test temperature: $70 \pm 3^{\circ}$ C Applied Voltage: rated voltage Fest period: (1.5 hour ON) $\rightarrow$ (0.5 hour OFF) cycled for total 1,000 + 48 / - 0 hours Measuring resistance hour after test
Low Temperature Store	$\triangle$ R: ± 5.0% Without distinct damage in appearance	(1) S 	Store temperature: $55 \pm 3^{\circ}$ C for total 1,000 + 48 / - 0 hours Measuring resistance 1 hour after test
High Temperature Store	$\triangle$ R: ± 5.0% Without distinct damage in appearance	+ 1 (2) M	Store temperature: +125 ± 2°C for total 1,000 + 48 / - 0 hours Measuring resistance 1 hour after test
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#### 7. Packaging

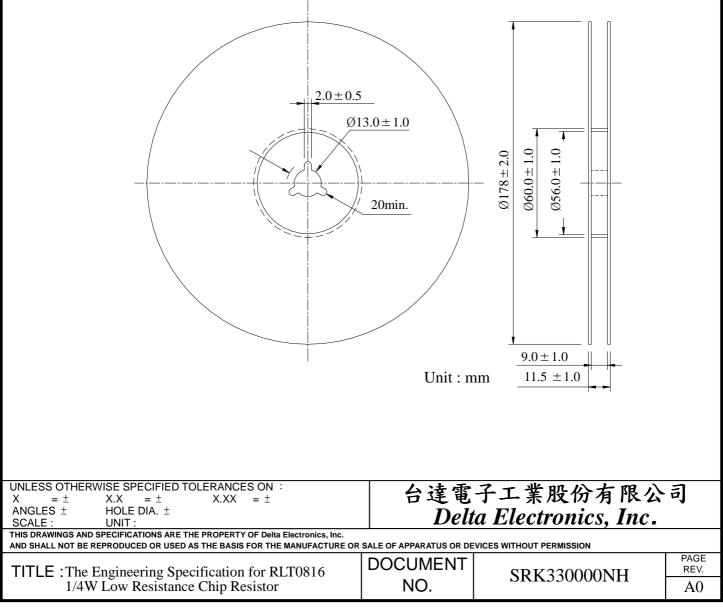
### 7-1 Dimensions

7-1-1 Tape packaging dimensions



Remark: Leader tape length  $\geq$  30 cm(150 Hollow carrier cavity)

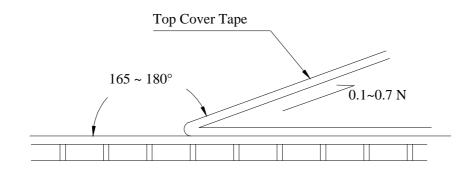
7-1-2 Reel dimensions



### 7-2 Peel force of top cover tape

The peel speed shall be about 300 mm/min.

The peel force of top cover tape shall be between 0.1 to 0.7 N.



7-3 Numbers of taping 5,000 pieces /reel

#### 7-4 Label making

The following items shall be marked on the reel.

- (1) Type designation.
- (2) Quantity
- (3) Manufacturing date code
- (4) Manufacturer's name

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#### 8. Carenote

- 8-1 Care note for storage
  - (1) Chip resistor shall be stored in a room where temperature and humidity must be controlled. (temperature 5 to 35°C, humidity 45 to 85% RH) However, a humidity keep it low, as it is possible.
  - (2) Chip resistor shall be stored as direct sunshine doesn't hit on it.
  - (3) Chip resistor shall be stored with no moisture, dust, a material that will make solderability inferior, and a harmful gas (Chloridation hydrogen, sulfurous acid gas, and sulfuration hydrogen)
- 8-2 Carenote for operating and handling
  - (1) It is necessary to protect the edge and protection coat of resistors from mechanical stress.
  - (2) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
  - (3) Resistors shall be used with in rated range shown in specification. Especially, if voltage more than specified value will be loaded to resistor, there is a case it will make damage for machine because of temperature rise depending on generating of heat, and increase resistance value or breaks.
  - (4) In case that resistor is loaded a rated voltage, it is necessary to confirms temperature of a resistor and to reduce a load power according to load reduction curve, because a temperature rise of a resistor depends on influence of heat from mounting density and neighboring element.
  - (5) Observe Limiting element voltage and maximum overload voltage specified in each specification
  - (6) If there is possibility that a large voltage (pulse voltage, shock voltage) charge to resistor, it is necessary that operating condition shall be set up before use.

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