## ndustrial Automation Headquarters

Taiwan: Delta Electronics, Inc.
No.18, Xinglong Rd., Taoyuan District,
TEL: + 886-3-362-6301 / FAX: +886-3-371-630

## Asia

China: Delta Electronics (Shanghai) Co., Ltd.
No. 182 Minyu Rd., Pudong Shanghai, P.R.
Post code : 201209
TEL Code +86 : $201-6872$-3988 / FAX: $+86-21-6872-3996$ EL: + 86-21-6872-3988-20-595
Japan: Delta Electronics (Japan), In
dustrial Automation Sales Department
Tokyo, Japan 105-0012
TEL: + +81-3-5733-1155 / FAX: +81-3-5733-1255
Korea: Delta Electronics (Korea), Inc.
1511, 219, Gasan Digital 1-Ro., Geumcheon-gu,
Seoul, 08501 South Korea
Singapore: Delta Energy Systems (Singapore) Pte Ltd Kaki Bukit Avenue 1, \#05-04, Singapore 417939 FEL: +65-6747-5155/ /FAX: +65-6744-9228
India: Delta Electronics (India) Pvt. Ltd. Plot No. 43, Sector 35 , HSIIDC Gurgaon
TIN 122001, Haryana, India TEL: +91-124-4874900 / FAX: +91-124-4874945
Thailand: Delta Electronics (Thailand) PC 909 Soi 9, Moo 4, Bangpoo Industrial Estate (E.P.Z), Pattana 1 Rd., T.Phraksa, A.Muang
Samutprakarn 10280, Thailand
TEL: $+66-2709-2800$ / FAX: $+66-2709-2827$
Australia: Delta Electronics (Australia) Pty Ltd Unit 2, Builiaing A, 18 -24 Ricketts Road,
Mount Waverley, Victoria
TEL: +61-1300-335-823/+61-3-9543-3720

## Americas

USA: Delta Electronics (Americas) Ltd. 5101 Davis Drive, Research Triangle Park, NC 27
TEL: +1-919-767-3813 / FAX: +1-919-767-3969

Brazil: Delta Electronics Brazil Ltd
strada Velha Rio-São Paulo, 5300 Eugênio de Melo - São José dos Campos CEP: 12247-004 TEL: +55-12-3932-2300 / FAX: +55-12-3932-237

## EMEA

EMEA Headquarters: Delta Electronics (Netherlands) B.V
Sales: Sales.IA.EMEA@deltaww.com
echnical Support: iatechnicalsupport@deltaww.com
Customer Support: Customer-Support@deltaww.com Service: Service.IA.emea@deltaww.com EL: +31(0)40 8003900
BENELUX: Delta Electronics (Netherlands) B.
Automotive Campus 260, 5708 JZ Helmona, The Netherlands
Mail: Sales. IA. Benelux@deltaww.com LE: +31(0)40 8003900
ACH: Delta Electronics (Netherlands) B.V. oesterweg 45, D-59494 Soest, Germany TEL: +49(0)29219870
France: Delta Electronics (France) S.A.
au bois Challand 2,15 rue des Pyrénée Lisses, 91090 Evry Cedex, France
Mail: Sales.IA.FR@deltaww.co
eria: Delta Electronics Solutions (Spain) S.L.
Ctra. De Viliaverde a Vallecas, $2651^{\circ}$ Dcha Ed. TEL: $+34(0) 912237420$
Carrer Llacuna 166, 08018 Barcelona, Spain
al. Sales.IA..1beria@deltaww.con
Ity: Delta Electronics (Italy) S.r.
lia Meda 2-22060 Novedrate(CO)
azza Grazioli 1800186 Roma Italy
Mail: Sales.IA. Italy@delta
TEL: +390398900365
ussia: Delta Energy System LLC
reyskaya Plaza II, office 112 Vereyskaya str.
121357 Moscow Russia
EL: +7 4956443240
Turkey: Delta Greentech Elektronik San. Ltd. Sti. (Turkey)
Serifali Mah. Hendem Cad. Kule Sok. No:16-A
34775 Ümraniye - istanbul
Mail: Sales.IA. Turkey@d
TEL: +902164999910
MEA: Eltek Dubai (EItek MEA DMCC)
FFICE 2504, 25th Floor, Saba Tower
Jumeirah Lakes Towers, Dubai, UA
EL: Sales.IA.MEA@deltaww.com
ELL: $+971(0) 42690148$

## Delta Telescopic Belt Conveyor

 Integrated Drive LTC Series User Manual

## Copyright Notice

©Delta Electronics, Inc. All rights reserved.
All information contained in this user manual is the exclusive property of Delta Electronics Inc. (hereinafter referred to as "Delta ") and is protected by copyright law and all other laws. Delta retains the exclusive rights of this user manual in accordance with the copyright law and all other laws. No parts in this manual may be reproduced, transmitted, transcribed, translated or used in any other ways without the prior consent of Delta.

## Limitation of Liability

The contents of this user manual are only for the use of the product manufactured by Delta. Except as defined in special mandatory laws, Delta provides this user manual "as is" and does not offer any kind of warranty through this user manual for using the product, either express or implied, including but not limited to the following: (i) this product will meet your needs or expectations; (ii) the information contained in the product is current and correct; (iii) the product does not infringe any rights of any other person. You shall bear your own risk to use this product.

In no event shall Delta, its subsidiaries, affiliates, managers, employees, agents, partners and licensors be liable for any direct, indirect, incidental, special, derivative or consequential damages (including but not limited to the damages for loss of profits, goodwill, use or other intangible losses) unless the laws contains special mandatory provisions to the contrary.

Delta reserves the right to make changes to the user manual and the products described in the user manual without prior notice and afterwards.
(Translation of the original instructions)
$\square$ Disconnect AC input power before connecting any wiring to the controller.
$\boxtimes$ Even if the power has been turned off, a charge may still remain in the DC-link capacitors with hazardous voltages before the POWER LED is OFF. Do NOT touch the internal circuits and components.
$\boxtimes$ There are highly sensitive MOS components on the printed circuit boards. These components are especially sensitive to static electricity. Take anti-static measure before touching these components or the circuit boards.
$\square$ Never modify the internal components or wiring.
$\square$ Ground the controller by using the ground terminal. The grounding method must comply with the laws of the country where the controller is to be installed.
$\square$ Do NOT install the controller in a location with high temperature, direct sunlight or inflammable materials or gases.
$\boxtimes$ Never connect the controller output terminals U1, V1, W1 or U2, V2, W2 directly to the AC mains circuit power supply.

च After finishing the wiring of the controller, check if U1, V1, W1; U2, V2, W2 are shortcircuited to PE with a multimeter. Do NOT power the controller if short circuits occur. Eliminate the short circuits before the controller is powered.
$\square$ The rated voltage range for the controller must be from 342 V to 528 V for 460 V models.
च Refer to the table below for short circuit rating:

| Model (Power) | Short circuit rating |
| :---: | :---: |
| 460 V | 5 kA |

$\square$ Even if the three-phase AC motor is stopped, a charge with hazardous voltages may still remain in the main circuit terminals of the controller.
$\square$ The performance of electrolytic capacitor will degrade if it is not charged for a long time. It is recommended to charge the controller which is stored in no charge condition every 2 years for 3-4 hours to restore the performance of electrolytic capacitor in the controller. Note: When power up the controller by using connecting AC power to R/S/T terminals, use 220-240 $V_{A C}$ of single-phase power between $R$ and $S$ terminals to charge the controller for 30 minutes (do not run the controller). Then charge the controller at 380-480 $\mathrm{V}_{\mathrm{Ac}}$ rated voltage of three-phase power between R, S, and T terminals for one hour (do not run the controller). By doing these, restore the performance of electrolytic capacitor before starting to run the controller. Do NOT run the controller at $100 \%$ rated voltage right away.
$\square$ If the controller generates leakage current over AC 3.5 mA or over DC 10 mA on a grounding conductor, compliance with local grounding regulations or IEC61800-5-1 standard is the minimum requirement for grounding.
$\square$ LTC controller is designed for applications in normal industrial environments. If it is used in a public low-voltage power supply system such as housing buildings, install proper suppression devices like isolating transformer or input reactor because nonlinear load may cause harmonic current. This helps suppress interference caused by harmonic current from public low-voltage power supply system. For more information, please contact Delta.

## NOTE:

The content of this manual may be revised without prior notice. Please consult our distributors or download the latest version at http://www.deltaww.com/iadownload acmotordrive

## Table of Contents

CHAPTER 1 INTRODUCTION ..... 1-1
1-1 Nameplate Information ..... 1-2
1-2 Model Name ..... 1-2
1-3 Serial Number ..... 1-3
1-4 Apply After Service by Mobile Device ..... 1-4
1-5 RFI Jumper ..... 1-5
1-6 Dimensions ..... 1-8
CHAPTER 2 INSTALLATION ..... 2-1
2-1 Mounting Clearance ..... 2-2
2-2 Airflow and Power Dissipation ..... 2-3
2-3 Packaging Method ..... 2-4
CHAPTER 3 WIRING ..... 3-1
3-1 System Wiring Diagram ..... 3-3
3-2 Wiring ..... 3-4
CHAPTER 4 MAIN CIRCUIT TERMINALS ..... 4-1
4-1 Main Circuit Diagram. ..... 4-2
4-2 Main Circuit Terminal Specifications ..... 4-3
CHAPTER 5 CONTROL TERMINALS ..... 5-1
5-1 Slide Cover and Internal Devices ..... 5-2
5-2 Control Terminal Specifications ..... 5-3
5-3 Removing a Control Terminal Block. ..... 5-7
5-4 LED Indicators on Control Terminals ..... 5-8
CHPATER 6 OPTIONAL ACCESSORIES ..... 6-1
6-1 Digital Keypad PU08/PU08V. ..... 6-2
6-2 Circuit Breaker and Fuse ..... 6-4
6-3 Reactors (AC and Zero Phase) ..... 6-5
6-4 EMC Filter ..... 6-9
CHAPTER 7 SPECIFICATIONS ..... 7-1
7-1 460V Models ..... 7-2
7-2 Environment for Operation, Storage and Transportation ..... 7-5
7-3 Specification for Operation Temperature and Protection Level ..... 7-6
7-4 Derating Curve. ..... 7-7
CHAPTER 8 DESCRIPTIONS OF DRIVE AND PLC OPERATIONS ..... 8-1
8-1 An Overview of Functions ..... 8-2
8-2 Descriptions of Digital Keypad PU08/PU08V ..... 8-5
8-3 Function of Digital Keypad PU08/PU08V ..... 8-8
8-4 Fault Code Description of Digital Keypad PU08/PU08V ..... 8-9
8-5 VFDSoft Software ..... 8-16
8-6 WPLSoft Software ..... 8-17
CHAPTER 9 SUMMARY OF PARAMETER SETTINGS ..... 9-1
CHAPTER 10 DESCRIPTIONS OF PARAMETER SETTINGS ..... 10-00-1
00 User Parameters ..... 10-00-1
01 Basic Parameters ..... 10-01-1
02 Operation Method Parameters ..... 10-02-1
03 Output Function Parameters ..... 10-03-1
04 Input Function Parameters ..... 10-04-1
05 Multi-step Speed Parameters ..... 10-05-1
06 Protection Function Parameters ..... 10-06-1
07 Motor Parameters ..... 10-07-1
08 Special Parameters ..... 10-08-1
09 Communication Parameters ..... 10-09-1
10 Speed Feedback Control Parameters ..... 10-10-1
CHAPTER 11 TROUBLESHOOTING ..... 11-1
11-1 Over-current (oc) ..... 11-2
11-2 Over-voltage (ov) ..... 11-3
11-3 Low voltage (Lv) ..... 11-4
11-4 Overheat (oH1) ..... 11-5
11-5 Overload (oL). ..... 11-6
11-6 Keypad Display is Abnormal ..... 11-7
11-7 Phase Loss (PHL) ..... 11-8
11-8 Motor Does Not Run ..... 11-9
11-9 Motor Speed Cannot be Changed ..... 11-10
11-10 Motor Stalls During Acceleration. ..... 11-11
11-11 Motor is Abnormal. ..... 11-12
11-12 Electromagnetic / Induction Noise ..... 11-13
11-13 Operating Environment Condition ..... 11-14
11-14 Affecting Other Machines ..... 11-15
11-15 Indicator Description. ..... 11-16
CHAPTER 12 FAULT CODES AND MAINTENANCE ..... 12-1
12-1 Faults and Corrective Action ..... 12-3
12-2 Fault Codes and Descriptions ..... 12-4
12-3 Maintenance and Inspections. ..... 12-11
CHAPTER 13 PLC FUNCTION APPLICATIONS. ..... 13-1
13-1 PLC Summary ..... 13-2
13-2 Notes Before Using PLC ..... 13-3
13-3 Start-up ..... 13-5
13-4 Basic Principles of PLC Ladder Diagrams. ..... 13-16
13-5 Various PLC Device Functions ..... 13-28
13-6 Introduction to the Command Window ..... 13-43
13-7 Fault Display and Treatment ..... 13-123
APPENDIX A. REVISION HISTORY ..... A-1
Issued Edition: 00
Drive Firmware Version: V1.01 (Refer to Parameter 00-06 on the product to get the firmware version) Issued Date: November, 2022

## Chapter 1 Introduction

1-1 Nameplate Information
1-2 Model Name
1-3 Serial Number
1-4 Apply After Service by Mobile Device
1-5 RFI Jumper
1-6 Dimensions

After you receive the product, check the following:

1. Inspect the unit after unpacking to ensure that it was not damaged during shipment. Make sure that the part number printed on the package corresponds with the part number indicated on the nameplate.
2. Make sure that the voltage for the wiring is in the range indicated on the nameplate. Install the controller according to this manual.
3. Before applying the power, make sure that all the devices, including power, motor, control board and digital keypad are connected correctly.
4. When wiring the controller, make sure that the wirings for input terminals " $R, S, T$ " and output terminals " $\mathrm{U} 1, \mathrm{~V} 1, \mathrm{~W} 1, \mathrm{U} 2, \mathrm{~V} 2, \mathrm{~W} 2$ " are correct to prevent damage to the controller.

## 1-1 Nameplate Information

## 460V Models



## 1-2 Model Number




## 1-4 Apply After Service by Mobile Device

## 1-4-1 Location of Service Link Label

Service Link label is within the nameplate label


## 1-4-2 Service Link Label

## Scan QR code to apply

1. Find the QR code sticker (as shown above).
2. Run the QR code reader App on your smart phone.
3. Point your camera at the QR Code. Hold your camera steady until the QR code comes into focus.
4. Access the Delta After-Sales Service website.
5. Fill in the information in the columns marked with an orange star.
6. Enter the CAPTCHA and click Submit to complete the request.

## Cannot find the QR code?

1. Open a web browser on your computer or smart phone.
2. Enter https://service.deltaww.com/ia/repair in the browser address bar and press the Enter key.
3. Fill in the information in the columns marked with an orange star.
4. Enter the CAPTCHA and click Submit to complete the request.

## 1-5 RFI Jumper

RFI jumper:
LTC controller contains Varistors / MOVs that are connected from phase to phase and from phase to ground to prevent the controller from unexpected stop or damage caused by mains surges or voltage spikes. Because the Varistors / MOVs from phase to ground are connected to ground with the RFI jumper, note that removing the RFI jumper will disable the protection.

Removing the RFI jumper:
Pry the RFI jumper with a slotted screwdriver.


Isolating main power from ground:
When the power distribution system for the controller is a floating ground system (IT Systems) or an asymmetric ground system (Corner Grounded TN Systems), you must remove the RFI jumper. Removing the RFI jumper disconnects the internal capacitors from ground to avoid damaging the internal circuits and to reduce the ground leakage current.

Important points regarding ground connection:
$\boxtimes$ To ensure the safety of personnel, proper operation, and to reduce electromagnetic radiation, you must properly ground the motor and controller during installation.

- The diameter of the grounding cables must comply with the local safety regulations.
$\square$ You must connect the shielded cable to the controller's ground to meet safety regulations.
$\square$ Only use the shielded cable as the ground for equipment when the aforementioned points are met.
$\square$ When installing multiple controllers, do not connect the grounds of the controllers in series but connect each controller to ground. The following pictures show the correct and wrong ways to connect the grounds.


Pay particular attention to the following points:
$\boxtimes$ Do not remove the RFI jumper while the power is ON.
$\boxtimes$ Make sure the main power is OFF before removing the RFI jumper.
$\square$ Removing the RFI jumper also cuts the capacitor conductivity of the surge absorber to ground and the built-in EMC filter capacitors. Compliance with the EMC specifications is no longer guaranteed.
$\square$ Do not remove the RFI jumper if the mains power is a symmetrical grounded power system in order to maintain the efficiency for EMC circuit.
$\nabla$ Remove the RFI jumper when conducting high voltage tests. When conducting a high voltage test to the entire facility, disconnect the mains power and the motor if the leakage current is too high.

## Floating Ground System (IT Systems)

A floating ground system is also called an IT system, an ungrounded system, or a high impedance/ resistance (greater than $30 \Omega$ ) grounded system.
च Disconnect the ground cable from the internal EMC filter.
V In situations where EMC is required, check for excess electromagnetic radiation affecting nearby lowvoltage circuits. In some situations, the adapter and cable naturally provide enough suppression. If in doubt, install an extra electrostatic shielded cable on the power supply side between the main circuit and the control terminals to increase shielding.
च Do not install an external RFI/EMC filter. The external EMC filter passes through a filter capacitor and connects power input to the ground. This is very dangerous and damages the controller.

## Asymmetric Ground System (Corner Grounded TN Systems)

Caution: Do not remove the RFI jumper while power to the input terminal of the controller is ON. In the following four situations, you must remove the RFI jumper. This is to prevent the system from grounding through the RFI and filter capacitors and damaging the controller.


## Using the RFI jumper

In the situation as the diagram on the right shows, you can use the RFI jumper to pass through RFI capacitor to make an internal grounding and reduce electromagnetic radiation. In a situation with higher requirements for electromagnetic compatibility and a symmetrical grounding power system, you can install an EMC filter. For example, the diagram on the right is a symmetrical grounding power system.


## 1-6 Dimensions

Frame A
VFD2207LTC43A, VFD4015LTC43A


Unit: mm [inch]

| Frame | W | W1 | W2 | H | H1 | D | S1 | S2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | 221.0 | 140.0 | 140.0 | 246.0 | 232.0 | 94.6 | 5.2 | 5.2 |
|  | $[8.70]$ | $[5.51]$ | $[5.51]$ | $[9.69]$ | $[9.13]$ | $[3.72]$ | $[0.20]$ | $[0.20]$ |

Digital Keypad (Optional)
VFD-PU08


Unit: mm [inch]

| W | W1 | W2 | W3 | H | H1 | H2 | H3 | D | D1 | D2 | D3 | D4 | S1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 68.0 | 63.8 | 59.9 | 8.1 | 46.8 | 42.0 | 26.3 | 7.5 | 35.6 | 22.7 | 7.6 | 2.2 | 1.3 | M3 $^{*} 0$. |
| $[2.68]$ | $[2.51]$ | $[2.68]$ | $[0.32]$ | $[1.84]$ | $[1.65]$ | $[1.04]$ | $[0.30]$ | $[1.40]$ | $[0.89]$ | $[0.30]$ | $[0.09]$ | $[0.05]$ | $5(2 \mathrm{X})$ |

[This page intentionally left blank]

## Chapter 2 Installation

## 2-1 Mounting Clearance

## 2-2 Airflow and Power Dissipation

2-3 Packaging Method

## 2-1 Mounting Clearance

$\boxtimes$ Prevent fiber particles, scraps of paper, shredded wood, sawdust, metal particles, etc. from entering LTC or adhering to the heat sink.
$\square$ Install the controller in a metal cabinet to prevent the risk of fire.
■ Install the controller in a Pollution Degree 2 (IEC 60664-1) environment with clean and circulating air. A clean and circulating environment means air without polluting substances and dust.

The products' figures shown below are for reference only. The actual products may look different.
↔- Inflow
Outflow
$\longleftrightarrow$ Clearance

Mounted vertical installation / side-by-side horizontal installation (Frame A)


Figure 2-1

Mounted vertical installation / side-by-side horizontal installation (Frame A)


Figure 2-2

Minimum mounting clearance

| Frame | $\mathrm{A}[\mathrm{mm}]$ | $\mathrm{B}[\mathrm{mm}]$ | $\mathrm{D}[\mathrm{mm}]$ |
| :---: | :---: | :---: | :---: |
| A | 50 | 30 | 0 |

NOTE:
Table 2-1
The minimum mounting clearances A-D stated in Table 2-1 apply to LTC Frame A. Failure to follow the minimum mounting clearances may cause the controller fan to malfunction and cause heat dissipation problems.

## Frame A VFD2207LTC43A; VFD4015LTC43A

Table 2-2


Figure 2-3

## NOTE:

- The mounting clearance stated is for installing the controller in an open area, as shown in Figure 2-3. To install the controller in a confined space (such as cabinet or electric box), follow the following rules: (1) Keep the minimum mounting clearances. (2) Install a ventilation equipment or an air conditioner to keep surrounding temperature lower than operation temperature. (3) Refer to parameter setting and set up Pr.02.03.
- The table below shows the heat dissipation and the required air volume when installing a single controller in a confined space.
- Refer to the table below (Airflow Rate for Cooling) for ventilation equipment design and selection.
- Refer to the table below (Power Dissipation for Controller) for air conditioner design and selection.
- Ambient temperature derating curve shows the derating status in different temperature in relation to different protection level.


## 2-2 Airflow and Power Dissipation

| Model | Airflow Rate for Cooling |  |  |  |  | Power Dissipation for Controller |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Flow Rate [cfm] |  |  |  | Flow Rate [m³/hr] |  | Power Dissipation [watt] |  |  |
|  | External | Internal | Total | External | Internal | Total | Loss External <br> (Heat sink) | Internal | Total |
| VFD2207LTC43A | 16 | - | 16 | 27.2 | - | 27.2 | 64 | 32 | 96 |
| VFD4015LTC43A | 16 | - | 16 | 27.2 | - | 27.2 | 122 | 53 | 175 |

- The required airflow shown in the table is for installing a single controller
- The heat dissipation shown in the in a confined space.
- When installing multiple controllers, the required air volume should be the required air volume for single controller $\times$ the number of the controllers. table is for installing a single controller in a confined space.
- When installing multiple controllers, volume of heat dissipation should be the heat dissipated for single controller $\times$ the number of the controllers.
- Heat dissipation for each model is calculated by rated voltage, current and default carrier.

Table 2-3

## 2-3 Packaging Method

As the diagram below shows, there are two layers of packaging for the controller. One controller is in the inner small carton, and three controllers are in the outer big carton.


Figure 2-4

## Chapter 3 Wiring

## 3-1 System Wiring Diagram

3-2 Wiring

After removing the packaging, verify that the power and control terminals are clearly noted. Read the following precautions before wiring.


V Disconnect AC input power before connecting any wiring to the controller.
च Even if the power has been turned off, a charge may still remain in the DC-link capacitors with hazardous voltages before the POWER LED is OFF. Do NOT touch the internal circuits and components.
$\nabla$ There are highly sensitive MOS components on the printed circuit boards. These components are especially sensitive to static electricity. Take anti-static measure before touching these components or the circuit boards.
$\boxtimes$ Never modify the internal components or wiring.
$\nabla$ Ground the controller by using the ground terminal. The grounding method must comply with the laws of the country where the controller is to be installed.
$\nabla$ Do NOT install the controller in a location with high temperature, direct sunlight or inflammable materials or gases.

V Never connect the controller output terminals U1, V1, W1 or U2, V2, W2 directly to the AC mains circuit power supply.
V After finishing the wiring of the controller, check if $\mathrm{U} 1, \mathrm{~V} 1, \mathrm{~W} 1$; U2, V2, W2 are short-circuited to PE with a multimeter. Do NOT power the controller if short circuits occur. Eliminate the short circuits before the controller is powered.
च The rated voltage range for the controller must be from 342 V to 528 V for 460 V models.
$\nabla$ Even if the three-phase AC motor is stopped, a charge with hazardous voltages may still remain in the main circuit terminals of the controller.
$\square$ The performance of electrolytic capacitor will degrade if it is not charged for a long time. It is recommended to charge the controller which is stored in no charge condition every 2 years for 3-4 hours to restore the performance of electrolytic capacitor in the controller. NOTE: When power up the controller by using connecting AC power to R/S/T terminals, use 220-240 $V_{A C}$ of single-phase power between $R$ and $S$ terminals to charge the controller for 30 minutes (do not run the controller). Then charge the controller at $380-480 \mathrm{~V}_{\mathrm{AC}}$ rated voltage of three-phase power between $R, S$, and $T$ terminals for one hour (do not run the controller). By doing these, restore the performance of electrolytic capacitor before starting to run the controller. Do NOT run the controller at $100 \%$ rated voltage right away.
च If the controller generates leakage current over AC 3.5 mA or over DC 10 mA on a grounding conductor, compliance with local grounding regulations or IEC61800-5-1 standard is the minimum requirement for grounding.

## 3-1 System Wiring Diagram



NOTE: For details on wiring method, see
Section 3-2 Wiring.
\(\left.$$
\begin{array}{c|l}\hline \begin{array}{c}\text { Power input } \\
\text { terminal }\end{array} & \begin{array}{l}\text { Supply power according to the rated power } \\
\text { specifications indicated in the manual. } \\
\text { See Chapter 7 Specification. }\end{array} \\
\hline \begin{array}{c}\text { Circuit } \\
\text { Breaker } \\
\text { or } \\
\text { Fuse }\end{array} & \begin{array}{l}\text { There may be a large inrush current during power on. } \\
\text { Refer to Section 6-2 Circuit Breaker for details. }\end{array} \\
\hline \text { Magnetic } & \begin{array}{l}\text { Switching the power ON/OFF on the primary side of the } \\
\text { magnetic contactor can turn the controller ON/OFF, but } \\
\text { frequent switching can cause machine failure. } \\
\text { Do not switch ON/OFF more than once an hour. }\end{array}
$$ <br>
\hline Do not use the magnetic contactor as the power switch <br>
for the controller; doing so shortens the life of the <br>

controller.\end{array}\right\}\)| When the main power supply capacity is greater than |
| :--- |
| 500 kVA, or when it switches into the phase capacitor, |
| the instantaneous peak voltage and current generated |
| may destroy the internal circuit of the controller. It is |
| (input |
| terminal) | | recommended that you install an input side AC reactor |
| :--- |
| in the controller. |
| This also improves the power factor and reduces power |
| harmonics. |
| The wiring distance should be within 10 m. |
| Refer to Section 6-3 AC Reactor for details. |

## 3-2 Wiring

## 3-2-1 Wiring Diagram

LTC


| Terminals | Descriptions |
| :---: | :--- |
| R, S, T | Mains input terminals (three-phase) |
| U1, V1, W1 | VFD1 drive output terminals for connecting a three-phase induction motor. |
| U2, V2, W2 | VFD2 drive output terminals for connecting a three-phase induction motor. |
| $\Theta \mathrm{E}$ | Ground connection; comply with local regulations. |

Main input power terminals
$\boxtimes$ DO NOT connect a three-phase model to single-phase power. R, S and T have no phase-sequence requirement; they can be connected in any sequence.
$\square$ You must install a NFB or circuit breaker between power input terminals and the main circuit terminals ( $R, S, T$ ). Add a magnetic contactor (MC) to the power input wiring to cut off power quickly and reduce malfunctions when the controller protection function activates. Both ends of the MC should have an R-C surge absorber.
$\square$ Tighten the screws in the main circuit terminal to prevent sparks caused by screws loosened due to vibration.

च Use voltage and current within the specifications in Chapter 7.
$\square$ The controller generates leakage current that flows through protective ground conductor to ground during its operation. ADD Type B residual current devices (RCDs), and choose a residual current of 30 mA above. Or choose generic RCDs with residual current larger than 200 mA and action time longer than 0.1 sec..

Output terminals of the main circuit
च If necessary, use an inductive filter only at the motor output terminals $\mathrm{U} 1, \mathrm{~V} 1$, W1; U2, V2, W2 of the controller. DO NOT use phase-compensation capacitors or L-C (Inductance-Capacitance) or R-C (Resistance-Capacitance), unless approved by Delta.
$\nabla$ DO NOT connect phase-compensation capacitors or surge absorbers at the output terminals of the controller.
$\square$ Use well-insulated motors to prevent any electric leakage from the motors.

## 3-2-2 Switching between two modes: SINK (NPN) / SOURCE (PNP)

X terminals are compatible with NPN and PNP modes. For details on NPN and PNP mode wiring in internal and external power supply, see the figures below.

■ Internal power supply


- External power supply




## NOTE:

PNP (DIP switch to Source) is the factory default for NPN/PNP switch function. For using different modes, make sure the hardware wiring is correct, and select by DIP switches (DIP switch AS2). The total output capacity of $24 \mathrm{~V}-\mathrm{DCM}$ internal 24 V power supply is 120 mA . For output capacity of external load, deduct the corresponding current consumption of the number of $X$ terminals activated ( 6 mA for each terminal).

## Chapter 4 Main Circuit Terminals

## 4-1 Main Circuit Diagram

4-2 Main Circuit Terminal Specifications

## 4-1 Main Circuit Diagram

LTC


Figure 4-1

| Terminals | Descriptions |
| :---: | :---: |
| $\mathrm{R}, \mathrm{S}, \mathrm{T}$ | Mains input terminals (three-phase) |
| $\mathrm{U} 1, \mathrm{~V} 1, \mathrm{~W} 1$ | VFD1 drive output terminals for connecting a three-phase induction motor. |
| $\mathrm{U} 2, \mathrm{~V} 2, \mathrm{~W} 2$ | VFD2 drive output terminals for connecting a three-phase induction motor. |
| E | Ground connection; comply with local regulations. |

## 4-2 Main Circuit Terminal Specifications



Figure 4-2

Wiring precautions:

- It is recommended to use LUG, and the wiring should comply with local regulations.
- Dimensions for LUG show as Figure 4-3 below. Recommended LUG model is SNYS2-3.7 or RNYS2-3.7 from K. S. TERMINALS INC..
- Use insulator that is resistant to 600 V and temperature resistant to $105^{\circ} \mathrm{C}$.
- If you install at $\mathrm{Ta} 45^{\circ} \mathrm{C}$ environment, use copper wires that have a voltage rating of 600 V and are temperature resistant to $70^{\circ} \mathrm{C}$ or above.
- If you install at $\mathrm{Ta} 45^{\circ} \mathrm{C}$ above environment, use copper wires that have a voltage rating of 600 V and are temperature resistant to $90^{\circ} \mathrm{C}$ or above.


Figure 4-3

| Model | Main Circuit Terminals R, S, T, U1, V1, W1, U2, V2, W2, PE |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Wiring Specifications | Maximum Wire Gauge | Minimum Wire Gauge | Screw Size \& Torque ( $\pm 10 \%$ ) |
| VFD2207LTC43A | IEC / GB | $2.5 \mathrm{~mm}^{2}$ | $1.5 \mathrm{~mm}^{2}$ | $\begin{gathered} \text { M3.5 } \\ 13.7 \mathrm{kgf}-\mathrm{cm} \end{gathered}$ |
|  | AWG | 12 AWG | 16 AWG |  |
| VFD4015LTC43A | IEC / GB | $2.5 \mathrm{~mm}^{2}$ | $2.5 \mathrm{~mm}^{2}$ | $\begin{gathered} \text { [11.9 lbf-in.] } \\ \text { [1.4 Nm] } \end{gathered}$ |
|  | AWG | 12 AWG | 14 AWG |  |

## Chapter 5 Control Terminals

5-1 Slide Cover and Internal Devices
5-2 Control Terminal Specifications
5-3 Removing a Control Terminal Block
5-4 LED Indicators on Control Terminals

## 5-1 Slide Cover and Internal Devices

Detach the Slide Cover


Descriptions of Relays:

1. Y0-Y6: Removable, corresponding to multi-function output terminals $\mathrm{Y} 0-\mathrm{Y} 6$.
2. Y7: Removable, corresponding to multi-function output terminals Y 7 (Y7-1 and Y7-2).
3. DIP switch AS2: Input contact X NPN (Sink) / PNP (Source) mode switch. Default is Source (PNP).
4. DIP switch AS1: PLC Run / Stop switch. Switch between Run/Stop status. Default is RUN.

NOTE: Do NOT touch the PIN of DIP switch AS1 or AS2 when switching them, as the figure above shows. This is to prevent electrical devices from being broken caused by static electricity through the human body.

## 5-2 Control Terminal Specifications



IO Terminal Block


| Terminals | Terminal Type | Stripping Length (mm) | Maximum Wire Gauge AWG ( $\mathrm{mm}^{2}$ ) | Minimum Wire Gauge AWG ( $\mathrm{mm}^{2}$ ) | Tightening Torque ( $\pm 10 \%$ ) ( $\pm 10 \%$ ) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Contact Y terminals | TB1 <br> Integrated | 7-8 | 14 (2.5) | 14 (2.5) | $\begin{gathered} 5.1 \mathrm{kgf}-\mathrm{cm} \\ {[4.43 \mathrm{lbf}-\mathrm{in}]} \\ {[0.5 \mathrm{~N}-\mathrm{m}]} \\ \hline \end{gathered}$ |
|  | TB2 <br> Integrated | 6-7 | 18 (1.0) | 24 (0.2) | $\begin{gathered} 5.7 \mathrm{kgf-cm} \\ {[4.96 \mathrm{lbf}-\mathrm{in}]} \\ {[0.56 \mathrm{~N}-\mathrm{m}]} \\ \hline \end{gathered}$ |
| 24 V <br> terminals | TB3 <br> Integrated | 5-6 | 18 (1.0) | 24 (0.2) | $\begin{gathered} 4.1 \mathrm{kgf}-\mathrm{cm} \\ {[3.54 \mathrm{lbf}-\mathrm{in}]} \\ {[0.4 \mathrm{~N}-\mathrm{m}]} \\ \hline \end{gathered}$ |
| Contact X terminals | TB4 <br> Removable | 7-8 | 18 (1.0) | 28 (0.2) | Screw-free |

## Wiring precautions:

■ The contacts $X / Y$ are not short-circuited to any wires at the factory default. As shown as © in the figure above, AS1, the DIP switch for PLC Run/Stop for LTC, is switched to the right side RUN (PLC Run) by default, whereas AS2, the DIP switch for SINK (NPN) / SOURCE (PNP) modes of contacts X for PLC, is switched to the right side SOURCE (PNP) by default.

- As shown as (A) in the figure above, $24 \mathrm{~V}-\mathrm{X}$ contacts are short-circuited to SOURCE (PNP) mode, whereas $X$ contacts-DCM are short-circuited to SINK (NPN) mode. For more information, see the wiring diagrams in Chapter 3 Wiring.
■ Tighten the wiring with a 3.5 mm width and 0.6 mm thickness slotted screwdriver.
- Tighten the wiring with a PH1 slotted screwdriver.

■ When wiring bare wires, ensure that they are perfectly arranged to go through the wiring holes.

- Select 450V for TB1 wirings, and 250V for TB2 wirings.
- How to reset EF fault when using EF terminal to connect to emergency stop circuit::

1. EF terminal is correctly connected to emergency stop circuit (channels between 24 V (Digital signal common terminal Source) - emergency stop switch N.C. contact - EF terminal can be connected in series);
2. Select one $X$ contact (such as X 0 ) to connect EF terminal in parallel to connect to emergency stop switch N.C. contact, and add one line of programming in PLC to realize:
2.1 When pressing the emergency stop button, both VFD1 and VFD2 of LTC stop outputting, and ERROR indicator flashes (EF fault will be triggered both on VFD1 and VFD2 and fault code EF is displayed on communication panel PU08 or PU08V);
2.2 When releasing the emergency stop button, ERROR indicator lights off, and at the same time, EF fault is reset (EF fault is no longer displayed on PU08 or PU08V).


NOTE: For details on using PLC for LTC, see Chapter 13 PLC Function Application in the user manual.

| Terminals | Function | Default (PNP Mode) |
| :---: | :--- | :--- |
| 24 V | Digital control signal common <br> (Source) | $+24 \mathrm{VDC} \pm 5 \% 120 \mathrm{~mA}$ |
| DCM | Digital control signal common (Sink) | Common terminal for multi-function input <br> terminals |
| X0-X7, <br> X10-X17, <br> X20 | Multi-function input terminals 0-20 | Refer to address 15B3H bit0-7 external <br> input status for function selections of <br> terminals X0-X7 <br> Refer to address 15B3H bit8-15 external <br> input status for function selections of <br> terminals X10-X17 <br> Refer to address 15B4H bit0 external input <br> status for function selections of terminal X20 <br> ON: activation current 5.6 mA $\geq 18 \mathrm{VDC}$ |


| Terminals | Function | Default (PNP Mode) |
| :---: | :---: | :---: |
|  |  | OFF: cut-off voltage $\leq 4 \mathrm{~V}$ D |
| EF | External fault input | ON : activation current $5.6 \mathrm{~mA} \geq 18 \mathrm{VDC}$ OFF: cut-off voltage $\leq 4 \mathrm{~V} D$ |
| SG+ | Modbus RS-485 <br> NOTE: SG+ and SG- are RS-485 serial communication terminals for PLC, and can also be used for uploading/downloading PLC programs. |  |
| RJ45 | PIN 1, 2, 6, 7: Reserved PIN 3: GND  <br> PIN 4: SG- PIN 5: SG+ PIN8: EV |  |
| Y0 | Multi-function relay output 0 (N.O.) | Resistive Load $\begin{aligned} & \text { 2.5A (N.O.) / 2.5A (N.C.) } 250 \mathrm{VAC}_{\mathrm{AC}} \\ & \text { 2.5A (N.O.) / 2.5A (N.C.) } 30 \mathrm{VDC}_{\mathrm{DC}} \end{aligned}$ <br> Inductive Load $\begin{aligned} & \text { 2A (N.O.) / 2A (N.C.) } 250 \mathrm{~V}_{\mathrm{AC}} \\ & 2 \mathrm{~A}(\mathrm{~N} . \mathrm{O} .) / 2 \mathrm{~A}(\mathrm{~N} . \mathrm{C} .) 30 \mathrm{~V}_{\mathrm{DC}} \end{aligned}$ |
| Y1 | Multi-function relay output 1 (N.O.) |  |
| Y2 | Multi-function relay output 2 (N.O.) |  |
| C0 | Y0-Y2 common terminal |  |
| Y3 | Multi-function relay output 3 (N.O.) |  |
| Y4 | Multi-function relay output 4 (N.O.) |  |
| Y5 | Multi-function relay output 5 (N.O.) |  |
| C1 | Y3-Y5 common terminal |  |
| Y6 | Multi-function relay output 6 (N.O.) |  |
| C2 | Y6 common terminal |  |
| Y7-1 | Multi-function relay output 7-1 (Double pole single throw N.O.) | Resistive Load <br> 1.2A (N.O.) / 1.2A (N.C.) $380 \mathrm{~V}_{\mathrm{AC}}$ <br> 3A (N.O.) / 3A (N.C.) $250 \mathrm{~V}_{\mathrm{AC}}$ <br> 2A (N.O.) / 2A (N.C.) 30 Vdc <br> Inductive Load <br> 1A (N.O.) / 1A (N.C.) $380 \mathrm{~V}_{\mathrm{AC}}$ <br> 2.4A (N.O.) / 2.4A (N.C.) $250 \mathrm{~V}_{\mathrm{AC}}$ <br> 2A (N.O.) / 2A (N.C.) 30 VDC |
| C3-1 | Y7-1 common terminal |  |
| Y7-2 | Multi-function relay output 7-2 (Double pole single throw N.O.) |  |
| C3-2 | Y7-2 common terminal |  |
| DC <br> POWER+ | 24 VDC output positive | $+24 \mathrm{VDC} \pm 3 \% 1.4 \mathrm{~A}$ |
| DC POWER+ |  |  |
| DC POWER- | 24 VDC output negative |  |
| DC POWER- |  |  |

* Analog control signal wiring specification: $0.75 \mathrm{~mm}^{2}$ [18 AWG] with shielded stranded wire.


## 5-3 Removing a Control Terminal Block

As the figure below shows, input terminal block (X contacts) can be detached manually, and output terminal block (Y contacts) cannot be detached manually.


## 5-4 LED Indicators on Control Terminals

LID LTC with three-phase 380-480V $\mathrm{V}_{\mathrm{AC}}$ power: Power indicator is ON (green).
[日] PLC indicator: Green (ON): PLC run; OFF: PLC stop.
[1] ERROR indicator: Red (ON):

| No. | Indicator Status | ERROR <br> DESCRIPTION | Flash Indication <br> ("-": ON; ".": OFF) |
| :---: | :--- | :---: | :---: |
| 1 | ERROR is steady ON | PLC Error | -------- |
| 2 | ERROR is ON for 1s and OFF for 1s <br> (in circulation) | VFD1 Error | ....---- |
| 3 | ERROR is ON for 2s and OFF for <br> $0.5 s ~(i n ~ c i r c u l a t i o n) ~$ | VFD2 Error | ..------- |

NOTE: Error indication priority: PLC Error > VFD1 Error > VFD2 Error
$\mathbb{L d}$ FWD indicator: ON (green) when the running direction that VFD1 or VFD2 corresponds to the motor is forward.
Ild REV indicator: ON (green) when the running direction that VFD1 or VFD2 corresponds to the motor is reverse.
[a] Contact X input indicator: $\mathrm{X0}-\mathrm{X} 20$, ON (green) when input signals are valid.
© Contact Y output indicator: $\mathrm{Y} 0-\mathrm{Y} 7, \mathrm{ON}$ (green) when output signals are valid. Among them, $\mathrm{Y} 7-1$ and $\mathrm{Y} 7-2$ use the same relay, that is, they use only one LED indicator to indicate ON/OFF.
© DC POWER output indicator: ON (green) when power output is normal.
NOTE: Only one color displays when each of the indicators lights ON for LTC.

## Chapter 6 Optional Accessories

## 6-1 Digital Keypad PU08/PU08V

6-2 Circuit Breaker and Fuse
6-3 Reactors (AC and Zero Phase)
6-4 EMC Filter

The optional accessories listed in this chapter are available upon request. Installing additional accessories to your controller substantially improves the controller's performance. Select accessories according to your need or contact your local distributor for suggestions.

## 6-1 Digital Keypad PU08/PU08V

VFD-LTC series uses digital keypad panel to serve as function displays and operations
Digital Keypad Panel PU08:


Figure 6-1
There are four indicators on the panel

- STOP Stop indicator: lights ON when running stops.
- RUN Running indicator: lights ON when motor runs.
- FWD Forward running indicator: lights ON when motor runs in a forward direction.
- REV Reverse running indicator: lights ON when motor runs in a reverse direction.

Digital Keypad Panel PU08V:

Main Display Area
Displays frequency, current, voltage, user-defined, fault codes, etc.

## Status Display Area

Indicates drive's running status:
Run, Stop, Forward/Reverse,
Potentiometer enable/disable

## Up Key

Changes parameters or values
RUN Key
VFD Run
STOP/RESET Key
Makes the drive stop running and resets faults


## Mode Selection

Displays mode changes step by step for selection

## ENTER Key

1. Enters setting functions, such as forward (Frd), industry application functions (APP) etc.
2. Confirms parameter settings

## Left-shift/Down Key

Changes value or parameters / Long press MODE key to switch between left-shift key and down key

Frequency Setting Potentiometer
Use this knob for main frequency command input

Figure 6-2

## 6-2 Circuit Breaker and Fuse

## Air Circuit Breaker (ACB)

It is recommended the surrounding temperature for $A C B$ should be $\geq 50^{\circ} \mathrm{C}$. In the meanwhile, consider temperature derating for components with ON / OFF switch in accordance with the ambient temperature of the on-site distribution panel.

460V Models

| Frame | Models | VFD1 Output <br> Current $[A]$ | VFD2 Output <br> Current $[A]$ | Input <br> Current $[A]$ | Selection of <br> ACB $[A]$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | VFD2207LTC43A | 2.5 | 5.5 | 10.3 | 20 |
|  | VFD4015LTC43A | 4.2 | 9.0 | 14.3 | 25 |

Table 6-1

Fuse Specification Chart:

## Fuse

$\square$ Fuse specifications lower than the table below are allowed.
$\square$ Use certified fuses that comply with local regulations.

| 460V Models | Input Current $[A]$ | Fuse Specification |  |
| :---: | :---: | :---: | :---: |
|  |  | I $[A]$ | Bussmann P/N |
| VFD2207LTC43A | 10.3 | 25 | JJS-25 |
| VFD4015LTC43A | 14.3 | 30 | JJS-30 |

Table 6-2

## 6-3 Reactors (AC and Zero Phase)

AC Input Reactor
Install an AC reactor at the input side of an AC motor drive can increase line impedance, improve the power factor, reduce input current, increase system capacity, and reduce interference generated from the motor drive. It also reduces momentary voltage surges or abnormal current spikes from the mains power, further protecting the drive. For example, when the mains power capacity is higher than 500 kVA , or when using a phase-compensation capacitor, momentary voltage and current spikes may damage the AC motor drive's internal circuit. An AC reactor at the input side of the AC motor drive protects it by suppressing surges.

Installation Method:
Install an AC input reactor in series between the mains power and the three input phases R, S, T, as shown in the figure below:

LTC


Figure 6-3: AC input reactor installation diagram

The table below lists the standard specifications for Delta LTC AC input reactors:

| Models | Rated <br> Current <br> [Arms] | Saturation <br> Current <br> $[$ Arms $]$ | $3 \%$ <br> Reactor <br> $[\mathrm{mH}]$ | $5 \%$ <br> Reactor <br> $[\mathrm{mH}]$ | Built-in <br> DC Reactor |
| :---: | :---: | :---: | :---: | :---: | :---: |
| VFD2207LTC43A | 8 | 12 | 3.03 | 5.05 | No |
| VFD4015LTC43A | 14.7 | 22.1 | 1.65 | 2.75 | No |

Table 6-3

|  | Recommended Wire Size |  | Wiring Method | Maximum Wiring <br> Quantity |
| :---: | :---: | :---: | :---: | :---: |
| RF008X00A | $\leq 8$ AWG | $\leq 8.37 \mathrm{~mm}^{2}$ | Diagram A | Single-core*3 <br> or |
| T60006L2040W453 | $\leq 8$ AWG | $\leq 8.37 \mathrm{~mm}^{2}$ | Diagram B | Four-core cable*1 |

NOTE 1: *Motor cable is 600 V insulated cable wire
Table 6-4
NOTE 2: The table above only considers the motor wire size
NOTE 3: For information on maximum wiring quantity, see Chapter 4 Main Circuit Terminals.
Diagram A
Pass the cable through at least one zero phase reactor.


Figure 6-4
Diagram B

## Zero Phase Reactor



Figure 6-5

## Diagram C

## Zero Phase Reactor



Figure 6-6

NOTE 1: The table above is for reference only. Use a suitable cable type and diameter so that the cable can easily pass through the center of the zero phase reactor.

NOTE 2: Do not pass the grounding cable through the zero phase reactor; only pass the motor wire and power cable through the zero phase reactor. Signal line is not subjected to the limits.

NOTE 3: For the zero phase reactor used for signal lines, make the position of the zero phase reactor adjacent to the drive, and fix it to prevent pulling caused by vibration.

| Models* | Recommended <br> Wire Size | Wiring <br> Method | Qty | Applicable Network Wire / Signal Line |
| :---: | :---: | :---: | :---: | :---: |
| T60006L2040W453 | $\leq 8$ AWG | Diagram C | 1 | Category 5e shielding, shielded twisted-pair cable, <br> CAN standard cable (TAP-CB05, TAP-CB10) |

NOTE 1: *Select zero phase reactors according to actual wire gauges. This table is for reference only.
Table 6-5
NOTE 2: The size of some signal lines and communication cables may have limits due to mechanical considerations. Therefore, it is suggested to select a larger zero phase reactor.

Reference table for maximum motor wiring gauge when installing a zero reactor (including LUG width and motor cable temperature resistance)

| Zero phase reactor | Max. Wire Size / <br> LUG Width | Max. Wire Gauge AWG (1C*3) |  | Max. Wire Gauge AWG (4C*1) |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $90^{\circ} \mathrm{C}$ | $75^{\circ} \mathrm{C}$ | $90^{\circ} \mathrm{C}$ |  |
| RF008X00A | 13 MM | 3 AWG | 1 AWG | 3 AWG | 1 AWG |
| T60006L2040W453 | 11 MM | 9 AWG | 4 AWG | 6 AWG | 6 AWG |

Table 6-6


Figure 6-7
Unit: mm [inch]

| Model | A | B | C | $\mathbf{D}$ | $\mathbf{E}$ | $\mathbf{F}$ | $\mathbf{G}(\boldsymbol{\varnothing})$ | Torque |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RF008X00A | 98 | 73 | 36.5 | 29 | 56.5 | 86 | 5.5 |  |
|  | $5.2[3.858]$ | $5.2[2.874]$ | $5.2[1.437]$ | $5.2[1.142]$ | $5.2[2.224]$ | $5.2[3.386]$ | $5.2[0.217]$ | $<10 \mathrm{kgf} / \mathrm{cm}^{2}$ |

Table 6-7

## Ferrite Core

Model No.: T60006-L2040-W453


Unit: mm
Figure 6-8

## 6-4 EMC Filter

The table below shows external EMC filter models for each motor drive. Choose corresponding zero phase reactors and applicable shielding cables according to the required noise emission and electromagnetic interference rating for the best configuration and anti-interference performance. If radiation emission (RE) is not a concern on site and you only need conducted emission (CE) to reach Class C3, you do not need to install a zero phase reactor on the input side to reach the EMC standard.

460V Models

| LTC |  |  | EMC Filter Model \# | Zero Phase Reactor |  | Carrier Frequency | Conducted <br> Emission (CE) | Radiated <br> Emission (RE) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Frame | Drive Model \# | Rated Input Current [A] |  | Input Side $(R / S / T)$ | Output Side <br> ( $\mathrm{U} / \mathrm{V} / \mathrm{W}$ ) |  | Length of Output Shielded Cable C3 | EN61800-3 |
| A | VFD2207LTC43A | 10.3 | EMF014A43A | $\begin{array}{\|c\|} \hline \text { RF008X00A } \\ \text { or } \\ \text { T60006L204 } \\ \text { 0W453 } \\ \hline \end{array}$ | $\begin{array}{\|c\|} \hline \text { RF008X00A } \\ \text { or } \\ \text { T60006L2040 } \\ \text { W453 } \\ \hline \end{array}$ | $\leq 8 \mathrm{kHz}$ | 25 m | C3 |
|  | VFD4015LTC43A | 14.3 | EMF018A43A |  |  |  |  |  |

Table 6-8

Zero phase reactor installation position diagram:
$1^{*}$ : Install at the cable between the power supply and the EMC filter
$2^{*}$ : Install at the cable between the EMC filter and the drive
3*: Install at the cable between the drive and the motor


Figure 6-9

## Chapter 6 Optional Accessories | LTC

## EMC Filter Dimension:

## Model name: EMF014A43A



Figure 6-10

Model name: EMF018A43A

Unit: mm [inch]


Figure 6-11


Figure 6-12

## EMC Filter Installation

All electrical equipment, including AC motor drives, generates high frequency / low frequency noise and interferes with peripheral equipment by radiation or conduction during operation. Installing an EMC filter helps eliminate much interference. It is recommended to use DELTA EMC filter to have the best interference elimination performance.

Install and wire AC motor drive and EMC filter according to the instructions in the user manual to ensure compliance with the following regulations:

1. EN61000-6-4
2. EN61800-3: 1996
3. EN55011 (1991) Class A Group 1

## General Precautions

To achieve optimal effect on suppressing interference of AC motor drive, follow the instructions in the user manual to install and wire the AC motor drive. Moreover, pay attention to the precautions below.

1. Install EMC filter and $A C$ motor drive on the same metal plate.
2. Install $A C$ motor drive on footprint EMC filter or install EMC filter as close as possible to the AC motor drive.
3. Do the wiring as short as possible.
4. Ground the metal plate.
5. Fix the cover of EMC filter and AC motor drive or grounding on the metal plate. Leave the contact area as large as possible.

## Chapter 7 Specifications

## 7-1 460V Models

7-2 Environment for Operation, Storage and Transportation
7-3 Specification for Operation Temperature and Protection Level
7-4 Derating Curve

## 7-1 460V Models

| Frame |  | A |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Model VFD _ _ _ LTC___ |  | VFD2207LTC43A |  | VFD4015LTC43A |  |
|  | Descriptions | VFD1 | VFD2 | VFD1 | VFD2 |
|  | Rated Output Capacity (kVA) | 4.4 | 2.0 | 7.4 | 3.3 |
|  | Rated Output Current (A) | 5.5 | 2.5 | 9.0 | 4.2 |
|  | Applicable Motor Output (kW) | 2.2 | 0.75 | 4.0 | 1.5 |
|  | Applicable Motor Output (HP) | 3 | 1 | 5.5 | 2 |
|  | Overload Capacity | Sustains for 1 minute for every 5 minutes when the drive outputs $150 \%$ of the drive's rated output current. |  |  |  |
|  | Max. Output Frequency (Hz) | 0.1-400.0 |  |  |  |
|  | Carrier Frequency (kHz) | 2-12 (Default: 8) |  |  |  |
|  | Input Current (A) | 10.3 |  | 14.3 |  |
|  | Rated Voltage / Frequency | Three-phase 380-480 $\mathrm{V}_{\text {AC }}(-10-+10 \%)$, $50 / 60 \mathrm{~Hz}$ |  |  |  |
|  | Operating Voltage Range | $342-528 \mathrm{~V}_{\mathrm{AC}}$ |  |  |  |
|  | Mains Frequency Range | $47-63 \mathrm{~Hz}$ |  |  |  |
|  | Efficiency (\%) | 95 |  |  |  |
|  | Weight (kg) | 2.34 |  | 2.44 |  |
|  | Cooling Method | Fan cooling |  |  |  |
|  | EMC Filter | Optional |  |  |  |
|  | DC Power (W) | $35 \mathrm{~W}, 24 \mathrm{~V}$ DC |  |  |  |

Table 7-1

## NOTE:

1. Loading rate:
1.1 While VFD1 continuously runs with $100 \%$ of rated output current, VFD2 can still continuously run with $50 \%$ of rated output current or periodically run with $100 \%$ of rated output current (sustains for 30 seconds for every minute).
1.2 For applications other than telescopic belt conveyor, consider the loading conditions mentioned above. Contact Delta customer services for more information.
2. If carrier frequency is larger than 8 kHz , rated output current will decrease. See Figure 7-2 derating curve in Section 7-4.
3. Rated input current fluctuates due to not only connections between power adapter and reactors at input side, but also impedance at power side.

General Specifications

| Item |  | Specifications |
| :---: | :---: | :---: |
| Control Characteristics | Control Mode | Sine wave PMW (V/F control) |
|  | Frequency Setting Resolution | 0.01 Hz |
|  | Output Frequency Resolution | 0.01 Hz |
|  | Torque Characteristics | Auto-torque compensation, slip compensation, reaches $150 \%$ of rated torque when starting torque is 5.0 Hz . |
|  | Overload Capacity | 150\% of rated output current for 60 seconds |
|  | Skip Frequency | Three points can be set from $0.1-400.0 \mathrm{~Hz}$ |
|  | Accel. / Decel. Time | $0.1-600$ seconds (two steps of acceleration/deceleration time can be set separately) |
|  | Stall Prevention Level | Set by 0-200\% of the drive's rated current |
|  | DC Brake | Can be operated from $0.1-400.0 \mathrm{~Hz}$ when the drive receives stop signal. Starting time for DC current 0-100\% of rated current is $0-60$ seconds, and stopping time for that is also $0-60$ seconds. |
|  | V/F Curve | Adjustable V/F curve settings |
| Protection Functions |  | Over-voltage, over-current, low-voltage, external fault interruption, motor overload, drive overload, drive overheat |
| Built-in Functions for Integrated Drive |  | Built-in AVR (Automatic Voltage Regulation) function Acceleration/deceleration S-curve settings <br> Over-voltage, over-current stall prevention <br> Fault record <br> Torque compensation <br> Slip compensation <br> EF (External Fault) function <br> Carrier frequency adjustment <br> Upper and lower limit settings of output frequency <br> Parameter reset <br> Restart after fault <br> NPN/PNP mode selection |
| Digital Keypad Panel |  | Not included (optional accessories PU08/PU08V) |
| Product Compliance |  | CE <br> GB/T12668.3 |

Table 7-2

## PLC Specifications

| Item | Specifications | Note |
| :---: | :---: | :--- |
| Control Mode | Alternating back-and-forth scanning method |  |
| Inputs/Outputs <br> Control Mode | Cyclic refresh mode | Application commands (1-dozens of $\mu s)$ |
| Execution speed | Basic commands (several $\mu \mathrm{s}$ ) |  |
| Programming <br> Language | Commands and ladder diagrams | X: 17 points for user-defined, and 4 for <br> internal pre-defined <br> Y: 8 points for user-defined, and 5 for <br> internal pre-defined |
| Program Capacity | Digital inputs $(\mathrm{X}): 17+4=21$ <br> Digital outputs $(\mathrm{Y}): 8+5=13$ |  |

Table 7-3

| Type | Device | Item |  |  | Range |  | Function |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Relay <br> (bit) | X | External input relay |  |  | X0-X20, 17 points, octal | Total 34 points | Corresponds to external input points |
|  | X | Internal input relay |  |  | X21-X24, 4 points, octal |  | Corresponds to internal input points |
|  | Y | External output relay |  |  | Y0-Y7, 8 points, octal |  | Corresponds to external output points |
|  | Y | Internal output relay |  |  | Y10-Y14, 5 points, octal |  | Corresponds to internal output points |
|  | M | Auxiliary relay |  | ral purpose | M0-M999, 1000 points | Total 1280 points | Contacts can be used as ON/OFF switch in the program |
|  |  |  |  | ial purpose | M1000-M1279, 280 points |  |  |
|  | T | Timer |  | ms timer | T0-T159, 160 points | Total 160 points | Timer indicated by TMR instruction. If timing reaches its target, the T contact of the same number will be ON. |
|  | C | Counter |  | bit counting up neral purpose) | C0-C79, 80 points | Total 80 points | Counter indicated by CNT (DCNT) instruction. If counting reaches its target, the $C$ contact of the same number will be ON. |
| Register <br> word data (2 byte) | T | Current value of timer |  |  | T0-T159, 160 points |  | When the timing reaches the target, the contact of the timer will be ON. |
|  | C | Current value of counter |  |  | C0-C79, 16-bit counter, 80 points |  | When the counting reaches the target, the contact of the counter will be ON. |
|  | D | Data register |  | Latched | D0-D999, 1000 points | Total 1620 points | Memory area for data storage |
|  |  |  |  | Special purpose | D1000-D1619 - 620 points |  |  |
| Constant | K | Decimal |  | Single byte | Available setting range: $\mathrm{K}-32,768$ to K32,767 |  |  |
|  |  |  |  | Double byte | Available setting range: | K-2,147,483 | ,648 to K2,147,483,647 |
|  | H | Hexadecimal |  | Single byte | Available setting range: H0000 to HFFFF |  |  |
|  |  |  |  | Double byte | Available setting range: H00000000 to HFFFFFFFF |  |  |
| Serial communication port (program write/read) |  |  |  |  | RS-485 USB Port |  |  |

Table 7-4

## 7-2 Environment for Operation, Storage and Transportation

| Characteristics |  | Descriptions |
| :---: | :---: | :---: |
| EMI Filter |  | Without built-in |
| Environment | Protection Level | IP20 |
|  | Pollution Degree | 2 |
|  | Installation Location | An altitude of lower than 1000 m, indoor (no corrosive gases, liquids, and dust) |
|  | Surrounding Temperature | Non-condensing, non-freezing $-10-+45^{\circ} \mathrm{C}$. Derate when operated with full-load in temperature above $45^{\circ} \mathrm{C}$ to protect service life. |
|  | Storage Temperature | $-20-+60^{\circ} \mathrm{C}$ |
|  | Humidity | Below 90\% RH (non-condensing) |
|  | Vibration | 2.0 mm , peak to peak value range from $2-13.2 \mathrm{~Hz}$; 0.7-1.0 G range from $13.2-55 \mathrm{~Hz}$; <br> 1.0 G, range from $55-512 \mathrm{~Hz}$; <br> Compliance with IEC 60068-2-6 |

Table 7-5

## 7-3 Specification for Operation Temperature and Protection Level

| Model | Frame | Upper Cover at <br> Outer Case | Protection Level | Operation <br> Temperature |
| :---: | :---: | :---: | :---: | :---: |
| VFDxxxxLTCxxx | Frame A <br> $460 \mathrm{~V}:$ <br> $2.2 \mathrm{~kW}+0.75 \mathrm{~kW}$ <br> $4.0 \mathrm{~kW}++1.5 \mathrm{~kW}$ | $\mathrm{~N} / \mathrm{A}$ | IP 20 | $-10-45^{\circ} \mathrm{C}$ |

## 7-4 Derating Curve

V When choosing the correct model, consider factors such as ambient temperature, altitude, carrier frequency, control mode, and so on. That is,

Actual rated current for application $(A)=$ Rated output current $(A) \times$ Ambient temp. rated derating (\%) x Altitude rated derating (\%) x Carrier frequency rated derating (\%)

| Protection Level | Operating Environment |
| :---: | :--- |
| IP20 | 460 V : If the AC motor drive operates at the rated current, the ambient temperature <br> needs to be between $-10-45^{\circ} \mathrm{C}$. If the temperature is above $45^{\circ} \mathrm{C}$, decrease $2 \%$ of <br> the rated current for every $1^{\circ} \mathrm{C}$ increase in temperature. The maximum allowable <br> temperature is $60^{\circ} \mathrm{C}$. |

Table 7-7

## Ambient Temperature Derating Curve

460 V


Figure 7-1
The rated output current derating (\%) when carrier frequency is the default value:


Table 7-8

Carrier Frequency Derating Curve


Figure 7-2
The rated output current derating (\%) of 460V models for different carrier frequencies:

| Model | 8 | 9 | 10 | 11 | 12 |
| :--- | :---: | :---: | :---: | :---: | :---: |
| VFD_(kHz) | 100 | 94 | 88 | 83 | 78 |

Table 7-9

Altitude Derating Curve

| Condition | Operating Environment |
| :---: | :--- |
|  | If the AC motor drive is installed at an altitude of $0-1000 \mathrm{~m}$, follow normal operation <br> restrictions. For altitudes of $1000-2000 \mathrm{~m}$, decrease the drive's rated current by $1 \%$ or <br> High Altitude <br> lower the temperature by $0.5^{\circ} \mathrm{C}$ for every 100 m increase in altitude. The maximum <br> altitude for corner grounded is 2000 m. If installing at an altitude higher than 2000 m is <br> required, contact Delta for more information. $1 \mathrm{~m}=1$ meter |

Table 7-10


Figure 7-3

The rated output current derating (\%) for different altitudes above sea level:

| Altitude above Sea Level (Meter) | 0 | 1000 | 1500 | 2000 |
| :---: | :---: | :---: | :---: | :---: |
| Output Current / Rated Current (\%) | 100 | 100 | 95 | 90 |

Table 7-11
[This page intentionally left blank]

# Chapter 8 Descriptions of Drive and PLC Operations 

8-1 An Overview of Functions
8-2 Descriptions of Digital Keypad PU08/PU08V
8-3 Function of Digital Keypad PU08/PU08V
8-4 Fault Code Description of Digital Keypad PU08/PU08V
8-5 VFDSoft Software
8-6 WPLSoft Software

## 8-1 An Overview of Functions

Function descriptions of LTC:


The functions are described by numbers with circles in sequence, as shown in the diagram above:
(1) Power input (PE/R/S/T): Three-phase power and PE terminals.
(2) VFD1 drive output (PE/U1/V1/W1): VFD1 drive output and PE terminals.
(3) VFD2 drive output (U2/V2/W2/PE): VFD2 drive output and PE terminals.
(4) DC POWER (+/+/-/-): Built-in 24VDC 35W power in the controller. For the use of external load.
(5) POWER indicator: Lights ON (green) when LTC is powered on.
(6) VFD1 potentiometer: Turn clockwise to increase the frequency command. Turn counterclockwise to decrease the frequency command.
(7) VFD2 potentiometer: Turn clockwise to increase the frequency command. Turn counterclockwise to decrease the frequency command.
(8) VFD1 FWD/REV indicator: FWD indicator lights ON (green) when VFD1 is set as motor's running direction in forward. REV indicator lights ON (green) when VFD1 is set as motor's running direction in reverse.
(9) VFD2 FWD/REV indicator: FWD indicator lights ON (green) when VFD2 is set as motor's running direction in forward. REV indicator lights ON (green) when VFD2 is set as motor's running direction in reverse.
(10) Y0-Y7 indicator: Output signal indicator of contacts Y. Lights ON (green) when the corresponding relays activate.
(11) Input terminal block:
(11.1) X0-X7, X10-X17, X20: Multi-function input terminals. You can switch between PNP/NPN modes. The default is PNP mode.
(11.2) 24 V (two): Digital signal (contact X) common terminals (Source). The connection between 24 V and contact X is PNP (Source) mode.
(11.3) DCM (two): Digital signal (contact X) common terminals (Sink). The connection between DCM and contact $X$ is NPN (Sink) mode.
(11.4) EF: External fault signal input and can be connected in series to emergency stop switch (N.C.). If you do not use EF, EF must connect with digital signal common 24 V (Source) terminal; otherwise, EF fault occurs.
(11.5) SG+, SG-: RS-485 serial communication terminals for PLC, and can also be used for uploading/downloading PLC programs.
(12) Output terminal block:
(12.1) YO-Y6: Multi-function output terminals, corresponding to relays that support two load capacity: $240 \mathrm{~V}_{\mathrm{AC}} 2 \mathrm{~A}$ and $30 \mathrm{~V}_{\mathrm{DC}} 2 \mathrm{~A}$.
(12.2) Y7-1, Y7-2: Multi-function output terminals (are activated simultaneously), corresponding to relays that support three load capacity: $380 \mathrm{~V}_{\mathrm{AC}} 1 \mathrm{~A}, 240 \mathrm{~V}_{\mathrm{AC}} 2 \mathrm{~A}$, and $30 \mathrm{~V}_{\mathrm{DC}} 2 \mathrm{~A}$.
(12.3) C0-C2: Common terminals. C0 is the common terminal for Y0-Y2; C1 is the common terminal for $\mathrm{Y} 3-\mathrm{Y} 5$; C 2 is the common terminal for Y 6 .
(12.4) C3-1 and C3-2: Common terminals. C3-1 is the common terminal for Y7-1; C3-2 is the common terminal for Y7-2.
(13) Relays:
(13.1) Y0-Y6: Removable, corresponding to multi-function output terminals Y0-Y6.
(13.2) Y 7 : Removable, corresponding to multi-function output terminals Y 7 ( $\mathrm{Y} 7-1$ and $\mathrm{Y} 7-2$ ).
(14) DIP switch AS2: Input contact X NPN (Sink) / PNP (Source) mode switch. Default is Source (PNP).
(15) DIP switch AS1: PLC Run / Stop switch. Switch between Run/Stop status. Default is RUN.
(16) VFD1 interface: RJ45 communication port. VFD1 RS-485 communication port is used for connection between VFD1 and PC (or communication panel PU08/PU08V).
(17) VFD2 interface: RJ45 communication port. VFD1 RS-485 communication port is used for connection between VFD1 and PC (or communication panel PU08/PU08V).
(18) PLC interface: USB communication port. Used for connection between PLC and PC for the controller.
(19) Input (contact $X$ ) terminal indicator: Corresponding indicators to $\mathrm{X} 0-\mathrm{X} 7, \mathrm{X} 10-\mathrm{X} 17$, and X 20 . DC POWER indicator.
(20) Left-side of PLC indicator: PLC Run indicator.
(21) Left-side of ERROR indicator: LTC fault indicator.
(22) Output terminal block: Y7-1, Y7-2, C3-1, and C3-2 terminals.
(23) Output terminal block: $\mathrm{Y} 0-\mathrm{Y} 6$ and $\mathrm{C} 0-\mathrm{C} 2$ terminals.
(24) Input terminal block: X0-X20, 24V, DCM, EF, SG+, and SG- terminals.
(25) WARNING: WARNING, and output terminals.


Panel Description:

| Indication | Description |
| :---: | :---: |
| POWER indicator | POWER indicator: Lights ON when LTC is powered on. |
| Potentiometer at <br> left-side | VFD1 potentiometer: Turn clockwise to increase the frequency command. Turn <br> counterclockwise to decrease the frequency command. |
| Potentiometer at <br> right-side | VFD2 potentiometer: Turn clockwise to increase the frequency command. Turn <br> counterclockwise to decrease the frequency command. |
| FWD indicator | Forward running indicator: lights ON when motor runs in a forward direction. |
| REV indicator | Reverse running indicator: lights ON when motor runs in a reverse direction. |
| Y0-Y7 | Output signal indicator: lights ON when corresponding relays output signals. |

## 8-2 Descriptions of Digital Keypad PU08/PU08V

LTC series uses digital keypad panel to serve as function displays and operations
Digital Keypad Panel PU08 (see figure below)


## Frequency Setting Potentiometer

 Use this knob for main frequency command inputMode Selection
Displays mode changes step by step for selection

## ENTER Key

1. Enters setting functions, such as forward (Frd), industry application functions (APP) etc.
2. Confirms parameter settings

## Left-shift/Down Key

Changes value or parameters / Long press MODE key to switch between left-shift key and down key

There are four indicators on the panel:

- STOP Stop indicator: lights ON when running stops.
- RUN Running indicator: lights ON when motor runs.
- FWD Forward running indicator: lights ON when motor runs in a forward direction.
- REV Reverse running indicator: lights ON when motor runs in a reverse direction.

Digital Keypad Panel PU08V (see figure below)

## Main Display Area

Displays frequency, current, voltage, user-defined, fault codes, etc.

## Status Display Area

Indicates drive's running status: Run, Stop, Forward/Reverse, Potentiometer enable/disable

## Up Key

Changes parameters or values

## RUN Key <br> VFD Run

## STOP/RESET Key

Makes the drive stop running and resets faults

## Mode Selection

Displays mode changes step by step for selection

## ENTER Key

1. Enters setting functions, such as forward (Frd), industry application functions (APP) etc.
2. Confirms parameter settings

## Left-shift/Down Key

Changes value or parameters / Long press MODE key to switch between left-shift key and down key

## Frequency Setting

## Potentiometer

Use this knob for main frequency command input

Descriptions of Keypad Functions

| Display | Descriptions |
| :---: | :---: |
| $\mathfrak{c}$ | Displays the present frequency setting for the drive. |
| $\mathfrak{c}$ | Displays the actual frequency output from drive to motor |
|  | Displays the user-defined value ( $\mathrm{U}=\mathrm{F} \times \mathrm{Pr}$.00.05) |
|  | Displays the load current. |
|  | Forward command |
| $\mathfrak{c}$ | Reverse command |
|  | Displays the count value. |
|  | Displays a parameter item. |
| $\underset{\substack{\text { ruwn: } \\ \text { Revo }}}{190}$ | Displays a parameter value. |
|  | Displays an external fault. |
|  | Displays "End" for approximately one second if the data has been accepted and automatically stored in the register. |
| R | Displays if the setting data is not accepted or data value exceeds the allowed range. |

## 8-3 Function of Digital Keypad PU08/PU08V

Keypad Operation Process


NOTE: In mode selections, press MODE to set the parameters.

Parameter Setting

| $9!$ | 9198 9 | 4999 | Erdor | $E r r$ |
| :---: | :---: | :---: | :---: | :---: |
| $\square)^{-5}$ ENTER | $\square_{\text {FINTER }}$ | $\square \sim \underbrace{}_{\text {ENTER }}$ | Success to set parameters. | Input data error |

NOTE: In the parameter setting mode, you can press MODE to return to mode selections.

## Data Change

## $\stackrel{\text { START }}{F 699} \Rightarrow F E 89 \Rightarrow F 69 \%$

$\nabla$ क्ना $\quad \nabla$ हो []

## Direction Setting



Reference Table for the Seven-segment Digital Keypad LED Display

| Number | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 7-segment display | 18 | 1 | 5 | 3 | 18 | $E$ | 8 | 9 | 18 | 1 |
| Letter | A | b | Cc | d | E | F | G | Hh | li | Jj |
| 7-segment display | 8 | 18 |  | E1 | $E$ | $E$ | 18 | 18 | $11^{-}$ | 10 |
| Letter | K | L | n | Oo | P | q | r | S | Tt | U |
| 7-segment display | $10$ | 1 | 17 |  | $8$ | 8 | 10 | E | $91$ | 10 |
| Letter | v | Y | Z |  |  |  |  |  |  |  |
| 7-segment display | 11 | 18 | = |  |  |  |  |  |  |  |

## 8-4 Fault Code Description of Digital Keypad PU08/PU08V

The following fault codes, descriptions and corrective actions are displayed when the operation command source is set as digital keypad.

| Fault Code | Fault Descriptions | Corrective Actions |
| :--- | :--- | :--- |


| Fault Descriptions | Corrective Actions |
| :--- | :--- | :--- |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :---: | :---: |
|  | 2. When changing the communication address 2002H bit0 $=1$, the drive stops output. |  |
| [F9 | EEPROM write error (cF1.0) Internal EEPROM cannot be programmed. | Return to the factory for repair. |
| EFi | EEPROM write error (cF1.1) Internal EEPROM cannot be programmed. | Return to the factory for repair. |
| 二F\% | EEPROM read error (cF2.0) Internal EEPROM cannot be read. | Press RESET key to reset all parameters to defaults. If this solution does not work, return to the factory for repair. |
| EFI | EEPROM read error (cF2.1) Internal EEPROM cannot be read. | Press RESET key to reset all parameters to defaults. If this solution does not work, return to the factory for repair. |
| EF9\% | Drive wiring detection fault (cF3.0) | U-phase error, return to the factory for repair. |
| E\% \% | Drive wiring detection fault (cF3.1) | V-phase error, return to the factory for repair. |
| 二F3.0 | Drive wiring detection fault (cF3.2) | W-phase error, return to the factory for repair. |
| -193 | Drive wiring detection fault (cF3.3) | DC bus wiring detection error, return to the factory for repair. |
| E\% 5.9 | Drive wiring detection fault (cF3.4) | Temperature sensor error, return to the factory for repair. |
| EF9 | Autoacceleration/deceleration failure (cFA) | Check if the drive capacity matches the motor's. Check if the regenerative energy is too high. Check for sudden load changes. |
| EE-- | Communication fault (cE--) | Check the RJ45 connection between the AC motor drive for loose wires and wiring to the correct pins. <br> Check if the communication format is correct. <br> See the table of explanation of exception codes in Parameter Group 09 Communication Parameters in Chapter 10 for detailed information. |
| EOEF | Software protection enabled (codE) | Password is locked. |
|  | Phase loss protection (PHL) | Check if the input power is three-phase. |


| Fault Code | Fault Descriptions | Corrective Actions |
| :--- | :--- | :--- |
| Multi-motor fault protection | Check if the motor wiring is normal. <br> (OPHL) | Check if the input voltage is within the rated AC <br> motor drive input voltage range, and check for <br> possible voltage spikes. <br> If the phase-in capacitor or active power supply <br> unit acts in the same power system, the input <br> voltage may surge abnormally in a short time. In <br> this case, install an AC reactor. <br> Verify the wiring of the control circuit and the <br> wiring/grounding of the main circuit to prevent <br> interference. <br> Check if other fault codes such as cF3.0-cF3.2 <br> occur after cycling the power. If yes, return to the <br> factory for repair. <br> The ground short circuit current charges the <br> capacitor in the main circuit through the power. <br> Check if there is ground fault on the motor cable, |
| wiring box and its internal terminals. Troubleshoot |  |  |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :---: | :---: |
|  |  | wiring box and its internal terminals. Troubleshoot the ground fault. <br> Verify the wiring of the control circuit and the wiring/grounding of the main circuit to prevent interference. |
| 010 | Over-voltage during deceleration (ovd) | Increase the setting values for Pr.01.10 and Pr. 01.12 (deceleration time). <br> Reduce the brake frequency. <br> Use S-curve acceleration/deceleration. <br> Use over-voltage stall prevention function (Pr.06.00). <br> Use Auto-acceleration and Auto-deceleration Setting (Pr.01.16) <br> Adjust the brake level (Pr.08.00). <br> Check if the input voltage is within the rated AC motor drive input voltage range, and check for possible voltage spikes. <br> If the phase-in capacitor or active power supply unit acts in the same power system, the input voltage may surge abnormally in a short time. In this case, install an AC reactor. <br> The ground short circuit current charges the capacitor in the main circuit through the power. Check if there is ground fault on the motor cable, wiring box and its internal terminals. Troubleshoot the ground fault. <br> Verify the wiring of the control circuit and the wiring/grounding of the main circuit to prevent interference. |
| 010 | Over-voltage during constant speed (ovn) | Impulsive change of the load: 1. Reduce the load <br> 2. Adjust the brake level (Pr.08.00). <br> Use over-voltage stall prevention function (Pr.06.00). <br> Check if the input voltage is within the rated AC motor drive input voltage range, and check for possible voltage spikes. <br> If the phase-in capacitor or active power supply unit acts in the same power system, the input voltage may surge abnormally in a short time. In this case, install an AC reactor. <br> The ground short circuit current charges the capacitor in the main circuit through the power. <br> Check if there is ground fault on the motor cable, |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :--- | :--- |
|  | wiring box and its internal terminals. Troubleshoot <br> the ground fault. <br> Verify the wiring of the control circuit and the <br> wiring/grounding of the main circuit to prevent <br> interference. |  |
| Belt conveyor drive (VFD1) | Stall function is enabled for VDF1 of LTC. If <br> materials are jammed and stall conditions are <br> set, fault code StAL is triggered. |  |

Reset
After the causes that may trigger fault codes are corrected, execute either of the following actions to clear the trip: 1. Press RESET key on the panel (as shown in the figure below); 2. Set the external terminal to "fault reset command" and set this terminal to be ON; 3. Send the fault reset command through communications. Make sure the RUN command or signal is OFF before executing RESET to prevent damage to the drive or personal injury due to immediate operation after reset.


Digital Keypad Panel

## Chapter 8 Descriptions of Drive and PLC Operations | LTC

## 8-5 VFDSoft Software

VFDSoft, a monitoring software for Detla drives, widely supports a variety of VFD series such as VFDEL, VFD-C2000, VFD-M300, and LTC. VFDSoft connects with VFD1 and VFD2 of LTC through either Delta USB/RS-485 converting module IFD6500 or RJ45 port to monitor VFD's working.

For detailed information, download VFDsoft User Manual (as shown in the figure below) at Delta website: https://downloadcenter.deltaww.com/en-
US/DownloadCenter? $\mathrm{v}=1 \& \mathrm{CID}=06 \&$ itemID=06010501\&dataType=8\&sort expr=cdate\&sort dir=DESC


## 8-6 WPLSoft Software

WPLSoft is a program editing software used under WINDOWS operating system in Delta's DVP Series PLC for VFD-C2000 and VFD-LTC. WPLSoft not only provides functions of PLC program planning and Windows editing (such as cut, paste, copy, multi-window, etc.), but also Chinese/English notes editing function and other useful functions like register editing \& setting, file reading \& saving, as well as points diagram monitoring and setting, and so on.

Minimum system requirements for installing WPLSoft software:

| Item | System Requirements |
| :---: | :--- |
| Operating System | Windows 95 / 98 / 2000 / NT / ME / XP / 10 |
| CPU | Pentium 90 above |
| Storage | 16MB above (32MB above recommended) |
| Drive | Disk space: 100 MB above at a minimum <br> An optical disc drive (for installing WPLSoft) |
| Display | Resolution: $800 \times 600,16$ colors above. It is recommended to set screen <br> width $\times$ height to $800 \times 600$ pixels. |
| Mouse | Mouse for general purposes or compatible with Windows |
| Printer | Printers with Windows drivers |
| RS-485 Port | At least one RS-485 port that can be connected with PLC |

Visit the download center at Delta's website to download and install the software WPLSoft:
After finishing installation, WPLSoft program will be created in the specified default sub-directory under "C:|Program Files\Delta Industrial Automation\WPLSoft x.xx".

For detailed information on how to use WPLSoft, see Chapter 13 PLC Function Applications.
[This page intentionally left blank]

## Chapter 9 Summary of Parameter Settings

00 User Parameters
01 Basic Parameters
02 Operation Method Parameters
03 Output Function Parameters
04 Input Function Parameters
05 Multi-step Speed Parameters
06 Protection Function Parameters
07 Motor Parameters
08 Special Parameters
09 Communication Parameters
10 Speed Feedback Control Parameters

This chapter provides a summary of parameter (Pr.) setting ranges and defaults. You can set, change, and reset parameters through the digital keypad PU08/PU08V.
NOTE:

1. $N$ : You can set this parameter during operation.
2. For more details on parameters, refer to Chapter 10 Descriptions of Parameter Settings.

## 00 User Parameters

| Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
| 00.00 | AC Motor Drive Identity Code | 5: 460V, 1HP <br> 7: 460V, 2HP <br> 9: 460V, 3HP <br> 11: 460V, 5.5HP | Read only |
| 00.01 | AC Motor Drive Rated Current Display | Display by models | Read only |
| 00.02 | Parameter Reset | 0: Can be read/written <br> 1: Read only <br> 8: Keypad locked <br> 9: Reset all parameter settings to defaults ( 50 Hz ) <br> 10: Reset all parameter settings to defaults $(60 \mathrm{~Hz})$ | 0 |
| 00.03 | Start-up Display | $0: F$ (frequency command) <br> 1: H (output frequency) <br> 2: A (output current) <br> 3: U (user-defined) see Pr. 00.04 | 0 |
| 00.04 | Content of Multi-function Display (User-Defined) | 0 : Display user-defined (U) <br> 1: Display external terminal counter value (c) <br> 2: Display the status of multi-function input terminal <br> (d) <br> 3: Displays DC bus voltage (u) (Unit: VDC) <br> 4: Display output voltage (E) (Unit: VAC) <br> 6: Display power factor angle ( n ) <br> 7: Display power (P) (Unit: kW) <br> 11: Display IGBT temperature (h) (Unit: ${ }^{\circ} \mathrm{C}$ ) | 0 |
| 00.05 | User-Defined Coefficient K | 0.00-160.00 | 1.00 |
| 00.06 | Firmware Version | Read only (Display by default) | Read only |
| 00.07 | Reserved | None | None |
| 00.08 | Parameter Protection <br> Password Input | 0-9999 <br> $0-2$ : the number of wrong password attempts | 0 |
| 00.09 | Parameter Protection <br> Password Setting | 0-9999 <br> 0: No password protection or password is entered correctly (Pr.00.08) | 0 |

Chapter 9 Summary of Parameter Settings | LTC

| Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
|  |  | 1: Password has been set |  |
| 00.10 | Speed Control Mode | 0: V/F control <br> 1: SVC control | 0 |
| 00.11 | Reserved | None | None |
| 00.12 | 50 Hz Base Voltage Selection | $\begin{aligned} & 0: 400 \mathrm{~V} \\ & 1: 380 \mathrm{~V} \end{aligned}$ | 1 |
| 00.13 | User-defined Value (Maximum Output Frequency) | 0-9999 | 0 |
| 00.14 | Decimal Place of User-defined Value | 0-3 | 0 |
| 00.15 | Machine Type ID | 0-5: Reserved <br> 6: Telescopic motor <br> 7: Belt conveyor motor | Read only |

## 01 Basic Parameters

|  | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
| 01.00 | Maximum Output Frequency | $50.00-400.00 \mathrm{~Hz}$ | 50.00 |
| 01.01 | Motor Rated Frequency | $0.10-400.00 \mathrm{~Hz}$ | 50.00 |
| 01.02 | Motor Rated Voltage | 460 V models: $0.1-510.0 \mathrm{~V}$ | 380.0 |
| 01.03 | Mid-point Frequency | $0.10-400.00 \mathrm{~Hz}$ | 1.50 |
| 01.04 | Mid-point Voltage | 460 V models: $0.1-510.0 \mathrm{~V}$ | 20.0 |
| 01.05 | Minimum Output Frequency | $0.10-400.00 \mathrm{~Hz}$ | 1.50 |
| 01.06 | Minimum Output Voltage | 460V models: $0.0-480.0 \mathrm{~V}$ | 20.0 |
| 01.07 | Output Frequency Upper Limit | 0.1-120.0\% | 110 |
| 01.08 | Output Frequency Lower Limit | 0.0-100.0\% | 0 |
| 01.09 | Acceleration Time 1 | $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$. | 3.0 |
| 01.10 | Deceleration Time 1 | 0.1-600.0 sec. / 0.01-600.00 sec. | $\begin{aligned} & \hline 3.0 \text { (VFD1) } \\ & 0.2 \text { (VFD2) } \end{aligned}$ |
| 01.11 | Acceleration Time 2 | $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$. | 3.0 |
| 01.12 | Deceleration Time 2 | $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$. | $\begin{aligned} & \hline 3.0 \text { (VFD1) } \\ & 0.2 \text { (VFD2) } \end{aligned}$ |
| 01.13 | JOG Acceleration Time | $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$. | 1.0 |
| 01.14 | JOG Deceleration Time | $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$. | 1.0 |
| 01.15 | JOG Frequency | $0.10-400.00 \mathrm{~Hz}$ | 6 |
| 01.16 | Auto-acceleration and Auto-deceleration Setting | 0 : Linear acceleration and linear deceleration <br> 1: Auto-acceleration and linear deceleration <br> 2: Linear acceleration and autodeceleration <br> 3: Auto-acceleration and autodeceleration (set by load) <br> 4: Auto-acceleration and autodeceleration (set by Acceleration/ Deceleration Time setting) | 0 |
| 01.17 | S-curve for Acceleration Begin Time | $0.0-10.0 \mathrm{sec}$. $/ 0.00-10.00 \mathrm{sec}$. | 0 |
| 01.18 | S-curve for Acceleration Arrival Time | $0.0-10.0 \mathrm{sec}$. $/ 0.00-10.00 \mathrm{sec}$. | 0 |
| 01.19 | Acceleration and Deceleration Time Unit Setting | 0 : Unit: 0.1 sec. <br> 1: Unit: 0.01 sec . | 0 |
| 01.20 | Simple Positioning Stop Frequency 0 |  | 0.00 |
| 01.21 | Simple Positioning Stop Frequency 1 |  | 5.00 |
| 01.22 | Simple Positioning Stop Frequency 2 | $0.00-400.00 \mathrm{~Hz}$ | 10.00 |
| 01.23 | Simple Positioning Stop Frequency 3 |  | 20.00 |
| 01.24 | Simple Positioning Stop Frequency 4 |  | 30.00 |


|  | Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: | :---: |
| N | 01.25 | Simple Positioning Stop Frequency 5 |  | 40.00 |
| $N$ | 01.26 | Simple Positioning Stop Frequency 6 |  | 50.00 |
| $N$ | 01.27 | Simple Positioning Stop Frequency 7 |  | 60.00 |
| $N$ | 01.28 | Delay Time of Simple Positioning Stop 0 | 0.00-400.00 sec. | 0.00 |
| $N$ | 01.29 | Delay Time of Simple Positioning Stop 1 |  | 0.00 |
| N | 01.30 | Delay Time of Simple Positioning Stop 2 |  | 0.00 |
| $N$ | 01.31 | Delay Time of Simple Positioning Stop 3 |  | 0.00 |
| $N$ | 01.32 | Delay Time of Simple Positioning Stop 4 |  | 0.00 |
| $N$ | 01.33 | Delay Time of Simple Positioning Stop 5 |  | 0.00 |
| N | 01.34 | Delay Time of Simple Positioning Stop 6 |  | 0.00 |
| $N$ | 01.35 | Delay Time of Simple Positioning Stop 7 |  | 0.00 |
| N | 01.36 | Energy Restriction Mode (Only for V/F Control Mode) | 0: Disabled <br> 1: OFD (Over Flux Deceleration) mode <br> 2: TEC (Traction Energy Control) mode | 0 |
| $N$ | 01.37 | TEC Current Restriction | Reserved | None |
| N | 01.38 | TEC Voltage Compensation Gain | 0.01-655.35 | 0.5 |
| N | 01.39 | TEC Voltage Compensation Filter | 0.01-655.35 sec. | 0.05 |
| $N$ | 01.40 | OFD Voltage Compensation Gain | 0.01-655.35 | 0.05 |

## 02 Operation Method Parameters

|  | Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: | :---: |
| $N$ | 02.00 | First Master Frequency Command Source | 3: RS-485 communication (RJ45 for VFD1 and VFD2) <br> 4: Digital keypad potentiometer | 4 |
| $N$ | 02.01 | Operation Command Source | 2: External terminals <br> 4: RS-485 communication (RJ45 for VFD1 and VFD2) | 2 |
|  | 02.02 | Motor Stop Method | 0: STOP: ramp to stop; EF: coast to stop <br> 1: STOP: coast to stop; EF: coast to stop <br> 2: STOP: ramp to stop; EF: ramp to stop <br> 3: STOP: coast to stop; EF: ramp to stop <br> 4: Simple positioning stop; EF: coast to stop | 0 |
|  | 02.03 | PWM Carrier Frequency Selection | 2-12 kHz | 8 |
|  | 02.04 | Motor Direction Control | 0: Enable forward/reverse <br> 1: Disable reverse <br> 2: Disable forward | 0 |
|  | 02.05 | Drive's Operation Control when Power is ON and RUN Command Source is Changed (External Terminals Only) | 1: Does not run when power is ON, and remains current operation status when RUN command is changed. <br> 3: Does not run when power is ON , and follows new RUN command when RUN command is changed. | 1 |
|  | 02.06 | Reserved | None | None |
|  | 02.07 | Reserved | None | None |
|  | 02.08 | Reserved | None | None |
| $N$ | 02.09 | Second Master Frequency Command Source | 3: RS-485 communication <br> 4: Control by potentiometer on digital keypad | 4 |
| $N$ | 02.10 | Combination of the First and Second Master Frequency Commands | 0 : First master frequency command only <br> 1: First master frequency command + second master frequency command <br> 2: First master frequency command - second master frequency command | 0 |
|  | 02.11 | Reserved | None | None |
| $N$ | 02.12 | Communication Frequency Command | $0.00-400.00 \mathrm{~Hz}$ | 50.00 |
|  | 02.13 | Frequency Command Saving <br> Selection | 0 : Saves the frequency before power is off <br> 2: Only saves the communication frequency command before power is off | 0 |
|  | 02.14 | Initial Frequency Command Mode at Stop | 0: Use current Frequency command <br> 1: Use zero Frequency command <br> 2: Refer to Pr. 02.15 to set up | 0 |
|  | 02.15 | Initial Frequency Command | $0.00-400.00 \mathrm{~Hz}$ | 60.00 |


| Pr. | Parameter Name | Setting Range | Default |
| :---: | :--- | :--- | :---: |
|  | Setting at Stop | bit0=1: The Master Frequency Command Source is the <br> First Master Frequency Source (Pr.02.00). <br> bit1=1: The Master Frequency Command Source is the <br> 02.16 | Master Frequency Command <br> Source Display |
| 02.17 | Second Master Frequency Source (Pr.02.09). <br> bit2=1: The Master Frequency Command Source is the <br> external multi-function input terminal | Read <br> only |  |
| Display | bit1=1: Operation Command source is the RS-485 <br> communication <br> bt2=1: Operation Command Source is the external <br> terminal (MI1) <br> bit3=1: Operation Command Source is the external <br> multi-function input terminals | Read |  |
| 02.18 | User-defined Value 2 Setting | $0-P r .00 .13$ | only |

## 03 Output Function Parameters

| Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
| 03.00 | Multi-function Output (MO1) | 0 : No function <br> 1: Indication during RUN <br> 2: Indication of frequency reached <br> 3: Zero speed <br> 4: Over-torque detection <br> 5: Base Block (B.B.) indication <br> 6: Low-voltage detection <br> 7: Drive operation mode <br> 8: Fault indication <br> 9: Desired frequency reached <br> 10: Terminal count value reached <br> 11: Preliminary count value reached <br> 12: Over-voltage stall prevention <br> 13: Over-current stall prevention <br> 14: IGBT overheating warning (ON: $110^{\circ} \mathrm{C}$; OFF: <br> $105^{\circ} \mathrm{C}$ ) <br> 15: Over-voltage <br> 16: Reserved <br> 17: Forward running (FWD) command <br> 18: Reverse running (REV) command <br> 19: Zero speed (including STOP) <br> 20: Warning indication <br> 21: Mechanical brake control (used with Pr.03.11, Pr.03.12) <br> 22: Drive is ready <br> 23-25: Reserved <br> 26: Belt conveyor motor stall failure (activates when belt conveyor motor stall occurs) | 8 |
| 03.01 | Multi-function Output (MO2) |  |  |
|  |  |  | VFD1: 26 <br> VFD2: 6 |
| 03.02 | Desired Frequency Reached | $0.00-400.00 \mathrm{~Hz}$ | 0.0 |
| 03.03 | Reserved | None | None |
| 03.04 | Reserved | None | None |
| 03.05 | Terminal Count Value | 0-9999 | 0 |
| 03.06 | Preliminary Count Value | 0-9999 | 0 |
| 03.07 | EF Activates when the Terminal Count Value Reached | 0 : Terminal count value reached, no EF displays <br> 1: Terminal count value reached, EF is triggered | 0 |
| 03.08 | Fan Cooling Control | 0 : Fan is always ON <br> 1: Fan is OFF after the AC motor drive stops for one minute | 0 |


| Pr. | Parameter Name | Setting Range | Default |
| :---: | :--- | :--- | :---: |
|  |  | $\begin{array}{l}\text { 2: Fan is ON when the AC motor drive runs; fan is } \\ \text { OFF when the AC motor drive stops } \\ \text { 3: Fan is ON after detecting heat sink temperature is } \\ \left.\text { reached (ON: } 60^{\circ} \mathrm{C} ; \text { OFF: } 40^{\circ} \mathrm{C}\right) \\ \text { 4: Fan is ON when the AC motor drive runs; fan is } \\ \text { OFF when the AC motor drive stops. Fan is } \\ \text { standby when at zero speed. }\end{array}$ |  |
| 03.09 | Reserved | None | None |
| 03.10 | Reserved | $\begin{array}{l}\text { None } \\ \text { Frequency }\end{array}$ | $0.00-20.00 \mathrm{~Hz}$ |$]$| None |
| :---: |
| 03.11 |
| Mechanical Brake Engage <br> Frequency |
| 03.13 |
| Display the Status of Multi- <br> function Output Terminal |
| 03.14 |
| Reserved |

## 04 Input Function Parameters



Chapter 9 Summary of Parameter Settings | LTC

| Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
|  |  | 23: Simple positioning stop by forward limit <br> 24: Simple positioning stop by reverse limit |  |
| 04.09 | Multi-function Input Contact Selection (N.O./N.C.) | 0-15 | 0 |
| 04.10 | Digital Terminal Input Response Time | 1-20 (*2 ms) | 1 |
| 04.11 | Reserved | None | None |
| 04.12 | Reserved | None | None |
| 04.13 | Reserved | None | None |
| 04.14 | Reserved | None | None |
| 04.15 | Reserved | None | None |
| 04.16 | Reserved | None | None |
| 04.17 | Reserved | None | None |
| 04.18 | Reserved | None | None |
| 04.19 | MI Terminal Control Mode Selection | 0 : Mode 1, single-wire start-up/stop <br> 1: Mode 2, two-wire/three-wire start-up/stop | 1 |
| 04.20 | Reserved | None | None |
| 04.21 | Reserved | None | None |
| 04.22 | Reserved | None | None |
| 04.23 | Reserved | None | None |
| 04.24 | Reserved | None | None |
| 04.25 | Reserved | None | None |
| 04.26 | Display the Status of Multifunction Input Terminal | See parameter descriptions below | Read <br> only |
| 04.27 | Internal/External Multi-function Input Terminals Selection | 0-15 | 0 |
| 04.28 | Internal Multi-function Input Terminal Status | 0-15 | 0 |
| 04.29 | Reserved | None | None |
| 04.30 | Reserved | None | None |
| 04.31 | Reserved | None | None |
| 04.32 | Reserved | None | None |
| 04.33 | Reserved | None | None |
| 04.34 | Reserved | None | None |

## 05 Multi-step Speed Parameters

|  | Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: | :---: |
| $N$ | 05.00 | Reserved | None | None |
| $N$ | 05.01 | Reserved | None | None |
| $N$ | 05.02 | Reserved | None | None |
| $N$ | 05.03 | Reserved | None | None |
| $N$ | 05.04 | Reserved | None | None |
| $N$ | 05.05 | Reserved | None | None |
| $N$ | 05.06 | Reserved | None | None |

## 06 Protection Function Parameters



| Pr | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
|  |  | 13: Reserved <br> 14: Phase loss protection (PHL) <br> 15: Reserved <br> 16: Auto-acceleration/deceleration failure (cFA) <br> 17: Software protection enabled (codE) <br> 18: EEPROM write error (cF1.0) <br> 19: EEPROM read error (cF2.0) <br> 20: Hardware protection fault 1 (HPF1) <br> 21: Hardware protection fault 1 (HPF2) <br> 22: Reserved <br> 23: Hardware protection fault 1 (HPF4) <br> 24: Drive wiring detection fault (cF3.0) <br> 25: Drive wiring detection fault (cF3.1) <br> 26: Drive wiring detection fault (cF3.2) <br> 27: Drive wiring detection fault (cF3.3) <br> 28: Drive wiring detection fault (cF3.4) <br> 29: Reserved <br> 30: Reserved <br> 31: Reserved <br> 32: Reserved <br> 33: Reserved <br> 34: Reserved <br> 35: Reserved <br> 36: Reserved <br> 37: Multi-motor fault protection (oPHL) <br> 38: IGBT temperature PTC OFF (TH1o) <br> 39: Reserved <br> 40: Reserved <br> 41: Belt conveyor drive (VFD1) stall failure (StAL) <br> 42: Over-voltage at stop (ovS) <br> 43: Over-voltage during acceleration (ovA) <br> 44: Over-voltage during deceleration (ovd) <br> 45: Over-voltage during constant speed (ovn) |  |
| 06.13 | Motor Phase Loss Detection Time | $\begin{aligned} & \text { 0: Disabled } \\ & 0-60 \mathrm{sec} \text {. } \end{aligned}$ | 0 |
| 06.14 | Current Detection for Motor Phase Loss | 10-100\% | 30 |

## 07 Motor Parameters

|  | Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: | :---: |
| $N$ | 07.00 | Motor Rated Current | 23\% FLA-120\% FLA | 100 |
| $N$ | 07.01 | Motor No-load Current | 0\% FLA-99\% FLA | 40 |
| $N$ | 07.02 | Torque Compensation | 0.0-10.0 | 0.0 |
| $N$ | 07.03 | Slip Compensation Gain | 0.00-10.00 | 0.00 |
| $N$ | 07.04 | Motor Parameter Auto-tuning | 0: Disabled <br> 1: Auto-tuning R1 (motor does not run) <br> 2: Auto-tuning R1 + no-load current (motor runs) | 0 |
|  | 07.05 | Motor Line-to-line Resistance R1 (Motor 0) | 0-65535 m | 0 |
|  | 07.06 | Motor Rated Slip (Motor 0) | $0.00-20.00 \mathrm{~Hz}$ | 3.00 |
|  | 07.07 | Slip Compensation Limit | 0-250\% | 200 |
|  | 07.08 | Torque Compensation Low Pass Filter Time | 0.01-10.00 sec. | 0.10 |
|  | 07.09 | Slip Compensation Low Pass Filter Time | 0.05-10.00 sec. | 0.20 |
|  | 07.10 | Accumulated Motor Operation Time | 00-1439 min. | 0 |
|  | 07.11 | Accumulated Motor Operation Time | 00-65535 days | 0 |
|  | 07.12 | Reserved | None | None |
|  | 07.13 | Reserved | None | None |
|  | 07.14 | Reserved | None | None |
|  | 07.15 | Reserved | None | None |
|  | 07.16 | Reserved | None | None |
|  | 07.17 | Reserved | None | None |

## 08 Special Parameters

|  | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: |
|  | DC Brake Current Level | 0-100\% | 0 |
|  | DC Brake Time At Start-up | 0.0-60.0 sec. | 0.0 |
|  | DC Brake Time At STOP | $0.0-60.0 \mathrm{sec}$. | 0.0 |
|  | DC Brake Frequency at STOP | $0.00-400.00 \mathrm{~Hz}$ | 0.00 |
|  | Restart after Momentary Power Loss | 0 : Operation stops after momentary power loss <br> 1: Operation continues after momentary power loss, speed tracking starts with the last frequency. <br> 2: Operation continues after momentary power loss, speed tracking starts with the minimum frequency. | 0 |
|  | Maximum Allowable Power Loss Time | 0.1-20.0 sec. | 2.0 |
|  | Base Block Speed Tracking | 0 : Disabled <br> 1: Speed tracking starts with the last frequency <br> 2: Speed tracking starts with the minimum output frequency (Pr.01.05) | 1 |
|  | Base Block Time for Speed Tracking | $0.1-5.0 \mathrm{sec}$. | 0.5 |
|  | Current Limit for Speed Tracking | 30-200\% | 150 |
|  | Skip Frequency 1 (Upper Limit) | $0.00-400.0 \mathrm{~Hz}$ | 0.00 |
|  | Skip Frequency 1 (Lower Limit) | $0.00-400.0 \mathrm{~Hz}$ | 0.00 |
|  | Skip Frequency 2 (Upper Limit) | $0.00-400.0 \mathrm{~Hz}$ | 0.00 |
|  | Skip Frequency 2 (Lower Limit) | $0.00-400.0 \mathrm{~Hz}$ | 0.00 |
|  | Skip Frequency 3 (Upper Limit) | $0.00-400.0 \mathrm{~Hz}$ | 0.00 |
|  | Skip Frequency 3 (Lower Limit) | $0.00-400.0 \mathrm{~Hz}$ | 0.00 |
|  | Number of Times of Restart after Fault | 0-10 | 0 |
|  | Auto-restart Interval of Fault | 0.1-6000 sec. | 60.0 |
|  | Automatic Energy-saving | 0 : Automatic energy-saving operation is disabled <br> 1: Automatic energy-saving operation is enabled | 0 |
|  | Automatic Voltage Regulation (AVR) | 0 : AVR function is enabled <br> 1: AVR function is disabled <br> 2: AVR function is disabled during deceleration <br> 3: AVR function is disabled at stop | 0 |
|  | Reserved | None | None |
|  | Vibration Suppression | 0.0-5.0 | 0 |


| Pr. | Parameter Name | Setting Range | Default |
| :---: | :--- | :--- | :---: |
| 08.21 | Reserved | None | None |
| 08.22 | Reserved | None | None |
| 08.23 | Reserved | None | None |
| 08.24 | Reserved | None | None |
| 08.25 | Reserved | None | None |

## 09 Communication Parameters

|  | Pr. | Parameter Name | Setting Range | Default |
| :---: | :---: | :---: | :---: | :---: |
| $\checkmark$ | 09.00 | Communication Address | 1-254 | 1 |
| $N$ | 09.01 | Transmission Speed | 0: Baud rate 4800 bps <br> 1: Baud rate 9600 bps <br> 2: Baud rate 19200 bps | 2 |
| $N$ | 09.02 | Transmission Fault Treatment | 0 : Warn and continue operation <br> 1: Fault and ramp to stop <br> 2: Fault and coast to stop <br> 3: No warning, no fault and continue operation | 3 |
| $N$ | 09.03 | Communication Time-out Detection | 0.0: No function <br> $0.1-120.0 \mathrm{sec}$. | 0 |
| $N$ | 09.04 | Communication Protocol | $0: 7, \mathrm{~N}, 2$ for ASCII <br> 1: 7, E, 1 for ASCII <br> 2: 7, O, 1 for ASCII <br> 3: 8, N, 2 for RTU <br> 4: 8, E, 1 for RTU <br> 5: 8, O, 1 for RTU <br> 6: 8, N, 1 for RTU <br> 7: 8, E, 2 for RTU <br> 8: 8, O, 2 for RTU <br> 9: 7, N, 1 for ASCII <br> 10: 7, E, 2 for ASCII <br> 11: 7, O, 2 for ASCII | 3 |
|  | 09.05 | Reserved | None | None |
|  | 09.06 | Reserved | None | None |
| $N$ | 09.07 | Communication Response Delay Time | 0.0-200.0 ms (One unit: 2 ms ) | 1 |
|  | 09.08 | Reserved | None | None |

## 10 Speed Feedback Control Parameters

| Pr. | Parameter Name |  | Setting Range |
| :---: | :--- | :--- | :---: | Default

[This page intentionally left blank]

## Chapter 10 Descriptions of Parameter Settings

## 00 User Parameters

### 00.00 AC Motor Drive Identity Code

$N$ You can set this parameter during operation.

Default: Read only
Settings Read only

### 00.01 AC Motor Drive Rated Current Display

Default: Read only
Settings Read only
10 Pr. 00.00 displays the AC motor drive identity code. Use the following specification table to check if Pr. 00.01 setting is the rated current of the AC motor drive. Pr. 00.01 corresponds to the identity code of Pr.00.00.
11 Pr.00.01 indicates the rated output current of the AC motor drive. Use this parameter to check if the displayed values matches the drive used.

Specification table of drive capacity, identity code, and rated current:

| Frame | A |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Models | VFD2207LTC43A |  | VFD4015LTC43A |  |
| Description | Telescopic | Belt conveyor | Telescopic | Belt conveyor |
| Power (kW) | 0.75 | 2.2 | 1.5 | 4.0 |
| Power (HP) | 1 | 3 | 2 | 5.5 |
| Identity Code | 5 | 9 | 7 | 11 |
| Rated Current (A) | 2.5 | 5.5 | 4.2 | 9.0 |

### 00.02 Parameter Reset

Default: 0

| Settings | 0: Can be read/written |
| ---: | :--- |
|  | 1: Read only |
| 8: Keypad locked |  |
| 7: Reset CANopen Slave index |  |
| 9: Reset all parameter settings to defaults $(50 \mathrm{~Hz})$ |  |
| 10: Reset all parameter settings to defaults $(60 \mathrm{~Hz})$ |  |

ITd 9 or 10: Resets all parameters to defaults when parameters are abnormal due to fault or incorrect tuning.
[1] 9: The base voltage of 50 Hz power system varies with Pr. 00.12 settings.
[l] 1: All parameters cannot be changed and Err will be displayed if any values are entered. To write any parameters, set Pr.00.02= 0 .
11 When Pr.00.02=8, keypad is locked and can be unlocked by pressing ENTER key continuously for five seconds.

## 00．03 Start－up Display

Default： 0
Settings 0：$F$（frequency command）
1：H（output frequency）
2：A（output current）
3：U（user－defined）see Pr． 00.04
ㅁ．Determines the start－up display page after power is applied to the drive．The user－defined contents display according to the Pr． 00.04 settings．
$\square$ When power is applied，the drive starts self－detecting and Pon is displayed．After flashing about five seconds，the default start－up display shows．

## 00．04 Content of Multi－function Display（User－Defined）

Default： 3

| Settings | 0 ：Display user－defined（U） |
| :---: | :---: |
|  | 1：Display external terminal counter value（c） |
|  | 2：Display the status of multi－function input terminal（d） |
|  | 3：Displays DC bus voltage（u）（Unit：VDC） |
|  | 4：Display output voltage（E）（Unit： $\mathrm{V}_{\mathrm{AC}}$ ） |
|  | 6：Display power factor angle（ n ） |
|  | 7：Display power（P）（Unit：kW） |
|  | 11：Display IGBT temperature（h）（Unit：${ }^{\circ} \mathrm{C}$ ） |

B 2 $2 \sharp$
$0 \quad: 5$
$\because 3: 7$
Eट2円
n9月路
P日月日
6 3 An

Used to select the display contents as required only when Pr．00．03＝3．

## 00．05 User－Defined Coefficient K

Unit： 0.1
Default： 1.00
Settings 0．00－160．00
Determines the multiplying factor $(\mathrm{K})$ for the user－defined unit．The display value is calculated as follows：User－defined unit $(\mathrm{U})=$ Output frequency $(\mathrm{H}) \times$ User－defined Coefficient $(\mathrm{K})($ Pr．00．05）

## 00．06 Software Version

Default：Read only
Settings Read only

## 00．07 Reserved

# 00.08 Parameter Protection Password Input 

Default: 0
Settings 0-9999
[1] Displayed value 0-2 is the number of wrong password attempts.
[1] Enter the password that is set in Pr.00.09. Enter the correct password here to enable changing parameter settings. You are limited to a maximum of three attempts. After three consecutive failed attempts, a blinking "codE" appears. You must restart the AC motor drive before you can try again to enter the correct password. To avoid problems in the future, be sure to write down the password after you set this parameter. If you forget the password, return the unit to Delta to unlock the password.

### 00.09 Parameter Protection Password Setting

Default: 0
Settings 0-99990: No password protection or password is entered correctly (Pr.00.08). 1: Password has been set.

凹 This parameter is for setting the password protection. Password can be set directly the first time. After you set the password, the value of Pr. 00.09 is 1 , which means password protection is activated. However, if the value of Pr. 00.09 is 0 , the password protection is deactivated, which means you can change any of the parameter settings (including resetting the parameter protection password for Pr.00.09). When Pr. 00.09 is 1 and if you want to change any of the parameter settings, you must enter the correct password in Pr. 00.08 to deactivate the password, and this would make Pr. 00.09 become 0 .

## NOTE:

If you set this parameter to 0 again, the password protection function is deactivated. No password protection is enabled for future rebooting. Otherwise, as long as you set a password other than 0 , this password protection is always reactivated after you reboot the motor drive. If you want to change any of the parameter settings after rebooting the motor drive, enter the correct password in Pr. 00.08 to deactivate the password.This parameter is used to prevent personnel from setting other parameters by accident.
Method 1: Enter the original password into Pr. 00.09 again (or you can enter a new password if you want; be sure to record it).
Method 2: Reboot the drive to restore the password protection function.
Method 3: Enter any value that is not the password into Pr.00.08. (Pr.00.08 displays End regardless of whether the password is entered correctly.)

Password Decode Flow Chart


### 00.10 Speed Control Mode

Default: 0
Settings 0: V/F control
1: SVC control
$10]$ Determines the control mode of the AC motor drive.
M/F Control: (Voltage/Frequency Control)
The V/F control is a constant value control mode. In this control mode, frequency decreasing and magnetic field increasing are under control. But as the frequency decreases, a problem rises: the insufficiency of motor's torque in a weaken low frequency magnetic field. To solve this problem, set Pr.07.02 Torque Compensation to compensate torque then to have the best operating performance.
Common applications are water pumps, conveyors, compressors and treadmills.
[1] Vector Control:
The vector control mode can eliminate the relationship between the field current vector and the armature flux, and auto-tune the torque compensation and slip compensation to increase the dynamic response of the motor drive.
[1]
Common applications are textile equipment, printing equipment, crane equipment and drilling machinery.Related parameter: Pr.07.02 Torque Compensation

### 00.11 Reserved

### 00.1250 Hz Base Voltage Selection

$$
\text { Default: } 1
$$

Settings 0:400V
1: 380V
Determines the base voltage when the drive resets to 50 Hz .

### 00.13 User-defined Value

Default: 0
Settings 0-9999
Corresponds to Maximum Output Frequency (Pr.01.00).When Pr.00.13 is not set to 0 , " $F$ " automatically disappears in frequency mode and the rightmost digit blinks. The ranges for many functions display according to Pr.00.13, including the UP/DOWN key on the keypad, multi-step speed function, and JOG function.When Pr.00.13 is not set to 0 , and the frequency source is from communications, use Pr. 02.18 to change the frequency command instead of using communication address 2001H.

### 00.14 Decimal Place of User-defined Value

## Default: 0

$$
\text { Settings } 0-3
$$

Sets the place of decimal point for Pr.00.13.
Example: If you want to set the user-defined value to 10.0, set Pr.00.13 to 100 and $\operatorname{Pr} .00 .14$ to 1 .

### 00.15 Machine Type ID

Default: Read only
Settings 0-5: Reserved
6: Telescopic motor
7: Belt conveyor motor
[This page intentionally left blank]

## 01 Basic Parameters

### 01.00 Maximum Output Frequency

$\approx$ You can set this parameter during operation.

Default: 50.00
Settings $\quad 50.00-400.00 \mathrm{~Hz}$

### 01.01 Motor Rated Frequency

Default: 50.00
Settings $\quad 0.10-400.00 \mathrm{~Hz}$
10 Set this value according to the rated motor frequency as indicated on the motor nameplate.

### 01.02 Motor Rated Voltage

Default: 380.0
Settings 460 V models: $0.1-510.0 \mathrm{~V}$
[1] For 460 V models, the default is 380.0 V .
1 Set the maximum output voltage. The setting must be smaller than or equal to the rated motor voltage as indicated on the motor nameplate.

### 01.03 Mid-point Frequency

Default: 1.50
Settings $\quad 0.10-400.00 \mathrm{~Hz}$
Sets the mid-point frequency of any V/F curve. This setting determines the V/F ratio between the Minimum Frequency and the Mid-point Frequency.

### 01.04 Mid-point Voltage

Default: 20.0
Settings 460 V models: $0.1-510.0 \mathrm{~V}$
[10) For 460 V models, the default is 20.0 V .
$\mathbb{L}$ Sets the mid-point voltage of any V/F curve. This setting determines the V/F ratio between the Minimum Frequency and the Mid-point Frequency.

### 01.05 Minimum Output Frequency

Default: 1.50
Settings $\quad 0.10-400.00 \mathrm{~Hz}$
Sets the minimum output frequency of the AC motor drive in V/F curve.

### 01.06 Minimum Output Voltage

Default: 20.0
Settings 460 V models: $0.0-480.0 \mathrm{~V}$
$\square$ For 460 V models, the default is 20.0 V .
[1] Sets the minimum output voltage of the AC motor drive in V/F curve
$\square$ The settings for Pr.01.01-Pr.01.06 must meet the condition of Pr. $01.02 \geq$ Pr. $01.04 \geq$ Pr.01.06; Pr.01.01 $\geq$ Pr. $01.03 \geq$ Pr.01.05.

### 01.07 Output Frequency Upper Limit

Default: 110
Settings 0.1-120\%
$10]$ This parameter must be equal to or greater than the Output Frequency Lower Limit (Pr.01.08). The Maximum Output Frequency (Pr.01.00) is equal to $100 \%$.
(1) The Output Frequency Upper Limit value $=($ Pr. $01.00 \times$ Pr.01.07 $) / 100$


## V/F Curve

### 01.08 Output Frequency Lower Limit

Default: 0
Settings 0.0-100\%
凹 The Output Frequency Lower Limit value $=($ Pr. $01.00 \times$ Pr.01.08 $) / 100$
(1) Use the output frequency upper and lower limit settings to prevent operator misuse, overheating caused by the motor's operating at a too low frequency, or mechanical wear due to a too high speed.
(1) If the output frequency upper limit calculated is 50 Hz and the frequency setting is 60 Hz , the maximum output frequency is 50 Hz .
[1f the output frequency lower limit calculated is 10 Hz and the minimum output frequency setting (Pr.01.05) is 1.5 Hz , then the drive operates at 10 Hz when the Frequency command is higher than Pr. 01.05 but lower than 10 Hz . If the Frequency command is lower than the minimum output frequency (Pr.01.05), the drive is in ready status without output.

### 01.09 Acceleration Time 1

Default: 3.0
Settings $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$.
[a] You can switch the acceleration/deceleration time 1 or 2 by setting the external terminals MI2MI4 to 7 .

### 01.10 Deceleration Time 1

Default:

$$
3.0 \text { (VFD1) / } 0.2 \text { (VFD2) }
$$

Settings $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$.
You can switch the acceleration/deceleration time 1 or 2 by setting the external terminals MI2MI4 to 7 .

### 01.11 Acceleration Time 2

Default: 3.0
Settings $\quad 0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$.
You can switch the acceleration/deceleration time 1 or 2 by setting the external terminals MI2MI4 to 7 .

### 01.12 Deceleration Time 2

Default:
3.0 (VFD1) / 0.2 (VFD2)

Settings $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$.
You can switch the acceleration/deceleration time 1 or 2 by setting the external terminals MI2MI4 to 7 .

### 01.13 JOG Acceleration Time

Default: 1.0
Settings $\quad 0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$.

### 01.14 JOG Deceleration Time

Default: 1.0
Settings $0.1-600.0 \mathrm{sec} . / 0.01-600.00 \mathrm{sec}$.

### 01.15 JOG Frequency

Default: 6.00
Settings $\quad 0.10-400.00 \mathrm{~Hz}$
Use only external terminal setting MI2, MI3 or MI4 to 8 (JOG). When the JOG command is ON, the AC motor drive accelerates from the Minimum Output Frequency (Pr.01.05) to the Jog Frequency (Pr.01.15). When the JOG command is OFF, the AC motor drive decelerates from the JOG frequency to stop. The acceleration/deceleration time is set by the JOG Acceleration/Deceleration Time (Pr.01.13, Pr.01.14).
$\square$ The drive must be stopped before using the JOG command. During Jog operation, other operation commands are not accepted, except FORWARD/REVERSE commands.


The Definition of JOG Accel./Decel. Time

### 01.16 Auto-acceleration and Auto-deceleration Setting

Default: 0

| Settings | $0:$ Linear acceleration and linear deceleration |
| :--- | :--- |
|  | 1: Auto-acceleration and linear deceleration |
| 2: Linear acceleration and auto-deceleration |  |
| 3: Auto-acceleration and auto-deceleration (set by load) |  |
|  | Auto-acceleration and auto-deceleration (set by Acceleration/Deceleration |
|  | Time setting) |

1 With auto-acceleration and auto-deceleration, it is possible to reduce vibration and shocks during starting and stopping the load. During auto-acceleration, the torque is automatically measured and the drive accelerates to the set frequency with the shortest acceleration time and the smoothest starting current. During deceleration, the drive automatically determines the loaded regenerative energy to steadily and smoothly stop the motor in the shortest deceleration time. When this parameter is set to 4 , the actual acceleration/deceleration time refers to acceleration/deceleration time settings in Pr.01.09-Pr.01.12. Thus the actual acceleration/deceleration time is equal to or greater than acceleration/deceleration time settings.Auto-acceleration and auto-deceleration makes the complicated processes of tuning unnecessary. It does not stall during acceleration and does not need a brake resistor during deceleration to stop. It can also improve operation efficiency and save energy.

### 01.17 S-curve for Acceleration Begin Time

Default: 0.0
Settings 0: 0.0-10.0 sec. $/ 0.00-10.00 \mathrm{sec}$.

### 01.18 S-curve for Acceleration Arrival Time

Default: 0.0
Settings $0: 0.0-10.0 \mathrm{sec} . / 0.00-10.00 \mathrm{sec}$.
$\llbracket$ Using an S-curve gives the smoothest transition between speed changes. Pr.01.17-Pr.01.18 adjusts the acceleration and deceleration S-curve. When enabled, the drive produces a different acceleration and deceleration curve according to the acceleration and deceleration time. When setting this parameter to 0.0 , it is linear acceleration and linear deceleration.
$\square$ The following diagram shows that the original setting of the Acceleration and Deceleration Time is only for reference when you enable the S-curve. The actual acceleration and deceleration time depends on the selected S-curve ( 0.1 to 10.0). Pr.01.17 must be smaller than Pr. 01.09 or Pr.01.11; Pr.01.18 must be smaller than Pr.01.10 or Pr.01.12. Otherwise, the S-curve is invalid.
The total acceleration time $=$ Pr. $01.09+$ Pr. 01.17 or Pr. $01.11+$ Pr.01.17
The total deceleration time $=$ Pr.01.10 + Pr.01.18 or Pr.01.12 + Pr.01.18


## Acceleration/deceleration Characteristics

### 01.19 Acceleration and Deceleration Time Unit Setting

Default: 0
Settings 0: Unit: 0.1 sec .
1: Unit: 0.01 sec .
The acceleration time determines the time required for the AC motor drive to ramp from 0.00 Hz to the Maximum Output Frequency (Pr.01.00). The deceleration time determines the time required for the AC motor drive to decelerate from the Maximum Output Frequency down to 0 Hz .Select the Acceleration / Deceleration Time 1, 2 with the multi-function input terminal settings. The defaults are Acceleration Time 1 and Deceleration Time 1.
[1]
Pr.01.19 settings change the settings of acceleration and deceleration time unit, further changing the setting range of acceleration and deceleration time.

01.20 Simple Positioning Stop Frequency 0

Default: 0.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
01.21 Simple Positioning Stop Frequency 1

Default: 5.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
01.22 Simple Positioning Stop Frequency 2

Default: 10.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
01.23 Simple Positioning Stop Frequency 3

Default: 20.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
01.24 Simple Positioning Stop Frequency 4

Default: 30.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
01.25 Simple Positioning Stop Frequency 5

Default: 40.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$

### 01.26 Simple Positioning Stop Frequency 6

Default: 50.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$

### 01.27 Simple Positioning Stop Frequency 7

Default: 60.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
1 The setting for Pr.01.20-Pr. 01.27 must follow the condition below:
Pr. $01.20 \leq$ Pr. $01.21 \leq$ Pr. $01.22 \leq$ Pr. $01.23 \leq$ Pr. $01.24 \leq$ Pr. $01.25 \leq$ Pr. $01.26 \leq$ Pr. 01.27
If any two of the parameters (between Pr.01.20-Pr.01.27) have the same stop frequency, set their Delay Time of Simple Positioning Stop to the same values.

| N | 01.28 | Delay Time of Simple Positioning Stop 0 |
| :--- | :--- | :--- |
| N | $\mathbf{0 1 . 2 9}$ | Delay Time of Simple Positioning Stop 1 |
| N | $\mathbf{0 1 . 3 0}$ | Delay Time of Simple Positioning Stop 2 |
| N | $\mathbf{0 1 . 3 1}$ | Delay Time of Simple Positioning Stop 3 |
| N | $\mathbf{0 1 . 3 2}$ | Delay Time of Simple Positioning Stop 4 |
| N | $\mathbf{0 1 . 3 3}$ | Delay Time of Simple Positioning Stop 5 |
| N | $\mathbf{0 1 . 3 4}$ | Delay Time of Simple Positioning Stop 6 |
| N | $\mathbf{0 1 . 3 5}$ | Delay Time of Simple Positioning Stop 7 |

Default: 0.00
Settings $0.00-400.00 \mathrm{sec}$.
[1] This is valid only when Pr. 02.02 motor stop method is set to 4 : simple positioning stop.
$10]$ The settings $0-7$ for Pr.01.20-Pr.01.27 must work with the settings $0-7$ for Pr.01.28-Pr.01.35 and correspond to each other. For example, Pr. 01.20 must work with Pr.01.28.
$\square$ The function of Pr.01.28-Pr.01.35 is simple positioning. Speed starts to decelerate after the time set at Pr.01.28-Pr.01.35 elapses. The accuracy of positioning is self-assessed by user.

$\mathrm{S}=\mathrm{n} \times\left(\frac{t_{x}+\left(t_{x}+t_{2}\right)}{2}\right)$
S : operation distance (revolution)
n : rotation speed (revolution/second)
$t_{x}$ : delay time (second)
$t_{2}$ : deceleration time (second)
$\mathrm{n}=\mathrm{f} \times \frac{120}{\mathrm{p}}$
n : rotation speed (RPM) (revolution/minute)
P : number of poles in the motor
f: operation frequency

The value of $t_{x}$ in the equation above describes as below.

### 1.1 When the slope is negative ( $\mathbf{t} 1>\mathrm{t} 2$ )



$$
\mathrm{t}_{\mathrm{x}}=t_{1}+\left(\frac{f_{x}-f_{1}}{f_{2}-f_{1}}\right) \times\left(t_{2}-t_{1}\right)=t_{1}+\left(\frac{f_{x}-f_{1}}{10}\right) \times\left(t_{2}-t_{1}\right)
$$

1.2 When the slope is positive ( $\mathrm{t} 1<\mathrm{t} 2$ )

$\mathrm{t}_{\mathrm{x}}=t_{2}-\left(\frac{f_{2}-f_{x}}{f_{2}-f_{1}}\right) \times\left(t_{2}-t_{1}\right)=t_{2}-\left(\frac{f_{2}-f_{x}}{10}\right) \times\left(t_{2}-t_{1}\right)$

As shown in the figure below, assume that the radius of the four-pole motor is $r$ and rotation speed is $n$ (RPM).


## Example 1:

When the motor swivel table rotates at 50 Hz , and Pr. $02.02=4$ [Simple Positioning Stop; E.F.: coast to stop], and Pr.01.26 $=50 \mathrm{~Hz}$ [Simple Positioning Stop Frequency 6], and its corresponding Pr. $01.34=2 \mathrm{sec}$. [Delay Time of Simple Positioning Stop 6], then the deceleration time from 50 Hz to 0 Hz is 10 seconds.

After executing the stop command, Simple Positioning Stop activates, its rotation speed is $\mathrm{n}=$ $120 \times 50 / 4$ (revolution / minute) $=25$ (revolution / second)
The number of revolution of the swivel table $=(25 \times(2+12)) / 2=175$ (revolutions)


Therefore, the motor's operation distance after executing the stop command = number of revolutions $\times$ circumference $=175 \times 2 \pi r$. It also means that the swivel table goes back to the top after 175 revolutions.

## Example 2:

Assume that the motor swivel table rotates at 1.5 Hz , and Pr. $01.22=10 \mathrm{~Hz}$ [Simple Positioning Stop Frequency 2], and Pr. $01.30=10 \mathrm{sec}$. [Delay Time of Simple Positioning Stop 2], then the deceleration time from 60 Hz to 0 Hz is 40 seconds. The delay time at stop for 1.5 Hz is 1.5 second; the deceleration time from 1.5 Hz to 0 Hz is 1 second.

After executing the stop command, Simple Positioning Stop activates, its rotation speed is $\mathrm{n}=$ $120 \times 1.5$ / 4 (revolution / minute) $=1.5$ / 2 (revolution / second)

The number of revolution of the swivel table $=(1.5 / 2 \times(1.5+2.5)) / 2=1.5$ (revolutions)


Therefore, the motor's operation distance after executing the stop command $=$ number of revolutions $\times$ circumference $=1.5 \times 2 \pi r$. It also means the swivel table stops after running 1.5 revolutions (red point is at the bottom).

### 01.36 Energy Restriction Mode (Only for V/F Control Mode)

Default: 0
Settings 0: Disabled
1: OFD (Over Flux Deceleration) mode
2: TEC (Traction Energy Control) mode0 : Decelerates or stops according to original deceleration time settings.1: Acceleration, constant speed, and deceleration adjust automatically. Increase output frequency to reduce DC bus. After DC bus is reduced, output frequency is back to setting values.2: Slope of acceleration, constant speed, and deceleration are fixed.

### 01.37 TEC Current Restriction (Reserved) 01.38 TEC Voltage Compensation Gain

Default: 0.5
Settings 0.01-655.35

### 01.39 TEC Voltage Compensation Filter

Default: 0.05
Settings 0.01-655.35 sec.

### 01.40 OFD Voltage Compensation Gain

Default: 0.05
Settings 0.01-655.35
[This page intentionally left blank]

## 02 Operation Method Parameters

$\mathcal{N}$ You can set this parameter during operation.

### 02.00 First Master Frequency Command Source

Default: 4

## Settings 3: RS-485 communication (RJ45 for VFD1 and VFD2) <br> 4: Digital keypad potentiometer

### 02.01 Operation Command Source

Default: 2

## Settings 2: External terminals <br> 4: RS-485 communication (RJ45 for VFD1 and VFD2)

Sets the Master Frequency Command Source for the AC motor drive.Pr. 02.09 is only valid when you set one of Pr. 04.06 or Pr. $04.08=22$. When setting 22 is activated, the frequency command source is the setting for Pr.02.09. You can only enable only one of the first master frequency command and second master frequency command sources at one time.
### 02.02 Motor Stop Method

Default: 0
Settings 0: STOP: ramp to stop; EF: coast to stop
1: STOP: coast to stop; EF: coast to stop
2: STOP: ramp to stop; EF: ramp to stop
3: STOP: coast to stop; EF: ramp to stop
4: Simple positioning stop; EF: coast to stopDetermines how the motor is stopped when the drive receives the Stop command.Ramp to stop: According to the set deceleration time, the AC motor drive decelerates to the Minimum Output Frequency (Pr.01.05), and then stops.

Coast to stop: According to the load inertia, the AC motor drive stops output immediately, and the motor coasts to stop.The motor stop method is usually determined by the characteristics of the motor load and how frequently it is stopped.(1) Use "ramp to stop" for the safety of personnel or to prevent material from being wasted in applications where the motor must stop immediately after the drive stops. You must set the deceleration time accordingly.(2) If idling is allowed or the load inertia is large, use "coast to stop".Example uses are blowers, punching machines, centrifuges and pumps.

## Chapter 10 Descriptions of Parameter Settings | LTC



Ramp to stop and Coast to stop


### 02.03 PWM Carrier Frequency Selection

Default:
8 ( $\leq 5.5 \mathrm{HP}$ ) / 4 (7.5 HP)
Settings $\quad 2-12 \mathrm{kHz}$Determines the PWM carrier frequency for the AC motor drive.

| Carrier <br> Frequency | Acoustic <br> Noise | Electromagnetic <br> Noise or Leakage <br> Current | Heat <br> Dissipation | Current <br> Wave |
| :---: | :---: | :---: | :---: | :---: |
| 2 kHz | Significant | Minimal | Minimal | Significant |

10 From the table, you see that the PWM carrier frequency has significant influences on the electromagnetic noise, the AC motor drive heat dissipation, and the motor acoustic noise. Therefore, if the surrounding noise is greater than the motor noise, lower the carrier frequency to reduce the temperature rise. Although the motor has quiet operation in the higher carrier frequency, consider the entire wiring and interference.

### 02.04 Motor Direction Control

Default: 0

| Settings | $0:$ Enable forward/reverse |
| :--- | :--- |
|  | 1: Disable reverse |
|  | 2: Disable forward |

$\square$ Prevents damage caused by misoperation of the motor FWD/REV directions.

### 02.05

## Drive's Operation Control when Power is ON and RUN Command Source is Changed (External Terminals Only)

## Default: 1

Settings 1: Does not run when power is ON, and remains current operation status when RUN command is changed.
3: Does not run when power is ON, and follows new RUN command when RUN command is changed.

Ind As the table below shows, this parameter sets the drive operation status when power is ON or RUN command source (from external terminals only) is changed.

| 02.05 Settings | Power is ON | RUN Command Source is Changed |
| :---: | :---: | :---: |
| 1 | Drive does not run | Remains current operation status |
| 3 | Drive does not run | Operation status varies with the <br> changed RUN command |

This parameter sets whether the drive receives the RUN command when RUN command source is from external terminals, RUN command remains and drive power is ON. If Pr.02.05=1, the drive does not receive the RUN command. To make the motor run, disable the RUN command first, and enable it again.

Power is ON:
When the RUN command source is from external terminals and RUN command is ON (MI1DCM=closed), the drive runs according to Pr. 02.05 settings after power is applied.
(1) If Pr.02.05=1 or 3, the drive does not run. Disable the RUN command first, and then enable it again to make the drive run.

(1) RUN Command Source is Changed:

Regardless of whether the drive is in operation or at stop, when new RUN command source is from external terminal, and its terminal status (ON: RUN; OFF: STOP) is different from the current status of the drive, the drive's running status is determined by Pr. 02.05 settings:
(1) If Pr. $02.05=1$, the drive's running status does not vary with external terminal status.
(2) If Pr. $02.05=3$, the drive runs or stops immediately in accordance with external terminal status.


When Pr.02.05=1 or 3, it does not guarantee that the drive will never run under this condition. It is possible the drive may be set in motion by a mechanical vibration or malfunctioning switch. Pay extra attention when using this function.

```
02.06 Reserved
02.07 Reserved
02.08 Reserved
02.09 Second Master Frequency Command Source
```

Default: 3
Settings 3: RS-485 communication
4: Control by potentiometer on digital keypad

### 02.10 Combination of the First and Second Master Frequency Commands <br> Default: 0

Settings 0: First master frequency command only
1: First master frequency command + second master frequency command
2: First master frequency command - second master frequency command

### 02.11 Reserved

02.12 Communication Frequency Command

Default: 50.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$Sets the frequency command or reads the communication frequency command.

### 02.13 Frequency Command Saving Selection

Default: 0
Settings 0: Saves the frequency before power is off
2: Only saves the communication frequency command before power is off
Determines whether to save the frequency set by users before power is off.

### 02.14 Initial Frequency Command Mode at Stop

Default: 0

> | Settings | $0:$ Use current Frequency command |
| :--- | :--- |
|  | 1: Use zero Frequency command |
|  | 2: Refer to Pr. 02.15 to set up |

Determines whether to save the frequency set by users before power is off.

### 02.15 Initial Frequency Command Setting at Stop

Default: 60.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$Pr.02.14 and Pr.02.15 are used to determine the initial frequency command at stop.Pr.02.14=0: The initial frequency at stop is the current frequency.Pr.02.14=1: The initial frequency at stop is 0 .Pr.02.14=2: The initial frequency at stop is the Pr. 02.05 setting.

### 02.16 Master Frequency Command Source Display

| Seftings | Dit0ult: Read only |
| :--- | :--- |
|  | Frequency Source (Pr.02.00). |
|  | bt1=1: The Master Frequency Command Source is the Second Master |
|  | Frequency Source (Pr.02.09). |
| bt2=1: The Master Frequency Command Source is the external multi- |  |
| function input terminal |  |

Master frequency command source can only be read from this parameter.

### 02.17 Operation Command Source Display

Default: Read only
Settings bt1=1: Operation Command source is the RS-485 communication
bt2=1: Operation Command Source is the external terminal (MI1) bt3=1: Operation Command Source is the external multi-function input terminals

Operation command source can only be read from this parameter.

### 02.18 User-defined Value 2 Setting

Default: 0

$$
\text { Settings } \quad 0-\mathrm{Pr} .00 .13
$$

[1] When Pr.00.13 is not set to 0 and frequency source is from communication, Pr. 02.18 will be used to read/write to change the operation frequency.

Chapter 10 Descriptions of Parameter Settings | LTC
02.19 User-defined Value 2

Default: Read only
Settings 0-9999

## 03 Output Function Parameters

$N$ You can set this parameter during operation.
03.00 Multi-function Output (MO1)

Default: 8

### 03.01 Multi-function Output (MO2)

Default:
VFD1: 26
VFD2: 6
1 Summary of Function Settings

| Settings | Functions |  |
| :---: | :--- | :--- |
| 0 | No function | Output terminal with no function |
| 1 | Indication during RUN | Activates when the drive outputs voltage or RUN <br> command is given. |
| 2 | Indication of frequency <br> reached | Activates when output frequency of the drive reaches to <br> the setting frequency. |
| 3 | Zero speed | Activates when output frequency of the drive is lower than <br> the minimum output frequency. |
| 4 | Over-torque detection | Activates when the drive detects over-torque. Pr.06.04 sets <br> the over-torque detection level, and Pr.06.05 sets the over- <br> torque detection time. |
| 5 | Base Block (B.B.) indication | Activates when external interruption (B.B.) occurs in the <br> drive and stops outputting. |
| 7 | Drive operation mode | Activates when the drive detects input voltage is too low. |
| 8 | Fault indication |  |
| external terminals. |  |  |, | Activates when the drive detects fault occurs. (oc, ov, ovA, |
| :--- |
| ovd, ovn, oH1, oL, oL1, EF, cF3.0-5, HPF1,2,4, ocA, ocd, |, | ocn). |
| :--- |


| Settings | Functions | Descriptions |
| :---: | :--- | :--- |
| 15 | Over-voltage | Activates when DC bus over-voltage is detected. |
| 16 | Reserved | None |
| 17 | Forward running (FWD) <br> command | Activates when the drive running direction is forward <br> (FWD). |
| 18 | Reverse running (REV) <br> command | Activates when the drive running direction is reverse <br> (REV). |
| 19 | Zero speed (including STOP) | Activates when the drive is in standby or stop. |
| 20 | Warning indication | Activates when the drive detects warning occurs (CExx, <br> AUE, FbE, SAvE). |
| 21 | Mechanical brake control | Activates when output frequency $\geq$ Pr.03.11 setting value. <br> Deactivates when output frequency $\leq$ Pr.0312 setting value <br> after STOP command. |
| 22 | Drive is ready | Activates when the drive is ready. |
| 23 | Reserved | None |
| 24 | Reserved | None |
| 25 | Reserved | None |
| 26 | Belt conveyor motor stall <br> failure | Activates when belt conveyor motor stall occurs. |

### 03.02 Desired Frequency Reached

Default: 0.0
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
1 Once the output frequency reaches the desired frequency, if the corresponding multi-function output terminal is set to 9 (Pr. 03.00 and Pr.03.01), this multi-function output terminal is "closed".


Timing diagram of multi-function terminals and frequency reached
03.03 Reserved
03.04 Reserved
03.05 Terminal Count Value

Default: 0
Settings 0-9999
$\square$ Sets the count value of the LTC internal counter, which can be triggered by using the external multi-function input terminals on the control circuit. When the count reaches the setting value, the specified output terminal activates by setting one of the multi-function input terminals (the count value resets after reaching the setting for Pr.03.05).

### 03.06 Preliminary Count Value

Default: 0

> Settings 0-9999When the count value counts from c1 to reach this value, the corresponding multi-function output terminal is activated (MO\#11: Preliminary count value reached). You can use this parameter as the end of counting to make the drive run from the low speed to stop.The timing diagram shows as follows.


### 03.07 EF Activates when the Terminal Count Value Reached

## Default: 0

Settings 0: Terminal count value reached, no EF displays
1: Terminal count value reached, EF is triggered
When Pr.03.07=1, the drive stops and EF is triggered when the count value is reached until EF is reset.

### 03.08 Fan Cooling Control

Default: 0
Settings 0 : Fan is always ON
1: Fan is OFF after the AC motor drive stops for one minute
2: Fan is ON when the AC motor drive runs; fan is OFF when the AC motor drive stops
3: Fan is ON after detecting heat sink temperature is reached $\left(\mathrm{ON}: 60^{\circ} \mathrm{C}\right.$; OFF: $40^{\circ} \mathrm{C}$ )
4: Fan is ON when the AC motor drive runs; fan is OFF when the AC motor drive stops. Fan is standby when at zero speed.
(1) Use this parameter to control the fan.

1 Because fan is controlled by both belt conveyor motor and telescopic motor, fan will be ON when either fan of drives is ON, but can only be OFF when both fans of drives are OFF.

### 03.09 Reserved

03.10 Reserved
03.11 Mechanical Brake Release Frequency

Default: 0.00
Settings $\quad 0.00-20.00 \mathrm{~Hz}$

### 03.12 Mechanical Brake Engage Frequency

Default: 0.00
Settings $\quad 0.00-20.00 \mathrm{~Hz}$

1. These two parameters set control of the mechanical brake through the output terminals (Relay) by setting Pr. 03.00 to 21 .

[10] Pr. 03.00 multi-function output terminal 21 (Mechanical Brake Control): When the output frequency reaches Pr. 03.11 (Mechanical Brake Release Frequency), the multi-function output terminal is closed (ON). When the output frequency reaches Pr.03.12 (Mechanical Brake Engage Frequency), the multi-function output terminal is open (OFF).

### 03.13 Display the Status of Multi-function Output Terminal

Default: Read only
Settings Read only
1 The multi-function output terminals are falling-edge triggered. Thus if terminals are not activated, Pr.03.13 displays 3.
03.14 Reserved
[This page intentionally left blank]

## 04 Input Function Parameters

$N$ You can set this parameter during operation.

### 04.00 Keypad Potentiometer Bias

Default: 0.0
Settings 0.0-100.0\%
04.01 Keypad Potentiometer Bias Polarity

Default: 0
Settings 0: Positive bias
1: Negative bias

### 04.02 Keypad Potentiometer Gain

Default: 100
Settings 0.1-200.0\%

### 04.03 Keypad Potentiometer Negative Bias, Reverse Motion Enable/Disable

Default: 0

## Settings 0: No negative bias command <br> 1: Negative bias: REV motion enabled

[1] Use Pr.04.00-Pr. 04.03 for applications that use the potentiometer signal on the digital keypad to adjust the setting frequency. The potentiometer is not an external device, but you need to use it when setting parameters. Refer to the following examples to know how to use it.
$\llbracket \rrbracket$ As shown in the left figure below, operating the potentiometer on the digital keypad to the leftmost means the minimum value that the lowest voltage $0 \mathrm{~V}_{\mathrm{DC}}$ corresponds to. As shown in the right figure below, operating the potentiometer on the digital keypad to the rightmost means the maximum value that the highest voltage $5 \mathrm{~V}_{\mathrm{DC}}$ corresponds to.


The minimum


The maximum

## Example 1:

You set Pr. 02.00 to 4 (the master frequency command comes from keypad potentiometer), set Pr.04.00-Pr.04.03 to defaults, and then you can set the main frequency command through the keypad potentiometer.


## Example 2:

If you want the corresponded minimum value to be 12 Hz (the master frequency setting) when operating the keypad potentiometer to the leftmost, and then you can set other frequency settings as required. As can be seen from the diagram below, the correspondence between the keypad potentiometer (voltage) and setting frequency has been changed from $0-5 \mathrm{~V}$ (min.max.) / $0-60 \mathrm{~Hz}$ to $0-4 \mathrm{~V} / 0-60 \mathrm{~Hz}$. Therefore, the 4 V and above from the keypad potentiometer all correspond to 60 Hz . To use the full potentiometer range, refer to Example 3.


| Default: |  |
| :--- | :--- |
| Pr. $01.00=60 \mathrm{~Hz}$ | Max. operation frequency |
| Pr. $04.00=20 \%$ | Bias adjustment |
| Pr. $04.01=0$ | Bias direction adjustment |
| Pr. $04.02=100 \%$ | Gain of frequency adjustment |
| Pr. $04.03=0$ | No negative bias command |

To count the bias (See $\alpha$ in the formula):

$$
\begin{aligned}
& \frac{60-0 \mathrm{~Hz}}{5 \mathrm{~V}}=\frac{12-0 \mathrm{~Hz}}{\alpha \mathrm{~V}} \rightarrow \alpha=1 \mathrm{~V} \\
& \therefore \quad \operatorname{Pr} .04 .00=\frac{1 \mathrm{~V}}{5 \mathrm{~V}} \times 100 \%=20 \%
\end{aligned}
$$

## Example 3:

As shown in this example, the keypad potentiometer can be used for all ranges of $0-5 \mathrm{~V} / 0-60$ Hz settings. This increases flexibility.


To count the gain value:

$$
\operatorname{Pr} .04 .02=\frac{5 \mathrm{~V}}{[5-(-1)] \mathrm{V}} \times 100 \%=83.3 \%
$$

## Example 4:

This example shows how to use the first half range $0-2.5 \mathrm{~V}$ (min. $-1 / 2 \times$ max.) from the keypad potentiometer to set $0-60 \mathrm{~Hz}$ frequency settings. You can achieve the same results by either adjusting Pr. 04.02 gain or setting Pr. 01.00 to 120 Hz .


## Example 5:

This example is a typical negative bias application. Using negative bias to set the frequency greatly reduces the noise interference. In a noisy environment, do NOT use signals less than 1 V to set the drive's operation frequency.


## Example 6:

This example uses a negative bias to provide a noise margin. In additional, it uses a potentiometer frequency gain to reach the Maximum Output Frequency. This applies to a wide range of applications, and increases flexibility.


## Example 7:

This example uses potentiometer applications in all aspects, and its applications in forward and reverse control make it easy to combine with system for complex applications. Note that using the settings in this example disables the external FWD and REV controls.


## Example 8:

This example uses negative slope.
With these settings, the AC motor drive always runs in only one direction (reverse).


### 04.04 MI Terminal Control Mode Selection (MI1, MI2, MI3)

Default: 0
Settings Mode 1 (Pr.04.19=0)
0: MI1 start-up (FWD) / stop
1: Reserved
2: Reserved
Mode 2 (Pr.04.19=1)
0 : Two-wire operation control (1) MI1, MI2
1: Two-wire operation control (2) MI1, MI2
2: Three-wire operation control MI1, MI2, MI3
[1] NOTE: MI in the two VFDs of LTC has been pre-defined as two-wire operation control (1) in mode 2, so do NOT change the setting values in Pr.04.04 and Pr.04.19.
$\left[\begin{array}{ll}\text { [1] Settings and functions of Pr. } 04.04 \text { depend on Pr. } 04.19 \text { settings. }\end{array}\right.$
(1) When Pr.04-19=0 in mode 1, MI1 is used to control start-up and stop, and MI2 to MI4 are used to set multi-function terminals.
[1] When Pr.04.19=1 in mode 2, MI1 and MI2 in two-wire operation control (1) \two-wire operation control (2) are used to control start-up/stop and FWD/REV, MI3 and MI4 are used to set multifunction terminals; MI1 to MI3 in three-wire operation control are used to control start-up/stop and FWD/REV, MI4 is used to set multi-function terminal.

| Pr.04.19 Settings | Pr.04.04 Settings | External Terminal Control Circuits |
| :---: | :---: | :---: |
| $\begin{gathered} \text { Mode } 1 \\ \text { Pr.04.19=0 } \end{gathered}$ | Pr.04.04=0 <br> Single-wire <br> FWD / STOP |  |
| $\begin{gathered} \text { Mode } 2 \\ \text { Pr.04.19=1 } \end{gathered}$ | Pr.04.04=0 <br> Two-wire operation control (1) FWD / STOP, REV / STOP |  |


| Pr.04.19 Settings | Pr.04.04 Settings | External Terminal Control Circuits |
| :---: | :---: | :---: |
|  | $\text { Pr. } 04.04=1$ <br> Two-wire operation control (2) RUN / STOP, REV / FWD |  |
|  | Pr.04.04=2 <br> Three-wire operation control |  |

### 04.05 Reserved

04.06 Multi-function Input Command 1 (MI2)

Default: 1

### 04.07 Multi-function Input Command 2 (MI3)

Default: 14
04.08 Multi-function Input Command 3 (MI4)

Default: 5
Settings 0: No function
1: Reserved
2: Reserved
3: Reserved
4: Reserved
5: Fault reset
6: Acceleration / deceleration inhibit
7: $1^{\text {st }}$ and $2^{\text {nd }}$ acceleration / deceleration time selection
8: JOG command
9: B.B. inputs from external
10: Reserved
11: Reserved
12: Counter trigger
13: Clear the counter
14: External fault input
15: Reserved
16: Output stops
17: Parameter lock enabled
18: Operation command selection: external terminals
19: Reserved
20: Operation command selection: communication
21: Forward / reverse running command

22: Second master frequency command source enabled
23: Simple positioning stop by forward limit
24: Simple positioning stop by reverse limit
[] Summary of Function Settings

| Settings | Functions | Descriptions |
| :---: | :---: | :---: |
| 0 | No function | Set terminals to have no function to ensure they have no effect on operations. Set any unused terminals to 0 to avoid errors caused by incorrect wiring or malfunction. |
| 1 | Reserved |  |
| 2 | Reserved |  |
| 3 | Reserved |  |
| 4 | Reserved |  |
| 5 | Fault reset | Use this terminal to reset the drive after drive faults are cleared. |
| 6 | Acceleration / <br> deceleration inhibit | When this function is enabled, the drive stops acceleration or deceleration immediately. The drive resumes from the inhibit point once this function is disabled. |
| 7 | $1^{\text {st }}$ and $2^{\text {nd }}$ <br> acceleration / deceleration time selection | You can select the acceleration and deceleration time of the drive with the digital status of the terminals; there are two acceleration and deceleration selections. |
| 8 | JOG command | Executes the JOG operation only when the drive stops completely. While running, you can still change the operation direction, and the STOP key on the keypad is valid. Once the external terminal receives the OFF |


| Settings | Functions | Descriptions |
| :---: | :---: | :---: |
|  |  | command, the motor stops in the JOG deceleration time. Refer to Pr.01.13-Pr.01.15 for details. |
| 9 | B.B. (Base Block) inputs from external | When the AC motor drive receives a Base Block signal, it blocks all output and the motor coasts. When Base Block control is deactivated, the AC drive starts speed tracking from the frequency before blocking and synchronizes with the motor speed. It then accelerates to the master frequency. Even if the motor has been completely stopped after blocking, the drive executes speed tracking immediately once Base Block control is deactivated. (NOTE: bb: Base Block) (See Pr.08.06-Pr. 08.07 for details) |
| 10 | Reserved |  |
| 11 | Reserved | None |
| 12 | Counter trigger | Uses external signals such as connecting ON/OFF switch, lightening senor, etc., to trigger the counter. You can also use signals of the multifunction output terminal (counter reached, desired counter reached) to control the applications that based on the counter. For example: winding machine, packing machine. (See Pr. 03.05 and Pr. 03.06 for details.) |
| 13 | Clear the counter | ON : the current counter value is cleared and displays c 0 . The drive counts up when this function is disabled. |
| 14 | External Fault (EF) <br> input | When the drive receives status change from the EF terminal, the drive stops output immediately, and EF is displayed on the keypad. The motor coasts. The drive does not run until the external fault is cleared after you press RESET on the keypad. (EF: External Fault) |
| 15 | Reserved | None |
| 16 | Output stops | This terminal is to stop output. AC motor drive stops output and the motor coasts to stop if one of these settings is enabled. If status of the terminal is changed, AC motor drive restarts from 0 Hz . |
| 17 | Parameter lock enabled | ON: all parameter data are read 0. OFF: all parameters can be read. |


| Settings | Functions | Descriptions |
| :---: | :---: | :---: |
| 18 | Operation command selection (Pr. 02.01 setting / external terminals) | ON: operation command through external terminals; OFF: operation command through Pr. 02.01 setting. (NOTE: When 18 and 20 are ON at the same time, the priority is settings $18>$ setting 20.) |
| 19 | Reserved | None |
| 20 | Operation command selection (Pr. 02.01 setting / communication) | ON: operation command through communication; OFF: operation command through Pr. 02.01 setting. (NOTE: When 18 and 20 are ON at the same time, the priority is settings $18>$ setting 20.) |
| 21 | Forward / reverse <br> running command | Once this function is enabled, ON: reverse running / OFF: forward running, and up/down keys on the keypad cannot be used to change the running direction. |
| 22 | Second master <br> frequency command <br> source enabled | Pr. 02.09 settings are valid only when this terminal is ON. Used to switch between first / second master frequency command and operation command source. |
| 23 | Simple positioning stop by forward limit | If a motor receives such signal while running forward, it stops running forward. |
| 24 | Simple positioning stop by reverse limit | If a motor receives such signal while running in reverse, it stops running in reverse. |

### 04.09 Multi-function Input Contact Selection (N.O./N.C.)

Default: 0
Settings 0-15
[l] Sets the contact status of external multi-function input terminals MI2-MI4 to be normally open (N.O.) or normally closed (N.C.) according to the start-up / stop mode (Pr.04.19 and Pr.04.04).
[1] When the start-up/stop mode is single-wired (Pr.04.04=0, Pr.04.19=0), MI1 setting is invalid; when the start-up/stop mode is two-wired (Pr.04.04=0 or 1, Pr.04.19=1), MI2 setting is invalid; when the start-up/stop mode is three-wired (Pr.04.04=2, Pr.04.19=1), MI2 and MI3 settings are invalid. See the table below for details.


| Start-up / Stop Mode |  | MI4 | MI3 | MI2 | MI1 | The Setting of the MI Terminals |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Mode | Pr.04.19 | Pr.04.04 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | Multi-function <br> Input <br> Terminals | The Terminals Occupied <br> by the Start-up / Stop <br> Function |
| Single-wire | 0 | 0 | $0 / 1$ | $0 / 1$ | $0 / 1$ | - | MI4, MI3, MI2 <br> can be set as <br> N.O. or N.C. | MI1 is only controlled by <br> external terminals |
| Two-wire | 1 | 0 or 1 | $0 / 1$ | $0 / 1$ | - | - | MI4, MI3 can <br> be set as N.O. <br> or N.C. | MI1, MI2 are only <br> controlled by external <br> terminals |
| Three-wire | 1 | 2 | $0 / 1$ | - | - | - | MI4 can be <br> set as N.O. or <br> N.C. | MI1, MI2, MI3 are only <br> controlled by external <br> terminals |

Setting method:
Before setting this parameter, convert Bit3-Bit0 that represent the status of MI4-MI1 from binary to decimal.
For example: setting MI3 and MI4 to be $1=$ N.C.; setting MI1 and MI2 to be $0=$ N.O.. The setting value for Pr. 04.09 should be 12.

| Weight Bit | $2^{3} \quad 2^{2} \quad 2^{1} \quad 2$ |  |  |  |  | Setting value$\begin{aligned} & =\text { bit } 3 \times 2^{3}+\text { bit } 2 \times 2^{2}+\text { bit } 1 \times 2^{1} \\ & + \text { bit } 0 \times 2^{0}=8+4+0+0=12 \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 1 | 0 | 0 |  |  |
| $\begin{aligned} & 0=\mathrm{N} . \mathrm{O} . \\ & 1=\mathrm{N} . \mathrm{C} . \end{aligned}$ |  |  |  |  | MI1 <br> MI2 <br> MI3 |  |

### 04.10 Digital Terminal Input Response Time

Default: 1

## Settings 1-20 (*2 ms)

[1] This function is to delay and confirm the digital input terminal signal. 2 ms is one unit. The time for delay is also the time for confirmation. The confirmation prevents interference that could cause error in the input to the digital terminals. But in the meanwhile, it delays the response time though confirmation improves accuracy.

| 04.11 | Reserved |
| :---: | :--- |
| 04.12 | Reserved |
| 04.13 | Reserved |
| $\mathbf{0 4 . 1 4}$ | Reserved |
| $\mathbf{0 4 . 1 5}$ | Reserved |
| $\mathbf{0 4 . 1 6}$ | Reserved |
| $\mathbf{0 4 . 1 7}$ | Reserved |
| $\mathbf{0 4 . 1 8}$ | Reserved |

### 04.19 MI Terminal Control Mode Selection

Default: 1

$$
\begin{aligned}
\text { Settings } & 0 \text { : Mode } 1 \text {, single-wire start-up/stop } \\
& 1 \text { : Mode 2, two-wire/three-wire start-up/stop }
\end{aligned}
$$

(1) NOTE: MI in the two VFDs of LTC has been pre-defined as two-wire operation control (1) in mode 2, so do NOT change the setting values in Pr.04.04 and Pr.04.19.Determines the MI terminal control modes.
M Mode 1: MI1 is the start-up/stop terminal; MI2 and MI3 are the multi-function input terminals.
M Mode 2: MI1, MI2 and MI3 support the two-wire / three-wire start-up.

```
04.20 Reserved
04.21 Reserved
04.22 Reserved
04.23 Reserved
04.24 Reserved
04.25 Reserved
```


### 04.26 Display the Status of Multi-function Input Terminal

Default: Read only
Settings See parameter descriptions below
Displays 15 in Pr.04.26 when all the MI terminals are not active.
When MI1 (corresponds to bit0) is triggered, and the weight is 1, Pr.04-26=14 (15-1).
When MI2 (corresponds to bit1) is triggered, and the weight is 2 , $\operatorname{Pr} .04-26=13$ (15-2).
When MI3 (corresponds to bit2) is triggered, and the weight is 4 , $\operatorname{Pr} .04-26=11$ (15-4).
When MI4 (corresponds to bit3) is triggered, and the weight is 8 , $\operatorname{Pr} .04-26=7$ (15-8).
If more than one MI terminals are triggered, use 15 minus the weight that corresponds to the MI terminal.


For example, if you set MI 2 and MI 3 to be active (ON), Pr. 04.26 displays 9 (15-2-4=9, decimal).

| Weight Bit | $2^{3} 2^{2} 2^{1} 2^{0}$ |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | 0 | 1 | 1 | 0 |
| $\begin{aligned} & 0=O N \\ & 1=O F F \end{aligned}$ |  |  |  |  |

Setting value
$=15-\left(b i t 3 \times 2^{3}+b i t 2 \times 2^{2}+b i t 1 \times 2^{1}\right.$

+ bit $\left.0 \times 2^{0}\right)=15-(0+4+2+0)=9$


### 04.27 Internal/External Multi-function Input Terminals Selection

Default: 0

## Settings 0-15

[1] Selects the terminals MI1-MI4 to be either internal terminal or external terminal. You can activate internal terminals with Pr.04.28. A terminal cannot be both an internal terminal and an external terminal at the same time.


Setting method: Before setting this parameter, convert Bit3-Bit0 that represent the status of MI4-MI1 from binary to decimal. Set MI3 and MI4 as internal terminals; set MI1 and MI2 as external terminals. Pr.04-27 should be set to 12 .


### 04.28 Internal Multi-function Input Terminal Status

Default: 0
Settings 0-15
[1] Sets the internal terminal action (ON/OFF) through the keypad or communication. And use this parameter with Pr.04.27.


Setting method: To activate MI3 and MI4, set Pr. 04.28 to 12 .

| Weight Bit | $2^{3} 2^{2} \quad 2^{1} \quad 2^{0}$ |  |  |  |  | Setting value$\begin{aligned} & =\text { bit } 3 \times 2^{3}+\text { bit } 2 \times 2^{2}+\text { bit } 1 \times 2^{1} \\ & + \text { bit0 } \times 2^{0}=8+4+0+0=12 \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 0 | 0 |  |  |
| $\begin{aligned} & 0=\text { External } \\ & \text { terminal is OFF } \\ & 1=\text { Internal } \\ & \text { terminal is ON } \end{aligned}$ |  |  |  |  | MI1 <br> MI2 <br> MI3 |  |

Chapter 10 Descriptions of Parameter Settings | LTC

| 04.29 | Reserved |
| :---: | :--- |
| 04.30 | Reserved |
| $\mathbf{0 4 . 3 1}$ | Reserved |
| 04.32 | Reserved |
| $\mathbf{0 4 . 3 3}$ | Reserved |
| $\mathbf{0 4 . 3 4}$ | Reserved |

## 05 Multi-step Speed Parameters

$\mathcal{N}$ You can set this parameter during operation.

| 05.00 | Reserved |
| :---: | :---: |
| 05.01 | Reserved |
| 05.02 | Reserved |
| 05.03 | Res |
| 05.04 | Reserved |
| 05.05 | Reserved |
| 05.06 | Reserved |

Default: None
Settings None
12 MI1 to MI4 in VFD1 and VFD2 of LTC was defined as MI1 (FWD/Stop), MI2 (REV/Stop), MI3 (Emergency Stop), and MI4 (Fault Rest), and cannot be used as multi-step speed.
[This page intentionally left blank]

## 06 Protection Function Parameters

$N$ You can set this parameter during operation.

### 06.00 Over-voltage Stall Prevention

Default: 780 V
Settings 0: Disabled
660.0-820.0 VSetting Pr. 06.00 to 0.0 disables the over-voltage stall prevention function (connected with braking unit or brake resistor).
$\square$ During deceleration, the DC bus voltage may exceed its maximum allowable value due to motor regeneration in some situations, such as motor's loading inertia being too high. When overvoltage stall prevention is enabled and the DC bus voltage detected is too high, the drive stops decelerating (output frequency remains unchanged) until the DC bus voltage drops below the setting value.
$\square$ Use this function when you are unsure about the load inertia. When stopping under normal load, the over-voltage does not occur during deceleration and meet the deceleration time setting. Sometimes it may not stop due to over-voltage during decelerating to STOP when the load regenerative inertia increases. In this case, the AC motor drive extends the deceleration time automatically until the drive stops. If you encounter any problem with the deceleration time, refer to the following guides for troubleshooting. 1. Increase the deceleration time.
2. Install a brake resistor to dissipate the electrical energy that is regenerated from the motor.


### 06.01 Over-current Stall Prevention during Acceleration

Default: 170
Settings 20-250\% (0: Disabled)
During acceleration, the AC motor drive output current may increase abruptly and exceed the value specified in Pr. 06.01 due to rapid acceleration or excessive load on the motor. When this function is enabled, the AC motor drive stops accelerating and keeps the output frequency constant until the current drops below this setting.


### 06.02 Over-current Stall Prevention during Operation

Default: 170
Settings 20-250\% (0: Disabled)
[1] If the output current exceeds the setting value for Pr. 06.02 when the drive is operating, the drive decreases its output frequency to prevent the motor from stalling. If the output current is lower than the setting value for Pr.06.02, the drive accelerates again to the setting frequency.


Default: 0
Settings 0: No detection
1: Drive continues operation until OL1 or OL protection is triggered after overtorque is detected during constant speed operation
2: Drive stops and OL2 fault is triggered after over-torque is detected during constant speed operation
3: Drive continues operation until OL1 protection is triggered after over-torque is detected during acceleration and constant speed operation

4: Drive stops and OL2 fault is triggered after over-torque is detected during acceleration and constant speed operation
[1] Determines the operation of the drive after OL2 is triggered. Detection method: If the output current exceeds the Over-torque Detection Level (Pr.06.04) and the detection time is longer than the setting for Pr. 06.05 (Over-torque Detection Time), the fault code "OL2" is displayed on the drive. If a multi-function output terminal is set to over-torque detection (Pr.03.00=4), the output is ON. Refer to Pr. 03.00 for details.

### 06.04 Over-torque Detection Level

Default: 150
Settings 10-200\%$100 \%$ corresponds to the rated current of the drive (Pr.00.01).

### 06.05 Over-torque Detection Time

Default: 0.1
Settings $0.1-60.0 \mathrm{sec}$.
(1) Sets the drive's treatments after over-torque detection criteria is defined and after over-torque is detected.
[a] Over-torque detection criteria: The output current exceeds the Over-torque Detection Level (Pr.06.04) and the detection time is longer than the setting for Pr. 06.05 (Over-torque Detection Time). If a multi-function output terminal is set to over-torque detection (Pr.03.00=4) , the contact is ON (closed). Refer to Pr. 03.00 for details.

### 06.06 Electronic Thermal Overload Relay Selection (OL1)

Default: 2
Settings 0: Standard motor (motor with the fan on the shaft)
1: Special motor (with external forced cooling)
2: Disabled
Prevents self-cooled motor from overheating under low speed. Use an electronic thermal relay to avoid motor burnout due to overheating.


### 06.07 Electronic Thermal Overload Relay Action Time

Default: 60
Settings $\quad 30-600 \mathrm{sec}$.
[1] Use this parameter to set the action time of the electronic thermal relay. It works based on the $I^{2} t$ characteristic curve of electronic thermal relay, the output frequency and current of the drive, and the operation time to prevent the motor from overheating.


### 06.08 Fault Record 1 (The Most Recent)

06.09 Fault Record 2
06.10 Fault Record 3
06.11 Fault Record 4
06.12 Fault Record 5

Default: 0
Display 0-40
[a] Fault Record Descriptions

| ID* |  | Fault Descriptions |
| :---: | :--- | :--- |
| 0 | No fault record |  |
| 1 | Over-current (oc) |  |
| 2 | Over-voltage (ov) |  |
| 3 | IGBT overheating (oH1) |  |
| 4 | Reserved |  |


| ID* | Fault Descriptions |
| :---: | :--- |
| 5 | Overload (oL) |
| 6 | Overload 1 (oL1) |
| 7 | Overload 2 (oL2) |
| 8 | External Fault (EF) |
| 9 | Over-current during acceleration (ocA) |
| 10 | Over-current during deceleration (ocd) |
| 11 | Over-current during steady operation (ocn) |
| 12 | Reserved |
| 13 | Reserved |
| 14 | Phase loss protection (PHL) |
| 15 | Reserved |
| 16 | Auto-acceleration/deceleration failure (cFA) |
| 17 | Software protection enabled (codE) |
| 18 | EEPROM write error (cF1.0) |
| 19 | EEPROM read error (cF2.0) |
| 20 | Hardware protection fault 1 (HPF1) |
| 21 | Hardware protection fault 2 (HPF2) |
| 22 | Reserved |
| 23 | Hardware protection fault 4 (HPF4) |
| 24 | Drive wiring detection fault (cF3.0) |
| 25 | Drive wiring detection fault (cF3.1) |
| 26 | Drive wiring detection fault (cF3.2) |
| 27 | Drive wiring detection fault (cF3.3) |
| 28 | Drive wiring detection fault (cF3.4) |
| $29-36$ | Reserved |
| 37 | Multi-motor fault protection (oPHL) |
| 38 | IGBT temperature PTC OFF (tH1o) |
| $39-40$ | Reserved |
| 41 | Belt conveyor drive (VFD1) stall failure (StAL) |
| 42 | Over-voltage at stop (ovS) |
| 43 | Over-voltage during acceleration (ovA) |
| 44 | Over-voltage during deceleration (ovd) |
| 45 | Over-voltage during constant speed (ovn) |
|  |  |
| 1 |  |

### 06.13 Motor Phase Loss Detection Time

Default: 0
Settings 0-60 sec.

### 06.14 Current Detection for Motor Phase Loss

Default: 30
Settings 10-100\%

1 Uld Use Pr.06.13 with Pr.06.14. When three-phase imbalance occurs among three-phase motors and the imbalance reaches Pr. 06.14 setting percentage (the percentage is $100 \%$ equal to the AC motor drives rated current settings in Pr.00.01), and lasts the time set in Pr.06.13, fault code oPHL is triggered and the drive stops to prevent the motor from damage due to the three-phase unbalanced operation.
$\mathcal{N}$ You can set this parameter during operation.

### 07.00 Motor Rated Current

Default: 100
Settings 23\% FLA-120\% FLA
11 Sets this value according to the rated current of the motor as indicated on the motor nameplate. The factory settings are set according to the drive's rated current. The default is $100 \%$ of the drive's rated current (FLA).
[ad There is 0.1 error between the drive's actual setting value and minimum value.
If drive's power is larger than motor's power, overload protection (OL2) should be enabled against the motor in case motor runs with its rated current in a long time. For motor over-torque protection parameters, set Pr.06.03 to Pr.06.05. Suggested settings are as follows.
$\square$ Set Pr.06.03 = 4 to enable overload protection against the motor during acceleration and constant speed operationSet Pr.06.04 $=($ motor rated current $\div$ drive rated output current $) \times(100-150 \%)$Set Pr. $06.05=0.1-60.0 \mathrm{sec}$. (as required)

### 07.01 Motor No-load Current

Default: 40
Settings 0\% FLA-99\% FLA
1 Sets no-load current of the motor. This directly affects slip compensation.

### 07.02 Torque Compensation

Default: 0
Settings 0.0-10.0
Sets the AC motor drive to automatically increase voltages to get a higher torque when the AC motor drive is running.

### 07.03 Slip Compensation Gain

Default: 0
Settings 0.0-10.0
$\square$ For an asynchronous motor, increasing the load on the AC motor drive causes slip to increase and results in decreased speed. Use this parameter to set the compensation frequency, and reduce the slip to maintain the synchronous speed when the motor runs at the rated current. When the drive output current is higher than motor no-load current (Pr.07.01), the drive compensates the frequency according to this parameter. If the actual speed ratio is slower than expected, increase the parameter setting value; otherwise, decrease the setting value.

### 07.04 Motor Parameter Auto-tuning

Default: 0

$$
\begin{array}{ll}
\text { Settings } & 0 \text { : Disabled } \\
& \text { 1: Auto-tuning R1 (motor does not run) } \\
& \text { 2: Auto-tuning R1 + no-load current (motor runs) }
\end{array}
$$

Iad 1 and 2: Executes motor parameter auto-tuning once the drive receives RUN command. 1: Only
the R1 value is automatically detected, and Pr. 07.01 should be manually entered.
2: Unload the AC motor drive, and values for Pr. 07.01 and Pr. 07.05 will be automatically entered.
The steps for motor auto-tuning are:

1. Ensure that all the drive parameters are set to defaults and the motor wiring is correct.
2. Ensure that the motor is not loaded before executing auto-tuning and that the shaft is not connected to any belt or reducer.
3. Enter the correct values for Pr.01.01 Motor Rated Frequency, Pr.01.02 Motor Rated Voltage, Pr.07.00 Motor Rated Current, and Pr.07.06 Motor Rated Slip (Motor 0).
4. After you set Pr. 07.04 to 2 , the AC motor drive executes auto-tuning immediately after receiving a RUN command (CAUTION: The motor will run! Avoid damage to the drive or personal injury caused by motor running.). The total auto-tuning time is 15 seconds $=$ Pr. $01.09+$ Pr.01.10. (Higher-power drives need longer acceleration and deceleration time.)
5. After auto-tuning is finished, check if Pr. 07.01 and Pr. 07.05 all have values. If not, set Pr. 07.04 again, and then press the RUN key on the keypad to repeat auto-tuning.
6. Finally, set Pr.00.10 to 1 and set other parameters according to your application requirements.
> Related parameters: Pr.01.01 Motor Rated Frequency, Pr.01.02 Motor Rated Voltage, Pr. 07.00 Motor Rated Current, Pr.07.01 Motor No-load Current, Pr.07.05 Motor Line-to-line Resistance R1 (Motor 0), Pr. 07.06 Motor Rated Slip (Motor 0)

## NOTE:

1. In vector control mode, it is not recommended to have motors run in parallel.
2. Vector control mode is not recommended if the motor rated power exceeds the rated power of the AC motor drive.

### 07.05 Motor Line-to-line Resistance R1 (Motor 0)

Default: 0
Settings $0-65535 \mathrm{~m} \Omega$
1 This parameter value is automatically set after motor auto-tuning process. You can also set this parameter without using motor parameter auto-tuning. This resistance value is the $R$ value between phase and phase of the motor. Regardless of the motor wiring method, this resistance value is the measured value of any two motor leads.

### 07.06 Motor Rated Slip (Motor 0)

Default: 3
Settings $\quad 0.00-20.00 \mathrm{~Hz}$
凹ets the rated slip of motor load. Enter the actual rated RPM from the motor nameplate.Refer to the rated RPM and the number of poles from the motor nameplate and use the following formula to calculate the rated slip:
Rated slip $=\mathrm{F}-(\mathrm{N} \times \mathrm{P} \div 120)$
F: Rated frequency (Hz)
N : Rated speed (RPM)
P: Number of poles in the motor (Pole)Assume that the rated frequency of the motor is 60 Hz with 4 poles, and the rated RPM is 1650 .
The rated slip calculated by the formula is $60 \mathrm{~Hz}-(1650 \mathrm{rpm} \times 4 \div 120)=5 \mathrm{~Hz}$.This parameter is related to Pr.07.03 Slip Compensation Gain. To get the best slip compensation effect, you must enter the correct settings for this parameter. Incorrect settings may cause invalid slip compensation and even damage the motor and the AC motor drive.Related parameter: Pr.07.03 Slip Compensation Gain

### 07.07 Slip Compensation Limit

Default: 200
Settings 0-250\%Sets the upper limit of the compensation frequency (the percentage of Pr.07.06). If the motor speed is lower than the target speed and the speed does not change after adjusting the Pr. 07.03 setting, the AC motor drive may have reached the upper limit of the compensation frequency. In this case, increase the Pr. 07.07 setting, and then confirm again.Related parameters: Pr.07.03 Slip Compensation Gain, Pr.07.06 Motor Rated Slip (Motor 0)

### 07.08 Torque Compensation Low Pass Filter Time

Default: 0.1
Settings $\quad 0.01-10.00 \mathrm{sec}$.
This function is usually applied in applications with heavy load where the motor current changes frequently for the current compensation to increase the output torque. The frequent current change can cause machine vibration. In this case, increase the Pr. 07.08 setting to solve this problem.

### 07.09 Slip Compensation Low Pass Filter Time

Default: 0.2
Settings $0.05-10.00 \mathrm{sec}$.
[1] This function is usually applied in applications with heavy load where the motor speed changes frequently for the speed compensation to reach the synchronous speed. The frequent speed change can cause machine vibration. In this case, increase the Pr. 07.09 setting to solve this problem.

10 If you set Pr. 07.08 and Pr. 07.09 to 10 seconds, the compensation response time is the slowest; however, the system may be unstable if you set the time too short. It depends on actual conditions.

### 07.10 Accumulated Motor Operation Time

Default: 0
Settings 0: Disabled
00-1439 min.

### 07.11 Accumulated Motor Operation Time

Default: 0
Settings 00-65535 days
1 Use Pr. 07.10 and Pr. 07.11 to record the motor operation time. To clear the operation time, set Pr.07.10 and Pr.07.11 to 0 . An operation time shorter than 60 seconds is not recorded in Pr.07.10.

Chapter 10 Descriptions of Parameter Settings | LTC
07.12 Reserved
07.13 Reserved
07.14 Reserved
07.15 Reserved
07.16 Reserved
07.17 Reserved

## 08 Special Parameters

$\mathcal{N}$ You can set this parameter during operation.

### 08.00 DC Brake Current Level

Default: 0
Settings 0-100\%
Sets the level of the DC brake current output to the motor at start-up and stop. When setting the DC brake current, the rated current of the drive is $100 \%$. Therefore, when you set this parameter, increase the level slowly to reach the desired braking torque. But the maximum cannot be larger than rated current of the motor.

### 08.01 DC Brake Time At Start-up

Default: 0.0
Settings $0.0-60.0 \mathrm{sec}$.
Determines the duration of the DC brake current output to the motor when the drive starts up.

### 08.02 DC Brake Time At STOP

Default: 0.0
Settings $0.0-60.0 \mathrm{sec}$.
Determines the duration of the DC brake current during stopping. To enable the DC brake when the drive stops, you must set Pr. 02.02 (Motor Stop Method) to 0 and 2 (ramp to stop).

### 08.03 DC Brake Frequency at STOP

Default: 0.00
Settings $\quad 0.00-400.00 \mathrm{~Hz}$
$\lfloor$ Determines the starting frequency of the DC brake before the drive ramps to stop. When this setting is less than Pr. 01.05 (Minimum Output Frequency), the starting frequency for the DC brake begins at the minimum frequency.
Use the DC brake before running the motor when the load is movable at stop, such as with fans and pumps. The motor coasts and is in unknown rotation direction before the drive starts up. Execute the DC brake before you start the motor.
$\mathbb{1}$ Use the DC Brake at STOP when you need to brake the motor quickly or to control the positioning, such as with cranes or cutting machines.

### 08.04 Restart after Momentary Power Loss

Default: 0
Settings 0: Operation stops after momentary power loss
1: Operation continues after momentary power loss, speed tracking starts with the last frequency.

2: Operation continues after momentary power loss, speed tracking starts with the minimum frequency.
(1a) Determines the operation mode when the drive restarts from a momentary power loss.

### 08.05 Maximum Allowable Power Loss Time

Default: 2.0

## Settings $\quad 0.1-20.0 \mathrm{sec}$.

Determines the maximum time of allowable power loss. If the duration of a power loss exceeds this parameter setting, the AC motor drive stops output after the power recovers.Pr. 08.04 is valid when the maximum allowable power loss time is $\leq 5$ seconds and the AC motor drive displays i . If the AC motor drive is powered off due to overload, even if the maximum allowable power loss time is $\leq 5$ seconds, Pr. 08.04 is invalid after the power recovers.
### 08.06 Base Block Speed Tracking

Default: 1
Settings 0: Disabled
1: Speed tracking starts with the last frequency
2: Speed tracking starts with the minimum output frequency (Pr.01.05)
Determines the AC motor drive restart method after an external base block is enabled.



### 08.07 Base Block Time for Speed Tracking

Default: 0.5
Settings $0.1-5.0 \mathrm{sec}$.
1 When momentary power loss is detected, the AC motor drive blocks its output and then waits for a specified period of time (determined by Pr.08.07, called Base Block Time) before resuming operation. Set this parameter to the time that allows the residual voltage at the output side to decrease to 0 V before activating the drive again.
Ind This parameter also determines the waiting time before resuming operation after an external base block and Number of Times of Restart after Fault (Pr.08.15).

### 08.08 Current Limit for Speed Tracking

Default: 150
Settings 30-200\%
Limits the drive output current during a speed tracking.When executing a speed tracking, the V/F curve is defined by the settings in Parameter Group 01.


Timing Diagram of Restart after Momentary Power Loss
08.09 Skip Frequency 1 (Upper Limit)
08.10 Skip Frequency 1 (Lower Limit)
08.11 Skip Frequency 2 (Upper Limit)
08.12 Skip Frequency 2 (Lower Limit)
08.13 Skip Frequency 3 (Upper Limit)
08.14 Skip Frequency 3 (Lower Limit)

Default: 0.00
Settings $\quad 0.00-400.0 \mathrm{~Hz}$
Sets the AC motor drives skip frequency. The drives frequency setting skips these frequency ranges. However, the frequency output is continuous. Set these six parameters as follows Pr. $08.09 \geq$ Pr. $08.10 \geq$ Pr. $08.11 \geq$ Pr. $08.12 \geq$ Pr. $08.13 \geq$ Pr.08.14.


### 08.15 Number of Times of Restart after Fault

Default: 0
Settings 0-10

### 08.16 Auto-restart Interval of Fault

Default: 60
Settings $0.1-6000 \mathrm{sec}$.
1 Sets the times that the AC motor drive can reset and restart automatically after fault (allowed fault: oc, ov) is triggered.If Pr.08.15 is set to 0 , the drive does not reset or restart automatically after faults are triggered.
The drive starts speed tracking with the last frequency after restarting after fault.

Use this parameter with Pr.08.15. For example, when Pr.08.15=10 and Pr.08.16=600 seconds ( 10 minutes), the number of times of restart after fault will be automatically reset to 10 if no more fault occurs over 600 seconds since restart from previous fault.

### 08.17 Automatic Energy-saving

Default: 0

## Settings 0: Automatic energy-saving operation is disabled

1: Automatic energy-saving operation is enabled
[10] When energy-saving is enabled, the motor acceleration/deceleration operates with full voltage. During constant speed operation, it automatically calculates the best voltage value according to the load power. This function is not suitable for fluctuating loads or loads which are nearly full during operation.


### 08.18 Automatic Voltage Regulation (AVR)

Default: 0
Settings 0: AVR function is enabled
1: AVR function is disabled
2: AVR function is disabled during deceleration
3: AVR function is disabled at stop
[1. The rated voltage of a 220 V motor is usually $200 \mathrm{~V}_{\mathrm{AC}}, 60 \mathrm{~Hz} / 50 \mathrm{~Hz}$, and the input voltage of the AC motor drive may vary from $180-264 \mathrm{~V}_{\mathrm{AC}}, 50 \mathrm{~Hz} / 60 \mathrm{~Hz}$. Therefore, when the AC motor drive is used without the AVR function, the output voltage is the same as the input voltage. When the motor runs at the voltage exceeding $12-20 \%$ of the rated voltage, it causes higher temperatures, damaged insulation, and unstable torque output, which result in losses due to shorter motor lifetime.

In The AVR function automatically regulates the output voltage of the AC motor drive to the motor's rated voltage when the input voltage exceeds the motor's rated voltage. For example, if the V/F curve is set at $200 \mathrm{~V}_{\mathrm{AC}} / 50 \mathrm{~Hz}$ and the input voltage is at $200-264 \mathrm{~V}_{\mathrm{Ac}}$, then the drive automatically reduces the output voltage of the motor to a maximum of $200 \mathrm{~V}_{\mathrm{AC}} / 50 \mathrm{~Hz}$. If the input voltage is at $180-200 \mathrm{~V}_{\mathrm{AC}}$, the output voltage to motor is in direct proportion to the input voltage.
[1] When the motor ramps to stop, disabling the AVR function would shorten the deceleration time. Moreover, using with the auto-acceleration and auto-deceleration functions makes the motor's deceleration quicker.

### 08.19 Reserved

08.20 Vibration Suppression

Default: 0

## Settings $0.0-5.0$

[1] The drift current occurs in a specific frequency area of the motor and it causes serious motor vibration. It is recommended to use this parameter (the recommended value is 2.0) to greatly improve this situation. (The drift current zone for high-power motors is usually in the low frequency area.)

### 08.21 Reserved

08.22 Reserved
08.23 Reserved
08.24 Reserved
08.25 Reserved

## 09 Communication Parameters

$\wedge$ You can set this parameter during operation.

VFD1 and VFD2 RJ45 ports:

1. To connect to PC, use Delta IFD6500 as a communication converter (from RS-485 to USB).
2. To connect to panel, use digital keypad PU08 or PU08V.
3. PIN 8: EV (power Vcc port). Used as power and works only with optional digital keypad PU08 or PU08V.


Modbus RS-485
PIN 1, 2, 6, 7: Reserved

PIN 3: GND
PIN 4: SG-
PIN 5: SG+
PIN 8: EV
VFD2
RJ45
VFD1

### 09.00 Communication Address

Default: 1
Settings 1-254
Sets the communication address for the drive if the AC motor drive is controlled through RS-485 serial communication. The communication address for each AC motor drive must be unique.

### 09.01 Transmission Speed

Default: 2
Settings 0: Baud rate 4800 bps
1: Baud rate 9600 bps
2: Baud rate 19200 bps
Sets the transmission speed between the computer and the AC motor drive.

### 09.02 Transmission Fault Treatment

Default: 3
Settings 0: Warn and continue operation
1: Fault and ramp to stop
2: Fault and coast to stop
3: No warning, no fault and continue operation
Determines how the AC motor drive reacts if a transmission fault occurs.

### 09.03 Communication Time-out Detection

Default: 0
Settings $0.1-120.0 \mathrm{sec}$.
0.0: No function

1 Sets detection time of communication time-out for serial communications. If Pr. 09.03 is not equal to 0.0 , and Pr.09.02=0-2, and there is no communication on the bus during the timeout detection period (set by Pr.09.03), the digital keypad displays "cE10".

### 09.04 Communication Protocol

Default: 3

$$
\begin{array}{ll}
\text { Settings } & 0: 7, N, 2 \text { for } \mathrm{ASCII} \\
& \text { 1: } 7, \mathrm{E}, 1 \text { for } \mathrm{ASCII} \\
\text { 2: } 7, \mathrm{O}, 1 \text { for } \mathrm{ASCII} \\
\text { 3: } 8, \mathrm{~N}, 2 \text { for RTU } \\
\text { 4: } 8, \mathrm{E}, 1 \text { for RTU } \\
\text { 5: } 8, \mathrm{O}, 1 \text { for RTU } \\
\text { 6: } 8, \mathrm{~N}, 1 \text { for RTU } \\
\text { 7: } 8, \mathrm{E}, 2 \text { for RTU } \\
\text { 8: } 8, \mathrm{O}, 2 \text { for RTU } \\
\text { 9: } 7, \mathrm{~N}, 1 \text { for } \mathrm{ASCII} \\
\text { 10: } 7, \mathrm{E}, 2 \text { for } \mathrm{ASCII} \\
\text { 11: } 7, \mathrm{O}, 2 \text { for } \mathrm{ASCII}
\end{array}
$$

1 Control by PC (Computer Link): When using the RS-485 serial communication interface, each drive's communication address must be specified in Pr.09.00. The computer then implements control using the drives' individual addresses.
[10] LTC series drive uses Modbus network protocol. You can set a LTC drive to communicate over Modbus networks using one of the following modes: ASCII (American Standard Code for Information Interchange) or RTU (Remote Terminal Unit). Data transmitted using ASCII mode must be converted to ASCII code first before transmission; data transmitted using RTU mode can transfer directly without converting. See below for code descriptions for ASCII mode.

1. Code Description

The CPU delays about 1 second when using the communication reset; therefore, there is at least 1 second delay time in the master station.
Each data byte is the combination of two ASCII characters. For example, a 1-byte of data: 64 Hex, is shown as ' 64 ' in ASCII, and consists of ' 6 ' ( 36 Hex ) and ' 4 ' ( 34 Hex ). The communication protocol is in hexadecimal, ASCII: "0"..." 9 ", "A"..."F", every hexadecimal value represents an ASCII code. The table below shows some example.

| Character | '0' | '1' | '2' | '3' | '4' | '5' | '6' | '7' |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ASCII code | 30 H | 31H | 32 H | 33 H | 34 H | 35 H | 36H | 37H |
| Character | '8' | '9' | 'A' | 'B' | 'C' | 'D' | 'E' | 'F' |
| ASCII code | 38 H | 39H | 41H | 42H | 43H | 44H | 45H | 46H |

2. Data Format

For ASCII
(Format: 7, N, 2)

(Format: 7, E, 1)

(Format: 7, O, 1)

(Format: 7, N, 1)

(Format: 7, E, 2)

(Format: 7, O, 2)


## For RTU

(Format: 8, N, 2)

(Format: 8, E, 1)

(Format: 8, O, 1)

(Format: 8, N, 1)

(Format: 8, E, 2)

(Format: 8, O, 2)


## 3. Communication Protocol

### 3.1 Communication Data Frame

ASCII mode:

| STX | Start character = ':' (3AH) |
| :---: | :---: |
| Address High | Communication address: <br> one 8 -bit address consists of 2 ASCII codes |
| Address Low |  |
| Function High | Command code: <br> one 8-bit command consists of 2 ASCII codes |
| Function Low |  |
| DATA ( $\mathrm{n}-1$ ) | Contents of data: <br> $\mathrm{n} \times 8$-bit data consists of 2 n ASCII codes $\mathrm{n} \leq 20$, maximum of 40 ASCII codes (20 sets of data) |
| to |  |
| DATA 0 |  |
| LRC Check High | LRC checksum: one 8 -bit checksum consists of 2 ASCII codes |
| LRC Check Low |  |
| END High | End characters: <br> END High = CR (ODH), END Low = LF (OAH) |
| END Low |  |

RTU mode:

| START | Defined by a silent interval of larger than / equal to 10 ms |
| :---: | :--- |
| Address | Communication address: 8-bit binary address |
| Function | Command code: 8-bit binary command |
| DATA $(\mathrm{n}-1)$ | Contents of data: <br> to |
| DATA 0-bit data, $\mathrm{n} \leq 40$ (20 sets of 16-bit data) |  |
| CRC Check Low | CRC checksum: |
| CRC Check High | one 16-bit CRC checksum consists of 2 8-bit binary characters |
| END | Defined by a silent interval of larger than / equal to 10 ms |

3.2 Communication Address (Address)

00 H : broadcast to all AC motor drives
01 H : AC motor drive of address 01
OFH: AC motor drive of address 15
10H: AC motor drive of address 16 FEH: AC motor drive of address 254
3.3 Function (Function Code) and DATA (Data Characters)

03H: read data from a register
06 H : single write, write single data to a register
08H: Loop detection
(1) 03 H : read data from a register (can read at most 20 sets of data simultaneously)

Example: Reading two continuous data from register address 2102H, AMD address is 01 H .

ASCII mode:
Command Message

| STX | ' $'$ |
| :---: | :---: |
| Address | '0' |
|  | '1' |
| Function | '0' |
|  | '3' |
| Starting address | '2' |
|  | '1' |
|  | '0' |
|  | '2' |
| Number of data (count by word) | '0' |
|  | '0' |
|  | '0' |
|  | '2' |
| LRC Check | 'D' |
|  | '7' |
| END | CR |
|  | LF |


| Response Message |  |
| :---: | :---: |
| STX | ':' |
| Address | '0' |
|  | '1' |
| Function | '0' |
|  | '3' |
| Number of data (count by byte) | '0' |
|  | '4' |
| Content of starting address$2102 \mathrm{H}$ | '1' |
|  | '7' |
|  | '7' |
|  | '0' |
| Content of address 2103H | '0' |
|  | '0' |
|  | '0' |
|  | '0' |
| LRC Check | '7' |
|  | '1' |
| END | CR |
|  | LF |

RTU mode:

| Command Message |  |
| :---: | :---: |
| Address | 01 H |
| Function | 03 H |
| Starting data address | 21 H |
|  | 02 H |
|  | 00 H |
| CRC Check Low | 02 H |
| CRC Check High | 6 FH |

Response Message

| Address | 01 H |
| :---: | :---: |
| Function | 03 H |
| Number of data <br> (count by byte) | 04 H |
| Content of data address <br> 2102 H | 17 H |
|  | 70 H |
| Content of data address <br> 2103 H | 00 H |
| CRC Check Low | 00 H |
| CRC Check High | FEH |

(2) 06 H : single write, write single data to a register (can write at most 20 sets of data simultaneously)
Example: Writing data $6000(1770 \mathrm{H})$ to register 0100 H . AMD address is 01 H .
ASCII mode:

Command Message

| STX | $\because$ |
| :---: | :---: |
| Address | '0' |
|  | '1' |
| Function | '0' |
|  | '6' |
| Data address | '0' |
|  | '1' |
|  | '0' |

Response Message

| STX | $\because$ |
| :---: | :---: |
| Address | '0' |
|  | '1' |
| Function | '0' |
|  | '6' |
| Data address | '0' |
|  | '1' |
|  | '0' |


|  | ' 0 ' |  | '0' |
| :---: | :---: | :---: | :---: |
| Data content | '1' | Data content | '1' |
|  | '7' |  | '7' |
|  | '7' |  | '7’ |
|  | '0' |  | '0' |
| LRC Check | '7' | LRC Check | ‘7’ |
|  | '1' |  | '1' |
| END | CR | END | CR |
|  | LF |  | LF |

RTU mode:

Command Message

| ADR | 01 H |
| :---: | :---: |
| CMD | 08 H |
| Data | 00 H |
|  | 00 H |
| Data | 17 H |
|  | 70 H |
| CRC Check High | 8 EH |

Response Message

| ADR | 01 H |
| :---: | :---: |
| CMD | 08 H |
| Data | 00 H |
|  | 00 H |
|  | Data |
|  |  |
| CRC Check Low | 70 H |
| CRC Check High | 8 EH |

### 3.4 Checksum

ASCII mode (LRC Check):
LRC (Longitudinal Redundancy Check) is calculated by summing up the values of the bytes from ADR1 to the last data character then calculating the hexadecimal representation of the 2'scomplement negation of the sum. For example, as shown in the above Section 3.3.(1): $01 \mathrm{H}+$ $03 \mathrm{H}+21 \mathrm{H}+02 \mathrm{H}+00 \mathrm{H}+02 \mathrm{H}=29 \mathrm{H}$, the 2 's-complement negation of 29 H is D 7 H .
RTU mode (CRC Check):
Starts from Address and ends at Data Content. It is calculated by the following steps:
Step 1: Load a 16-bit register (called CRC register) with FFFFh.
Step 2: Exclusive OR the first 8-bit byte of the command message with the low order byte of the 16-bit CRC register, putting the result in the CRC register.
Step 3: Examine the LSB of CRC register.
Step 4: If the LSB of CRC register is 0 , shift the CRC register one bit to the right, fill MSB with zero, then repeat step 3 . If the LSB of CRC register is 1 , shift the CRC register one bit to the right, fill MSB with zero, Exclusive OR the CRC register with the polynomial value A001H, then repeat step 3 .
Step 5: Repeat step 3 and 4 until you perform eight shifts. This processes a complete 8 -bit byte.
Step 6: Repeat step 2 through 5 for the next 8-bit byte of the command message. Continue doing this until all bytes are processed. The final contents of the CRC register are the CRC value. When transmitting the CRC value in the message, the upper and lower bytes of the CRC value must be swapped, that is, the lower order byte is transmitted first.

The following is an example of CRC generation using $C$ language. The function takes two arguments:

```
unsigned char* data <// a pointer to the message buffer
unsigned char length <// the quantity of bytes in the message buffer
unsigned int crc_chk(unsigned char* data, unsigned char length)
{
int j;
unsigned int reg_crc=0xffff;
while(length--){
    reg_crc ^= *data++;
    for(j=0;j<8;j++){
            if(reg_crc & 0x01){ /* LSB(b0)=1 */
            reg_crc=(reg_crc>>1) ^ 0xa001;
            }else{
                    reg_crc=reg_crc >>1;
            }
        }
}
return reg_crc; // return register to CRC
```

4. Address list

| Content | Address | Function |  |
| :---: | :---: | :---: | :---: |
| AC motor drive parameters | GGnnH | GG means parameter group, nn means parameter number. For example, the address of Pr. 04.01 is 0401 H . |  |
| Operation command | 2000H | bit0-1 | OOB: No function |
|  |  |  | 01B: Stop |
|  |  |  | 10B: Run |
|  |  |  | 11B: JOG + RUN |
|  |  | bit2-3 | Reserved |
|  |  | bit4-5 | 00B: No function |
|  |  |  | 01B: FWD |
|  |  |  | 10B: REV |
|  |  |  | 11B: Change direction |
|  |  | bit6-7 | O0B: 1st step accel./decel. |
|  |  |  | 01B: 2nd step accel./decel. |
|  |  | bit8-15 | Reserved |
|  |  | Frequency command |  |
|  |  | bit0 | 1: External Fault (E.F.) ON |
|  |  | bit1 | 1: Reset |
|  |  | bit2 | 1: Base Block (B.B.) ON |
|  |  | $\begin{gathered} \hline \text { bit3-4 } \\ \text { bit6-15 } \end{gathered}$ | Reserved |
| Fault status | 2100 H | Fault code: |  |
|  |  | 0 : No fault record |  |
|  |  | 1: Over-current (oc) |  |
|  |  | 2: Over-voltage (ov) |  |



| Content | Address | Function |  |
| :---: | :---: | :---: | :---: |
|  |  |  | 10B: Drive is in standby status <br> (RUN indicator is ON / STOP indicator flashes) <br> 11B: Drive is running <br> (RUN indicator is ON / STOP indicator is OFF) |
|  |  | bit2 | 1: JOG command |
|  |  | bit3-4 | 00B: Drive runs forward <br> (FWD indicator is ON / REV indicator is OFF) <br> 01B: Drive runs from reverse to forward <br> (FWD indicator is ON / REV indicator flashes) <br> 10B: Drive runs from forward to reverse <br> (FWD indicator flashes / REV indicator is ON) <br> 11B: Drive runs in reverse <br> (FWD indicator is OFF / REV indicator is ON) |
|  |  | bit5-7 | Reserved |
|  |  | bit8 | 1: Master frequency controlled by the communication interface |
|  |  | bit9 | 1: Master Frequency command controlled by analog signal input |
|  |  | bit10 | 1: Operation command controlled by the communication interface |
|  |  | bit11-15 | Reserved |
|  | 2102H | Frequency command (F) |  |
|  | 2103H | Output frequency (H) |  |
|  | 2104H | Output current (AXX.X) |  |
|  | 2105H | Reserved |  |
|  | 2106H | Reserved |  |
|  | 2107H | Reserved |  |
|  | 2108H | DC bus voltage (uXXX. ${ }^{\text {( }}$ ) |  |
|  | 2109H | Output voltage (EXXX.X) |  |
|  | 210AH | IGBT temperature ( ${ }^{\circ} \mathrm{C}$ ) |  |
|  | 2116 H | User-defined (Low word) |  |
|  | 2117H | User-defined (High word) |  |

## NOTE:

2116H is the data value of Pr.00.04, 2117H high byte is the decimal places of Pr.00.04, and low byte is the ASCII code of Pr.00.04.

## Exception response

When the drive is using the communication connection, if an error occurs, the drive responds to the error code and sets the highest bit (bit 7) of the command code to 1 (function code AND 80 H ) then responds to the control system to signal that an error occurred. If the keypad displays "CE.XX" as a warning message, " XX " is the error code at that time. Refer to the table of error codes below for communication error for reference.
The table below shows some example.

| ASCII mode: |  | RTU mode: |  |
| :---: | :---: | :---: | :---: |
| STX | ' $'$ | Address | 01H |
|  | '0' | Function | 86H |
| Address | '1' | Exception code | 02H |
| Function | '8' | CRC CHK Low | C 3 H |
| Function | '6' | CRC CHK High | A1H |
| Exception | '0' |  |  |
| Exception code | '2' |  |  |
| IRC | '7' |  |  |
|  | '7' |  |  |
| END | CR |  |  |
| END | LF |  |  |

The explanation of exception codes:

| Error code | Description |
| :---: | :--- |
| 01 | Function code error: recognized function codes by the drive are 03H, <br> $06 \mathrm{H}, 08 \mathrm{H}$, and 10H. |
| 02 | Incorrect data address: unrecognized data address by the drive. |
| 03 | Incorrect data content: data content is too large, and cannot be <br> recognized by the drive. |
| 04 | Unable to be processed by the drive: commands cannot be executed <br> by the drive. |
| 10 | Transmission time-out. |

Communication program of PC:
The following is a simple example of how to write a communication program for Modbus ASCII mode on a PC by C language.
\#include<stdio.h>
\#include<dos.h>
\#include<conio.h>
\#include<process.h>
\#define PORT 0x03F8 /* the address of COM1 */
/* the address offset value relative to COM1 */
\#define THR 0x0000
\#define RDR 0x0000
\#define BRDL 0x0000
\#define IER 0x0001
\#define BRDH 0x0001
\#define LCR 0x0003
\#define MCR 0x0004
\#define LSR 0x0005
\#define MSR 0x0006
unsigned char rdat[60];
/* read 2 data from address 2102 H of AC drive with address 1 */
unsigned char tdat[60]=\{:,0,1,0,3,2,1,0, '2, 0,0,0,2,D,7, \r, In\};
void main()\{
int $i$;
outportb(PORT+MCR,0x08); /* interrupt enable */
outportb(PORT+IER,0x01); /* interrupt as data in */
outportb(PORT+LCR,(inportb(PORT+LCR) | 0x80));
/* the BRDL/BRDH can be access as LCR.b7==1 */
outportb(PORT+BRDL,12); /* set baudrate=9600, 12=115200/9600*/
outportb(PORT+BRDH,0x00);
outportb(PORT+LCR,0x06); /* set protocol, <7,N,2>=06H,
$<7, E, 1>=1$ AH
$<7, \mathrm{O}, 1>=0 \mathrm{AH}$,
$<8, N, 2>=07 \mathrm{H}$,
$<8, \mathrm{E}, 1>=1 \mathrm{BH}$,
$<8, \mathrm{O}, 1>=0 \mathrm{BH}$
*/

```
    for (i=0;i<=16;i++)\{
        while(!(inportb(PORT+LSR) \& 0x20)); /* wait until THR empty */
        outportb(PORT+THR,tdat[i]); /* send data to THR */ \}
    \(\mathrm{i}=0\);
    while(!kbhit())\{
        if(inportb(PORT+LSR) \& 0x01) \{ /* b0==1, read data ready */
            rdat[i++]=inportb(PORT+RDR); /* read data form RDR */
    \} \(\} \quad\}\)
```


### 09.05 Reserved <br> 09.06 Reserved <br> 09.07 Communication Response Delay Time

Default: 1.0
Settings $\quad 0.0-200.0 \mathrm{~ms}$ (One unit: 2 ms )
(D) If the host controller does not finish the transmitting/receiving process, you can use this parameter to set the response delay time after the AC motor drive receives communication command as shown in the following picture.


## 10 Speed Feedback Control Parameters

$\mathcal{N}$ You can set this parameter during operation.
Parameter Group 10, from Pr. 10.00 to Pr.10.50, are all "reserved".
10.00

Reserved
10.50
[This page intentionally left blank]

## Chapter 11 Troubleshooting

For users to easily find common errors and solutions
11-1 Over-current (oc)
11-2 Over-voltage (ov)
11-3 Low voltage (Lv)
11-4 Overheat ( oH 1 )
11-5 Overload (oL)
11-6 Keypad Display is Abnormal
11-7 Phase Loss (PHL)
11-8 Motor Does Not Run
11-9 Motor Speed Cannot be Changed
11-10 Motor Stalls During Acceleration
11-11 Motor is Abnormal
11-12 Electromagnetic / Induction Noise
11-13 Operating Environment Condition
11-14 Affecting Other Machines
11-15 Indicator Description
$\square$ Only qualified personnel can work on troubleshooting to prevent accidents.

## Chapter 11 Troubleshooting | LTC

## 11-1 Over-current (oc)



## 11-2 Over-voltage (ov)



## 11-3 Low voltage (Lv)



## 11-4 Overheat (oH1)



## 11-5 Overload (oL)

Drive overloads (oL)


## 11-6 Keypad Display is Abnormal



## 11-7 Phase Loss (PHL)



## 11-8 Motor Does Not Run



## 11-9 Motor Speed Cannot be Changed



## 11-10 Motor Stalls During Acceleration



## 11-11 Motor is Abnormal



## 11-12 Electromagnetic / Induction Noise

Many sources of noise surround AC motor drives and affect them by radiation or conduction. This may cause the control circuits to malfunction and even trip or damage the AC motor drive. Of course, there are solutions to increase the noise tolerance of an AC motor drive, but this has its limits. Solving the problem from the outside as follows is the best.

1. Add a surge suppressor on relays or contactors to suppress noise caused by switching surges during switch ON and OFF.
2. Shorten the length of the wiring for the control circuit or serial communication and keep them separated from the power circuit wiring.
3. Comply with the wiring regulations by using shielded wires and isolation amplifiers for long wire length.
4. The grounding terminal must comply with the local regulations and be grounded independently; that is, do not use a common ground with electric welding machines and other power equipment.
5. Connect a noise filter at the mains input terminal of the AC motor drive to filter noise from the power circuit.

In short, solutions for electromagnetic noise exist of "no product" (disconnect disturbing equipment), "no spread" (limit emission from disturbing equipment) and "no receive" (enhance immunity).

## 11-13 Operating Environment Condition

Since the AC motor drive is an electronic device, you must deal with the operating environment conditions. Here are some remedial measures to use if necessary.

1. To prevent vibration, anti-vibration dampers are the last choice. Vibration must be within the specification. Vibration causes mechanical stress and it should not occur frequently, continuously or repeatedly to prevent damage to the AC motor drive.
2. Store the AC motor drive in a clean and dry location, free from corrosive fumes/dust to prevent corrosion and poor contacts. Poor insulation in a humid location can cause short circuits. If necessary, install the AC motor drive in a dust-proof and painted enclosure. If necessary in particular situations, use a completely sealed enclosure.
3. The ambient temperature should be within the specification. Too high or too low temperature affects the lifetime and reliability of the AC motor drive. For semiconductor components, damage occurs once any specification is out of range. It is necessary to periodically check air quality and the cooling fan and provide extra cooling if required. In addition, the microcomputer may not work in extremely low temperatures, making cabinet heating necessary.
4. Store the AC motor drive in a relative humidity range of $0 \%$ to $90 \%$ (non-condensing). Use an air conditioner and/or desiccator if necessary.

## 11-14 Affecting Other Machines

An AC motor drive may affect the operation of other machines due to many reasons. Some solutions are listed below:

High Harmonics at Power Side
You can reduce high harmonics at the power side during running.

1. Separate the power system: use a transformer for the AC motor drive.
2. Use a reactor at the power input terminal of the AC motor drive, as the figure below shows.

3. If using phase lead capacitors (never on the AC motor drive output!), use serial reactors to prevent damage to the capacitors from high harmonics.

## Motor Temperature Rises

When the motor is a standard induction motor with a fan, the cooling will be insufficient at low speed, causing the motor to overheat. In addition, high harmonics at the output increases copper and core losses. Use the following measures depending on load and operation range.

1. Use a motor with independent ventilation (forced external cooling) or increase the motor rated power.
2. Use an inverter-duty motor.
3. Do NOT run at low speed for long periods of time.

## 11-15 Indicator Description

| No. | LED Indicator Name | Color | Description |
| :---: | :---: | :---: | :---: |
| 1 | POWER | Green | ON: LTC is powered with three-phase power |
| 2 | PLC | Green | OFF: PLC stops running |
| 3 | ERROR | Red | 1. ON: PLC error <br> 2. Flashes ( ON for 1 s and OFF for 1s) in circulation: Belt conveyor motor drive error <br> 3. Flashes ( ON for 2 s and OFF for 0.5 s ) in circulation: Telescopic motor drive error Error indication priority: PLC error > belt conveyor motor drive error > telescopic motor drive error |
| 4 | FWD (left side) | Green | ON: The running direction that VFD1 corresponds to the motor is forward |
| 5 | REV (left side) | Green | ON: The running direction that VFD1 corresponds to the motor is reverse |
| 6 | FWD (right side) | Green | ON: The running direction that VFD2 corresponds to the motor is forward |
| 7 | REV (right side) | Green | ON: The running direction that VFD2 corresponds to the motor is reverse |
| 8 | DI input ( X contacts) | Green | ON: X0 to X20, corresponding DI signal inputs are valid |
| 9 | DO output (Y contacts) | Green | ON: YO to Y7, corresponding DO signal outputs are valid. Among them, $\mathrm{Y} 7-1$ and $\mathrm{Y} 7-2$ use the same relay, that is, they use only one LED indicator to indicate ON/OFF. |
| 10 | DC POWER | Green | ON : Power 24 V VC is normal |

## Chapter 12 Fault Codes and Maintenance

12-1 Faults and Corrective Action
12-2 Fault Codes and Descriptions
12-3 Maintenance and Inspections

The AC motor drive has various warnings and protections against errors such as over-voltage, low voltage, or over-current. Once an error occurs, the protections activate, the AC motor drive stops output, activates the error contacts, and the motor coasts to stop. Please refer to the warning/fault display from the AC motor drive and look up the corresponding causes and corrective actions in this chapter. The fault record is stored in the AC motor drive internal memory and can store the five most recent error messages. You can read it from the digital keypad or through the communications by accessing the parameters.

The AC motor drive includes a large number of electronic components, including ICs, resistors, capacitors, transistors, and cooling fans. These components do not last forever. Even under normal circumstances, they will eventually become error-prone if used past their lifespans. Therefore, you must perform periodic preventive maintenance to identify defective and worn out parts, and eliminate the causes of malfunctions in the AC motor drive at an early stage. At the same time, parts that have exceeded their product life should be replaced whenever possible to ensure safe operation.

Visual checks should be done regularly to monitor the AC motor drives operation, and to make sure nothing unusual happens. Check the situations listed in the following table.


CAUTION
$\boxtimes$ Wait five seconds after a fault has been cleared before pressing RESET key on the keypad.
$\boxtimes$ The drive must first be switched off for at least five minutes for $\leq 22 \mathrm{~kW}$ models until the charging indicator turns off.

च Only qualified personnel can work on maintenance or replace parts. (Remove metal items such as watch, rings, and other metal items before operation, and use only insulated tools.)
$\square$ Never modify internal components or wiring.
$\nabla$ The performance and the surrounding environment should meet the standard specifications. There should be no abnormal noise, vibration, or odor.
$\boxtimes$ Verify if the keypad displays normally. Check if there is any abnormality such as overheating or color change. Prevent the drive from electric shock and equipment accident.

## 12-1 Faults and Corrective Action

| No. | Fault Descriptions | Corrective Actions |
| :---: | :---: | :---: |
| 1 | ERROR indicator flashes with red light when LTC is powered ON. | 1. Check if EF fault occurs by using VFDSoft software, communication panel PU08 or PU08V. <br> 2. If $E F$ fault occurs, make sure that EF terminal is correctly connected to emergency stop circuit (channels between 24 V (Digital signal common terminal Source) - emergency stop switch N.C. contact - EF terminal can be connected in series). <br> 3. If EF function is unnecessary for your application, set Pr. 04.07 of both VFD1 and VFD2 to 0 by using VFDSoft software, communication panel PU08 or PU08V. <br> 4. If it is not an EF fault, see other fault corrective actions in this user manual. |
| 2 | Contact Y does not light on, does not output, and cannot drive the load. | 1. Check if PLC program runs normally. <br> 2. Check if the relay is correctly installed. <br> 3. Replace with a new one if the relay is malfunctioned. |
| 3 | Contact Y lights on, but does not output. | 1. Check if the relay is correctly installed. <br> 2. Replace with a new one if the relay is malfunctioned. |
| 4 | Motor 1 or Motor 2 does not run. | Connect VFDSoft software, communication panel PU08 or PU08V with communication interface (RJ45) of VFD1 and VFD2 respectively to check the fault of the drive. Troubleshoot the fault (such as mechanism, electricity) according to the descriptions in Chapter 11 and 12 in LTC user manual. Then, press RESET key on the keypad. |

## 12-2 Fault Codes and Descriptions

The following messages display when the operation command source is set as digital keypad.

| Fault Code | Fault Descriptions | Corrective Actions |
| :--- | :--- | :--- |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :---: | :---: |
| 018 | Overload 2 （oL2） <br> Motor overload． | Check if the motor is overloaded． <br> Adjust the over－torque detection settings to appropriate values（Pr．06．03－Pr．06．05）． |
| H9E： | Hardware protection fault 2 （HPF1） Abnormal hardware protection wiring of the controller． | CC，OC（current clamp）abnormal hardware protection wiring，return the unit to the factory． |
| H9E\％ | Hardware protection fault 2 （HPF2） Abnormal hardware protection wiring of the controller． | OV abnormal hardware protection wiring， return the unit to the factory． |
| 4 OF | Hardware protection fault 2 （HPF4） Abnormal hardware protection wiring of the controller． | OC abnormal hardware protection wiring， return the unit to the factory． |
| 日に年 | Over－current during acceleration （ocA） | Check for loose contacts between the AC motor drive and the motor． <br> Check for poor insulation wiring from U－V－W to the motor． <br> Increase the acceleration time <br> Reduce the torque compensation setting （Pr．07．02）． <br> Replace the drive with a larger capacity model． |
| ロロロ | Over－current during deceleration （ocd） | Check for poor insulation wiring from U－V－W to the motor． <br> Increase the deceleration time． <br> Replace the drive with a larger capacity model． |
| ロロロ | Over－current during steady operation （ocn） | Check for poor insulation wiring from U－V－W to the motor． <br> Check for possible shaft lock． <br> Replace the drive with a larger capacity model． |
| $E F$ | External Fault（EF） <br> 1．When multi－function input terminals （MI1－MI4）are set to external fault，the AC motor drive stops output． <br> 2．When changing the communication address 2002 H bit0 $=1$ ，the drive stops output． | The＂EF＂disappears once the signal source is cleared and reset． |
| EF | EEPROM write error（cF1．0） Internal EEPROM cannot be programmed． | Return to the factory for repair． |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :---: | :---: |
| EFit | EEPROM write error（cF1．1） Internal EEPROM cannot be programmed． | Return to the factory for repair． |
| EFEM | EEPROM read error（cF2．0） <br> Internal EEPROM cannot be read． | Press RESET key to reset all parameters to defaults． <br> If this solution does not work，return to the factory for repair． |
| EFI | EEPROM read error（cF2．1） <br> Internal EEPROM cannot be read． | Press RESET key to reset all parameters to defaults． <br> If this solution does not work，return to the factory for repair． |
| 二Fう！ | Drive wiring detection fault（cF3．0） | U－phase error，return to the factory for repair． |
| E\％\％ | Drive wiring detection fault（cF3．1） | V－phase error，return to the factory for repair． |
| E\％ 5 | Drive wiring detection fault（cF3．2） | W－phase error，return to the factory for repair． |
| －5\％ | Drive wiring detection fault（cF3．3） | DC bus wiring detection error，return to the factory for repair． |
| EF9\％ | Drive wiring detection fault（cF3．4） | Temperature sensor error，return to the factory for repair． |
| EF9 | Auto－acceleration／deceleration failure （cFA） | Check if the drive capacity matches the motor＇s． <br> Check if the regenerative energy is too high． Check for sudden load changes． |
| $E E-$ | Communication fault（cE－－） | Check the RJ45 connection between the AC motor drive for loose wires and wiring to the correct pins． <br> Check if the communication format is correct． See the table of explanation of exception codes in Parameter Group 09 Communication Parameters in Chapter 10 for detailed information． |
| ニ日 E | Software protection enabled（codE） | Password is locked． |
| Fi－1！ | Phase loss protection（PHL） | Check if the input power is three－phase． |
| OP\％ | Multi－motor fault protection（oPHL） | Check if the motor wiring is normal． |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :--- | :--- |
|  |  | Check if the input voltage is within the rated <br> AC motor drive input voltage range, and <br> check for possible voltage spikes. <br> If the phase-in capacitor or active power <br> supply unit acts in the same power system, <br> the input voltage may surge abnormally in a <br> short time. In this case, install an AC reactor. <br> Verify the wiring of the control circuit and the <br> wiring/grounding of the main circuit to prevent <br> interference. <br> Check if other fault codes such as cF3.0- |
| CF3.2 occur after cycling the power. If yes, |  |  |
| return to the factory for repair. |  |  |
| The ground short circuit current charges the |  |  |
| capacitor in the main circuit through the |  |  |
| power. Check if there is ground fault on the |  |  |
| motor cable, wiring box and its internal |  |  |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :--- | :--- |
|  |  | Verify the wiring of the control circuit and the <br> wiring/grounding of the main circuit to prevent <br> interference. |
| Over-voltage during deceleration | Increase the setting values for Pr.01.10 and <br> Pr.01.12 (deceleration time). <br> Reduce the brake frequency. <br> Use S-curve acceleration/deceleration. <br> Use over-voltage stall prevention function <br> (Pr.06.00). <br> Use Auto-acceleration and Auto-deceleration <br> Setting (Pr.01.16) <br> Adjust the brake level (Pr.08.00). <br> Check if the input voltage is within the rated <br> AC motor drive input voltage range, and <br> check for possible voltage spikes. <br> If the phase-in capacitor or active power <br> supply unit acts in the same power system, |  |
| the input voltage may surge abnormally in a |  |  |
| short time. In this case, install an AC reactor. |  |  |
| The ground short circuit current charges the |  |  |


| Fault Code | Fault Descriptions | Corrective Actions |
| :---: | :--- | :--- |
|  | Verify the wiring of the control circuit and the <br> wiring/grounding of the main circuit to prevent <br> interference. |  |
| Belt conveyor drive (VFD1) stall | Stall function is enabled for VDF1 of LTC. If <br> materials are jammed and stall conditions are <br> met, fault code StAL is triggered. |  |

Reset
After the causes that may trigger fault codes are corrected, execute either of the following actions to clear the trip: 1. Press RESET key on the panel (as shown in the figure below); 2. Set the external terminal to "fault reset command" and set this terminal to be ON; 3. Send the fault reset command through communications. Make sure the RUN command or signal is OFF before executing RESET to prevent damage to the drive or personal injury due to immediate operation after reset.


Digital Keypad Panel

## 12-3 Maintenance and Inspections

For regular maintenance, first stop operation, then turn off the power, and then take off the outer cover. Even after turning off the power supply, charging voltages remaining in the filter capacitor require some time to discharge. To avoid danger, operation must not start until the charging indicator goes off, and you confirm the voltage with a voltmeter to be below the safety value ( $\leq 25 \mathrm{~V}_{\mathrm{DC}}$ ).

## Ambient environment

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check the ambient temperature, humidity, and vibration and <br> check for any dust, gas, oil or water drops. | Visual inspection and <br> measurement with equipment <br> with standard specification | $\bigcirc$ |  |  |
| Check for any dangerous objects | Visual inspection | $\bigcirc$ |  |  |

## Voltage

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check that the voltage of main circuit and control circuit are <br> correct. | Measure with multimeter with <br> standard specifications | $\bigcirc$ |  |  |

## Digital keypad display

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check that the display is clear for reading. | Visual inspection | $\bigcirc$ |  |  |
| Check for any missing characters |  | $\bigcirc$ |  |  |

## Mechanical parts

|  |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Items to Check | Methods and Criterion | Daily | Half <br> Year |
| One <br> Year |  |  |  |  |
| Check for any abnormal sound or vibration |  | Visual and audible inspection |  | $\bigcirc$ |
| Check for any loose bolts (firm parts) | Securely tighten |  | $\bigcirc$ |  |
| Check for any deformed or damaged parts | Visual inspection |  | $\bigcirc$ |  |
| Check for any color change caused by overheating | Visual inspection |  | $\bigcirc$ |  |
| Check for any dust or dirt | Visual inspection |  | $\bigcirc$ |  |

Main circuit

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check for any loose or missing bolts |  | Securely tighten | $\bigcirc$ |  |
| Check for machine or insulator deformation, crack, damage <br> or color change due to overheating or aging | Visual inspection |  | $\bigcirc$ |  |
| Check for any dust or dirt | Visual inspection |  | $\bigcirc$ |  |

## Main circuit terminals and wiring

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check the terminal and copper plate for any color change or <br> deformation caused by overheating | Visual inspection |  |  |  |
| Check for damage to the wiring insulation or color change | Visual inspection |  | $\bigcirc$ |  |

## Main circuit terminal block

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :--- | :---: | :---: |
|  |  | Methods and Criterion | Daily | Half <br> Year |
| One <br> Year |  |  |  |  |
| Check for any damage |  |  | Oisual inspection |  |

## Main circuit filter capacitor

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :--- | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check for any liquid leaks, color change, crack or buckling <br> of the exterior cover | Visual inspection | 0 |  |  |
| Check if the safety valve is not removed. Check if the valve <br> is obviously expanded. | Visual inspection | $O$ |  |  |
| Measure static capacity when required |  |  | $\bigcirc$ |  |

## Main circuit resistor

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check for any odors or insulation cracks due to overheating | Visual and audible inspection |  | $\bigcirc$ |  |
| Check for any disconnections | Visual inspection |  | $\bigcirc$ |  |
| Check for damaged connections | Measure with multimeter with <br> standard specifications |  | $\bigcirc$ |  |

Main circuit transformer and reactor

| Items to Check |  | Maintenance Period |  |  |
| :--- | :---: | :---: | :---: | :---: |
|  | Methods and Criterion | Daily | Half <br> Year | One <br> Year |
| Check for any abnormal vibration or odors | Visual and audible inspection | O |  |  |

## Main circuit magnetic contactor and relay

| Items to Check | Maintenance Period |  |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  |  | Half <br> Year |  | One <br> Year |
| Check for any sound of vibration while running | Audible inspection | $\bigcirc$ |  |  |
| Check that the contact works correctly | Visual inspection | $\bigcirc$ |  |  |

Main circuit PCB and connector

| Items to Check |  | Maintenance Period |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | Methods and Criterion |  | Daily | Half <br> Year |
| One <br> Year |  |  |  |  |
| Check for any loose screws and connectors |  | Securely tighten |  |  |
| Check for any odors and color change | Visual inspection |  | $\bigcirc$ |  |
| Check for any crack, damage, deformation or corrosion | Visual inspection |  | $\bigcirc$ |  |
| Check for any liquid leaks or deformation in capacity | Visual inspection |  | $\bigcirc$ |  |

## Cooling system cooling fan

| Items to Check | Methods and Criterion | Maintenance Period |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Daily | Half <br> Year | One <br> Year |
| Check for any abnormal sound or vibration | Audible, visual inspection, and turn the fan by hand to see if it rotates smoothly. (turn off the power before inspection) |  |  | $\bigcirc$ |
| Check for any loose bolts | Securely tighten |  |  | $\bigcirc$ |
| Check for any color change caused by overheating | Visual inspection |  |  | $\bigcirc$ |

## Cooling system ventilation channel

| Items to Check | Maintenance Period |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | Daily | Half <br> Year | One <br> Year |
| Check for any obstruction or substance adhered in the heat <br> sink, air intake or air outlet | Audible inspection |  | 0 |  |

NOTE: Use a chemically neutral cloth to clean and use a dust cleaner to remove dust when necessary.

# Chapter 12 Fault Codes and Maintenance | LTC 

[This page intentionally left blank]

## Chapter 13 PLC Function Applications

## 13-1 PLC Summary

13-2 Notes Before Using PLC
13-3 Start-up
13-4 Basic Principles of PLC Ladder Diagrams
13-5 Various PLC Device Functions
13-6 Introduction to the Command Window
13-7 Fault Display and Treatment

## 13-1 PLC Summary

## 13-1-1 Introduction

The commands provided by the LTC's built-in PLC functions, including the ladder diagram editing tool WPLSoft, as well as the usage of basic commands and applications commands, mainly retain the operating methods of Deltas PLC DVP series.

## 13-1-2 WPLSoft Ladder Diagram Editing Tool

WPLSoft is a program editing software used under WINDOWS operating system in Delta's DVP Series PLC for VFD-C2000 and VFD-LTC. WPLSoft not only provides functions of PLC program planning and Windows editing (such as cut, paste, copy, multi-window, etc.), but also Chinese/English notes editing function and other useful functions like register editing \& setting, file reading \& saving, as well as points diagram monitoring and setting, and so on.

Minimum system requirements for installing WPLSoft software:

| Item | System Requirements |
| :---: | :--- |
| Operating System | Windows $95 / 98 / 2000 /$ NT / ME / XP / 10 |
| CPU | Pentium 90 above |
| Storage | 16 MB above (32MB above recommended) |
| Drive | Disk space: 100 MB above at a minimum <br> An optical disc drive (for installing WPLSoft) |
| Display | Resolution: $800 \times 600,16$ colors above. It is recommended to set screen <br> width $\times$ height to $800 \times 600$ pixels. |
| Mouse | Mouse for general purposes or compatible with Windows |
| Printer | Printers with Windows drivers |
| RS-485 port | At least one RS-485 port that can be connected with PLC |

## 13-2 Notes Before Using PLC

1. LTC provides two communication ports to upload/download PLC programs, as the figure below shows. Communication format for Channel 1 is ASCII (serial baud rate is at random).
Communication format for Channel 2 is 19200, 8, N, 2 (RTU), and communication station address is 1 .

© Main circuit terminals
O Control circuit terminals
The communication station address, serial baud rate, and communication format of SG+ and SGterminals (i.e. Channel 2) for LTC's PLC can be changed as required. See below for detailed descriptions.
(1) D1290 = 0 (default): D1295-1297 are fixed values, D1295 (station address) = 1, D1296
(communication serial baud rate) $=19200 \mathrm{bps}$, and D1297 (communication format) $=13$ ( 8 , N, 2, RTU).
(2) Change D1290 from 0 to 1 , then D1295-1297 can be changed.
(2.1) Change station address as required
(2.2) Change serial baud rate from $4.8 \mathrm{k}, 9.6 \mathrm{k}, 19.2 \mathrm{k}, 38.4 \mathrm{k}, 57.6 \mathrm{k}$ to 115.2 k . If the setting value of D1296 is larger than 1152, the setting value will be automatically set as the default value 192 (that is, 192 kbps ).
(2.3) Set communication format for D1297 based on the following protocols. If the setting value of D1297 is larger than 17, the setting value will be automatically set as the default value 13 (that is, 8, N, 2 (RTU)).

Settings

| 1: 7, N, 2 (ASCII) | $9: 8, \mathrm{O}, 1$ (ASCII) |
| :--- | :--- |
| 2: 7, E, 1 (ASCII) | $10: 8, \mathrm{E}, 2$ (ASCII) |
| 3: 7, O, 1 (ASCII) | $11: 8, \mathrm{O}, 2$ (ASCII) |
| 4: 7, E, 2 (ASCII) | $12: 8, \mathrm{~N}, 1$ (RTU) |
| 5: 7, O, 2 (ASCII) | $13: 8, \mathrm{~N}, 2$ (RTU) |
| 6: $8, \mathrm{~N}, 1$ (ASCII) | $14: 8, \mathrm{E}, 1$ (RTU) |
| $7: 8, \mathrm{~N}, 2$ (ASCII) | $15: 8, \mathrm{O}, 1$ (RTU) |
| $8: 8, \mathrm{E}, 1$ (ASCII) | $16: 8, \mathrm{E}, 2$ (RTU) |
|  | $17: 8, \mathrm{O}, 2$ (RTU) |

2. When uploading or downloading programs, PLC program will be disabled.
3. When using WPR commands to write in parameters, note that allowable maximum number of
times to change is $10^{9}$. Otherwise, a memory writing error may occur. The number of times to change depends on the writing value to be changed. If the writing value is not changed, the number of times will not be counted accumulatively; if the writing value is different from the last time, it will be counted as one time.
4. Set D1505=13445, D1503=11111 can restore PLC to default values.

## 13-3 Start-up

## 13-3-1 Connect to PC

Wiring: Connect LTC's PLC using USB or RS-485 through USB port to connect to PC.


Method of Running PLC programs:

1. If PLC programs already exist in the PLC board of LTC, power-on the LTC, detach the slide cover, and switch "ASI" dip switch to the right side (as the red frame in the figure below shows). Then, PLC programs can be run.


## Chapter 13 PLC Function Applications | LTC

2. PLC programs can also be run by USB port and input terminals SG+, SG-. Connect them to WPLSoft software in PC to upload/download and monitor PLC programs.
Method of Uploading/Downloading PLC programs:
(1) Use USB cable (AM/BM) to connect USB port to PC without supplying three-phase 380-480 $V_{\mathrm{AC}}$ power to LTC. Two ends of the USB cable (AM/BM) show as the left figure below. Communication format should be ASCII, and serial baud rate is at random, as the right figure below shows.

(2) Use Delta IFD6500 package (one gray flat cable and one black cable included). One end of the black cable is registered jack; the other end are two signal cables (green and yellow). Green cable connects to SG+, and yellow cable connects to SG-, as the left figure below shows. Communication format should be 19200, 8, N, 2, RTU, and station address should be 1, as the right figure below shows. Before uploading/downloading PLC programs, supply LTC with three-phase 380-480 V ${ }_{\text {AC }}$ power.


Communication Setting


## 13-3-2 I/O Device Correspondence

The diagram below shows the connection of contact $X$ and contact $Y$ between VFD1 and VFD2 in the internal PLC board.


NOTE: If external emergency stop signal is connected to EF terminal, pay attention that when external emergency stop occurs, EF fault will be triggered both on VFD1 and VFD2 and ERROR indicator flashes. Fault code EF is displayed on the communication panel PU08 or PU08V. In this case, set Y12 to be ON to reset the EF fault triggered on VFD1 and VFD2.

LTC PLC Input Port (External Points):

| No. | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Function | 24 V | X 0 | X 1 | X 2 | X 3 | X 4 | X 5 | X 6 | X 7 | X 10 | EF | DCM |
| No. | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 |
| Function | 24 V | X 11 | X 12 | X 13 | X 14 | X 15 | X 16 | X 17 | X 20 | SG+ | SG- | DCM |

LTC PLC Input/Output Port (Internal Points):

| No. | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Function | X21 | X 22 | X 23 | X 24 | Y 10 | Y 11 | Y 12 | Y 13 | Y 14 |  | GND | GND |
| Pre- <br> defined | Fault | Reserved | Fault | Bus low- <br> voltage | FWD / | REV / | Fault | FWD / | REV / |  |  |  |
| STOP | reset | STOP | STOP |  |  |  |  |  |  |  |  |  |

LTC PLC Output Port (External Points):

| No. | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Function | Y 0 | Y 1 | Y 3 | C 0 | Y 3 | Y 4 | Y 5 | C 1 | Y 6 | C 2 |

LTC PLC Output Port (External Points):

| No. | 1 | 2 | 3 | 4 |
| :---: | :---: | :---: | :---: | :---: |
| Function | $\mathrm{Y} 7-1$ | $\mathrm{C} 3-1$ | $\mathrm{Y} 7-2$ | $\mathrm{C} 3-2$ |

For diagram regarding output port (external points) corresponding to relays, see Section 3-2-1 Wiring Diagram.

## 13-3-3 WPLSoft Installation

Visit the download center at Delta's website to download and install the software WPLSoft (v2.05 or above):


After finishing installation, WPLSoft program will be created in the specified default sub-directory under "C:|Program Files\Delta Industrial Automation\WPLSoft x.xx".

## 13-3-4 Program Writing

Step 1. Click on the WPLSoft icon to start the editing software (see Figure 13-3-4-1).


Figure 13-3-4-1 (Left: WPLSoft icon; right: WPLSoft starting page)

Step 2: WPLsoft Editor window appears (see Figure 13-3-4-2). When running WPLSoft for the first time, as there is no existing file, only File (F), Communication (C), View (V), Options (O), and Help (H) are available on the function menu.


Figure 13-3-4-2

## NOTE:

When starting WPLSoft for the second time, the last editing file will be opened directly and displayed in the editor window. WPLSoft editor window is described as Figure 13-3-4-3.


```
Toolbar
```



Figure 13-3-4-3

Step 3. Click on the $\square$ button on the toolbar: New (Ctrl+N) to open a new file, as Figure 13-3-4-4 shows.
8. Dvp0 - Delta WPLSoft


Figure 13-3-4-4

## NOTE:

You can also select New (Ctr+N) under function menu File (F) to open a new file.
8: D D O - Delta WPLSoft


Figure 13-3-4-5

Step 4. Select a PLC Model window will then appear (see Figure 13-3-4-6). Set Program Title, File Name, Model Type, VFD Type, and Communication Setting.


Figure 13-3-4-6

Communication Setting: Set the communication method as required (see Figure 13-3-4-7).


Figure 13-3-4-7

Step 5. After settings are finished, click OK to start editing the programs. Methods of editing programs: 1. Instruction List Mode and 2. Ladder Diagram Mode, as Figure 13-3-4-8 shows. Use the method as required.

```
8, Dvp0 - Delta WPLSoft
    Eile Edit Compiler Comments Search View Communication Options Wizard Window Help
```





Figure 13-3-4-8

## NOTE:

In the ladder diagram mode, you can edit programs using buttons on the ladder diagram toolbar (see Figure 13-3-4-9).


Figure 13-3-4-9

## Example of Basic Operation

Input a ladder diagram as Figure 13-3-4-10 shows. The following steps show how to use mouse and keypad functions (F1 to F12) to edit programs.


Figure 13-3-4-10
Step 1. Create a new file, and then the page below appears.


Figure 13-3-4-11
Step 2. Click ${ }^{\left[\begin{array}{l}\text { 파 } \\ \text { 1 }\end{array}\right.}$ Normally Open Contact button or press function key F1. Input Device Instruction window appears. Select Device Name (e.g. M), Device Number (e.g. 10), and type Comment (e.g. Auxiliary coil). Then, click OK to finish settings, as Figure 13-3-4-12, 13-3-4-13 shows.


Figure 13-3-4-12


Figure 13-3-4-13
 appears. Select Device Name (e.g. Y), Device Number (e.g. 0), and type Comment (e.g. Output coil). Then, click OK to finish settings, as Figure 13-3-4-14, 13-3-4-15 shows.


Figure 13-3-4-14


Figure 13-3-4-15

Step 4. Press ENTER key, and then an Input Instruction window appears. Type "END" in the field, and then click OK, as Figure 13-3-4-16, 13-3-4-17 shows.


Figure 13-3-4-16


Figure 13-3-4-17

Step 5. Click Lation Lader Diagram=>Command button to convert the edited ladder diagram to the commands. After compiling is finished, the number of rungs (steps) appear on the left side of the busbar, as Figure 13-3-4-18 shows.
8. Dvp0 - Delta WPLSoft - [Ladder Diagram Mode]

最 File Edit Compiler Comments Search View Communication Options Wizard Window Help





Figure 13-3-4-18

## 13－3－5 Program Downloading

After a program was input using WPLSoft，click Compile c⿱⺊口灬⿸厂⿱二⿺卜丿． button．After compiling is finished，click Download 足，button to download the programs．WPLSoft downloads the program to the online PLC in the communication format that you specified for the communication settings．

## 13－3－6 Program Monitoring

After downloading the program，make sure that the PLC is in Run mode．On the Communications menu，click Online Mode button，and then click Start Ladder Diagram Control，as the figure below shows．This allows you to monitor and operate the ladder diagram while online．


## 13-4 Basic Principles of PLC Ladder Diagrams

## 13-4-1 Schematic Diagram of PLC Ladder Diagram Program Scanning



Repeated
implementation

## 13-4-2 Introduction to Ladder Diagrams

Ladder diagrams comprise a graphic language widely applied in automatic control, and employs common electrical control circuit symbols. After a ladder diagram editor has been used to create a ladder pattern, PLC program designed is completed. The use of a graphic format to control processes is very intuitive, and is readily accepted by personnel who are familiar with electrical control circuit technology. Many of the basic symbols and actions in a ladder diagram comprise commonly seen electrical devices in conventional automatic control power distribution panels, such as buttons, switches, relays, timers, and counters.

Internal PLC devices: The types and quantities of internal PLC devices vary in different brands of products. Although these internal devices use the same names as conventional electrical control circuit elements such as relays, coils, and contacts, a PLC does not actually contain these physical devices, and they instead correspond to basic elements in the PLCs internal memory (bits). For instance, if a bit is 1 , this may indicate that a coil is electrified, and if that bit is 0 , it will indicate that the coil is not electrified. An N.O. contact (Normal Open, or contact a) can be used to directly read the value of the corresponding bit, and an N.C. contact (Normal Close, or contact b) can be used to obtain the inverse of the bits value. Multiple relays occupy multiple bits, and 8 bits comprise one byte; two bytes comprise one word, and two words comprise a double word. When multiple relays are processing at the same time (such as addition/ subtraction or displacement, etc.), a byte, word, or double word can be used. Furthermore, a PLC contains two types of internal devices: a timer and a counter. It not only has a coil, but can count time and numerical values. Because of this, when it is necessary to process some numerical values, these values are usually in the form of bytes, words, or double words.

The various internal devices in a PLC all account for a certain quantity of storage units in the PLCs storage area. When these devices are used, the content of the corresponding storage area is read in the form of bits, bytes, or words.

Introduction to the basic internal devices in a PLC

| Device type | $\quad$ Description of Function |
| :---: | :--- |
|  | An input relay constitutes the basic unit of storage in a PLCs internal memory <br> corresponding to an external input point (which serves as a terminal connecting <br> with an external input switch and receiving external input signals). It is driven by <br> external input signals, to which it assigns values of 0 or 1. A program design <br> method cannot change the input relay status, and therefore cannot rewrite the <br> corresponding basic units of an input relay, and WPLSoft cannot be used to <br> perform compulsory On/Off actions. A relays contacts (contacts a and b) can be <br> used an unlimited number of times. An input relay with no input signal must be <br> left idle and cannot be used for some other purpose. <br> Input Relay <br> Device indicated as: X0, X1, X7, X10, X11, etc. This device is expressed <br> with the symbol "X", and a devices order is indicated with an octal number. <br> Please refer to Section 13-3-2 I/O Device Correspondence for input point <br> numbers. |
| Output Relay |  | | An output relay constitutes the basic unit of storage in a PLCs internal memory |
| :--- |
| corresponding to an external output point (which connects with an external |
| load). It may be driven by an input relay contact, a contact on another internal |
| device, or its own contacts. It uses one NO contact to connect with external |
| loads or other contacts, and, like input contacts, can use the contact an |
| unlimited number of times. An output relay with no input signal will be idle, but |
| may be used an internal relay if needed. |
| -Device indicated as: Yo, Y1,...Y7, Y10, Y11,....etc. This device is expressed <br> with the symbol "Y", and a devices order is indicated with an octal number. <br> Please refer to Section 13-3-2 I/O Device Correspondence for output point |
| numbers. |


| Device type | Description of Function |
| :---: | :--- |
|  | reached, the contact will be actuated (contact a will close, contact b will open), <br> and the timers fixed value will be given by the set value. Timer has a regulated <br> clock cycle (timing units: 100 ms ). As soon as power to the coil is cut off, the <br> contact will no longer be actuated (contact a will open, contact b will close), and <br> the original timing value will return to zero. <br> Device indicated as: T0, T1 to T159, etc. The device is expressed as the <br> symbol "T", and its order is expressed as a decimal number. |
| Data register | When a PLC is used to perform various types of sequence control and set time <br> value and count value control, it most commonly performs data processing and <br> numerical operations, and data registers are used exclusively for storage of data <br> and various parameters. Each data register contains 16 bits of binary data, <br> which means that it can store one word. Two data registers with adjacent <br> numbers can be used to process double words. <br> $-\quad$ Device indicated as: D0, D1 to D399, etc. The device is expressed as the <br> symbol "D", and its order is expressed as a decimal number. |

Ladder diagram images and their explanation

| Ladder Diagram Structures | Explanation of Commands | Command | Using Device |
| :---: | :---: | :---: | :---: |
| $\longmapsto \vdash$ | NO switch, contact a | LD | X, Y, M, T, C |
| 4 | NC switch, contact b | LDI | X, Y, M, T, C |
| 1 | Series NO | AND | X, Y, M, T, C |
| $\vdash$ | Series NC | ANI | X, Y, M, T, C |
| $\xrightarrow{\text { - }}$ | Parallel NO | OR | X, Y, M, T, C |
| $\because \vdash \left\lvert\, \begin{gathered}\square \\ \square\end{gathered}\right.$ | Parallel NC | ORI | X, Y, M, T, C |
|  | Positive edge-triggered switch | LDP | X, Y, M, T, C |
| $7 \nabla \Gamma$ | Negative edge-triggered switch | LDF | X, Y, M, T, C |
| $\dashv \longmapsto\|\uparrow\|$ | Positive edge-triggered series | ANDP | X, Y, M, T, C |
|  | Negative edge-triggered series | ANDF | X, Y, M, T, C |
|  | Positive edge-triggered parallel | ORP | X, Y, M, T, C |
|  | Negative edge-triggered parallel | ORF | X, Y, M, T, C |


| Ladder Diagram <br> Structures | Explanation of Commands | Command | Using Device |
| :---: | :---: | :---: | :---: |

## 13-4-3 Overview of PLC Ladder Diagram Editing

The program editing method begins from the left busbar and proceeds to the right busbar (the right busbar is omitted when editing using WPLSoft). Continue to the next row after completing each row; there is a maximum of 11 contacts on each row. If this is not sufficient, a continuous line will be generated to indicate the continued connection and more devices can be added. A continuous series of numbers will be generated automatically and identical input points can be used repeatedly. See figure below:


The ladder diagram programming method involves scanning from the upper left corner to the lower right corner. The coils and applications command-computing box are handled in the output, and the ladder diagram is placed on the farthest right. Taking the figure below as an example, we can gradually analyze the procedural sequence of the ladder diagram. The number in the upper right corner gives the sequential order.

Explanation of command sequence


## Explanation of basic structure of ladder diagrams

1. LD (LDI) command: An LD or LDI command is given at the start of a block.


LDP and LDF have this command structure, but there are differences in their action state. LDP, LDF only act at the rising or falling edge of a conducting contact. (see figure below):

2. AND (ANI) command: A series configuration in which a single device is connected with one device or a block.


ANDP, ANDF also have structures like this, but their action occurs at the rising and falling edge.
3. $O R$ (ORI) command: A single device is connected with one device or a block.


ORP, ORF also have identical structures, but their action occurs at the rising and falling edge.
4. ANB command: A configuration in which one block is in series with one device or block.

5. ORB command: A configuration in which one block is in parallel with one device or block.


In the case of ANB and ORB operations, if a number of blocks are connected, they should be combined to form a block or network from the top down or from left to right.
6. MPS, MRD, MPP commands: Branching point memory for multiple outputs, enabling multiple, different outputs. The MPS command begins at a branching point, where the so-called branching point refers to the intersection of horizontal and vertical lines. We have to rely on the contact status along a single vertical line to determine whether the next contact can give a memory command. While each contact is basically able to give memory commands, in view of convenience and the PLCs capacity restrictions, this can be omitted from some places when converting a ladder diagram. The structure of the ladder diagram can be used to judge what kinds of contact memory commands are used.

- MPS can be distinguished by use of the " $\rceil$ " symbol; this command can be used consecutively for up to 8 times. The MRD command is read from branching point memory; because logic states along any one vertical line must be the same, in order to continue analysis of other ladder diagrams, the original contact status must be read.
- MRD can be distinguished by use of the " $\mid$ " symbol. The MPP command is read from the starting state of the uppermost branching point, and it is read from the stack (pop); because it is the final command along a vertical line, it indicates that the state of the vertical line can be concluded.
- MPP can be distinguished by use of the " L" symbol. Although there should basically be no errors when using the foregoing analytical approach, the compiling program may sometimes omit identical state output, as shown in the following figure:



## 13-4-4 Commonly Used Basic Program Design Examples

Start, stop, and protection
Some applications may require a brief close or brief break using the buttons to start and stop equipment. A protective circuit must therefore be designed to maintain continued operation in these situations; this protective circuit may employ one of the following methods:
Example 1: Priority stop protective circuit
When the start NO contact $X 1=O$ n, and the stop NC contact $X 2=O f f, Y 1=O n$; if $X 2=O n$ at this time, coil Y 1 will no longer be electrified, and this is therefore referred to as priority stop.


## Example 2: Priority start protective circuit

When start NO contact $\mathrm{X} 1=\mathrm{On}$, and the stop NC contact $\mathrm{X} 2=\mathrm{Off}, \mathrm{Y} 1=\mathrm{On}$, and coil Y 1 will be electrified and protected. At this time, if $\mathrm{X} 2=\mathrm{On}$, coil Y 1 will still protect the contact and continue to be electrified, and this is therefore priority start.


Example 3: Setting (SET) and reset (RST) command protective circuit
The following figure shows a protective circuit composed of RST and SET commands. Priority stop occurs when the RST command is placed after the SET command. Because the PLC executes programs from the top down, at the end of the program, the state of Y 1 will indicate whether coil Y 1 is electrified. When X 1 and X 2 are both actuated, Y 1 will lose power, and this is therefore priority stop.
Priority start occurs when the SET command is placed after the RST command. When X1 and X 2 are both actuated, Y 1 will be electrified, and this is therefore priority start.

Top priority of stop


Top priority of start


## Commonly used control circuits

Example 4: Conditional control
$\mathrm{X} 1, \mathrm{X} 3$ are respectively start/ stop Y 1 , and X 2 \& X 4 are respectively start/ stop Y 2 ; all have protective circuits. Because Y 1 s NO contact is in series with Y 2 s circuit, it becomes an AND condition for the actuation of Y 2 . The action of Y 1 is therefore a condition for the action of Y 2 , and Y 1 must be actuated before Y 2 can be actuated.


Example 5: Interlocking control
The figure below shows an interlocking control circuit. Depending on which of the start contacts $\mathrm{X} 1, \mathrm{X} 2$ is valid first, the corresponding output Y 1 or Y 2 will be actuated, and when one is actuated, the other will not be actuated. This implies that Y 1 and Y 2 cannot be actuated at the same time (interlocking effect). Even if both X 1 and X 2 are valid at the same time, because the ladder diagram program is scanned from the top down, it is impossible for Y 1 and Y 2 to be actuated at same time. This ladder diagram assigns priority only to Y 1 .



## Example 6: Sequence control

If the NC contact of Y 2 in the interlocking control configuration of example 5 is put in series with the Y 1 circuit, so that it is an AND condition for actuation of Y 1 (see figure below), not only is Y 1 a condition for the actuation of Y 2 in this circuit, the actuation of Y 2 will also stop the actuation of Y 1 . This configuration confirms the actuation order of Y 1 and Y 2 .


## Example 7: Oscillating circuit

Oscillating circuit with a period of $\Delta T+\Delta T$
The figure below shows a very simple ladder diagram. When starting to scan the Y1 NC contact, because the Y1 coil has lost power, the Y1 NC contact will be closed. When the Y1 coil is then scanned, it will be electrified, and the output will be 1 . When the Y1 NC contact is scanned in the scanning cycle, because Y1 coil is electrified, the Y1 NC contact will be opened, the Y 1 coil will then lose power, and the output will be 0 . Following repeated scanning, the output of Y 1 coil will have an oscillating waveform with a period of $\Delta \mathrm{T}(\mathrm{On})+\Delta \mathrm{T}$ (Off).


Oscillating circuit with a period of $n T+\Delta T$
The program of the ladder diagram shown below uses timer T0 to control coil Y 1s electri fied time. After Y 1 is electrified, it causes timer T0 to close during the next scanning cycle, which will cause the output from Y 1 to have the oscillating waveform shown in the figure below. Here n is the timers decimal setting value, and T is the clock cycle of the timer.


## Example 8: Flashing circuit

The following figure shows an oscillating circuit of a type commonly used to cause an indicator light to flash or a buzzer to buzz. It uses two timers to control the On and Off time of Y1 coil. Here $\mathrm{n} 1, \mathrm{n} 2$ are the timing set values of T 1 and T 2 , and T is the clock cycle of the timer.


## Example 9: Triggering circuit

In the figure below, a command consisting of the differential of the rising edge of X 0 causes coil M0 to generate a single pulse for $\Delta \mathrm{T}$ (length of one scanning cycle), and coil Y 1 is electrified during this scanning cycle. Coil M0 loses power during the next scanning cycle, and NC contact M0 and NC contact Y1 are both closed. This causes coil Y1 to stay in an electrified state until there is another rising edge in input X0, which again causes the electrification of coil M0 and the start of another scanning cycle, while also causing coil Y 1 to lose power, etc. The sequence of these actions can be seen in the figure below. This type of circuit is commonly used to enable one input to perform two actions in alternation. It can be seen from the time sequence in the figure below that when input X 0 is a square wave signal with a period of T , the output of coil Y 1 will be a square wave signal with a period of 2 T .


## Example 10: Delay circuit

When input X0 is On, because the corresponding NC contact will be Off, the timer T10 will be in no power status, and output coil Y 1 will be electrified. T 10 will receive power and begin timing only after input X 0 is Off , and output coil Y 1 will be delayed for 100 sec . (K1000*0.1 sec. $=100 \mathrm{sec}$.) before losing power; please refer to the sequence of actions in the figure below.


TB:0.1 sec


Example 11: The open / close delay circuit is composed of two timers; output Y 4 will have a delay whether input X0 is On or Off.


Example 12: Extended timing circuit
In the circuit in the figure on the left, the total delay time from the moment input X 0 closes to the time output Y 1 is electrified is $(\mathrm{n} 1+\mathrm{n} 2)$ * T , where T is the clock cycle. Timers: T 11 , T12; clock cycle: T.


## 13-5 Various PLC Device Functions

| Item | Specifications | Notes |
| :---: | :---: | :--- |
| Control Mode | Alternating back-and-forth scanning method |  |
| Inputs/Outputs Control <br> Mode | Cyclic refresh mode |  |
| Execution speed | Basic commands (several $\mu \mathrm{s}$ ) | Application commands (1-dozens of $\mu \mathrm{s}$ ) |
| Programming Language | Commands and ladder diagrams |  |
| Program Capacity | 14000 steps | X: 17 points for user-defined, and 4 for <br> internal pre-defined <br> Y: 8 points for user-defined, and 5 for <br> internal pre-defined |
| Inputs/Outputs Points | Digital inputs $(\mathrm{X}): 17+4=21$ <br> Digital outputs $(\mathrm{Y}): 8+5=13$ |  |



## 13-5-1 Introduction to Device Functions

## Input / output contact functions

Input contact $X$ functions: Input contact $X$ is connected with an input device, and reads input signals entering the PLC. The number of times that contact $a$ or $b$ of input contact $X$ is used in the program is not subject to restrictions. The $\mathrm{On} / \mathrm{Off}$ state of input contact X will change as the input device switches On and Off; a peripheral device (WPLSoft) cannot be used to force contact X On or Off.

## Output contact Y functions

The job of output contact $Y$ is to send an On/Off signal to drive the load connected with output contact Y . Output contacts consist of two types: relays and transistors. While number of times that contact $a$ or $b$ of each output contact $Y$ is used in the program is not subject to restrictions, it is recommended that the number of output coil $Y$ be used only once in a program, otherwise the right to determine the output state when the PLC performs program scanning will be assigned to the programs final output Y circuit.


The output of Y 0 will be decided by circuit (2), i.e. decided by ON/OFF of X10.

Numerical value, constant [K]/[H]

| Constant | Single-byte | K | Decimal | K-32,768-K32,767 |
| :--- | :--- | :--- | :--- | :--- |
|  | Double-byte |  |  | K-2,147,483,648-K2,147,483,647 |
|  | Single-byte | H | Hexadecimal | H0000-HFFFF |
|  | Houble-byte |  |  | H00000000-HFFFFFFFF |

The PLC can use five types of numerical values to implement calculations based on its control tasks; the following is an explanation of the missions and functions of different numerical values.

## Binary Number, BIN

The PLCs numerical operations and memory employ binary numbers. Binary nibbles and relevant terms are explained as follows:

| bit | Bits are the fundamental units of binary values, and have a state of either 1 or 0 |
| :---: | :--- |
| Nibble | Comprised of a series of 4 bits (such as b3-b0); can be used to express a one- <br> nibble decimal number 0-9 or hexadecimal number: 0-F. |
| Byte | Comprised of a series of two nibbles (i.e. 8 bits, b7-b0); can express a <br> hexadecimal number: 00-FF. |
| Word | Comprised of a series of two bytes (i.e. 16 bits, b15-b0); can express a <br> hexadecimal number with four nibbles: $0000-$ FFFF. |
| Double Word | Comprised of a series of two words (i.e. 32 bits, b31-b0); can express a <br> hexadecimal number with eight nibbles: $00000000-F F F F F F F F$ |

Relationship between bits, digits, nibbles, words, and double words in a binary system (see figure below):


## Octal Number, OCT

The external input and output terminals of a DVP-PLC are numbered using octal numbers Example: External input: X0-X7, X10-X17 (Device number table);
External output: Y0-Y7, Y10-Y17 (Device number table)

## Decimal Number, DEC

Decimal numbers are used for the following purposes in a PLC system:

- The setting values of timer T or counter C , such as TMR C0 K50. (K constant)
- The numbers of devices including M, T, C, or D, such as M10 or T30. (device number)
- Used as an operand in an application command, such as MOV K123 D0. (K constant)


## Binary Code Decimal, BCD

Uses one nibble or 4 bits to express the data in a decimal number; a series of 16 bits can therefore express a decimal number with 4 nibbles. Chiefly used to read the input value of a fingerwheel numerical switch input or output a numerical value to a seven-segment display drive.

## Hexadecimal Number, HEX

Applications of hexadecimal numbers in a PLC system: Used as operands in application commands, such as MOV H1A2B D0. (H constant)

## Constant K

Decimal numbers are usually prefixed with a "K" in a PLC system, such as K100. This indicates that it is a decimal number with a numerical value of 100.
Example: K can be combined with bit device $\mathrm{X}, \mathrm{Y}, \mathrm{M}$, or S to produce data in the form of a nibble, byte, word, or double word, such as in the case of K2Y10 or K4M100. Here K1 represents a 4-bit combination, and K2-K4 variously represent 8,12 , and 16 -bit combinations.

## Constant H

Hexadecimal numbers are usually prefixed with the letter " H " in a PLC system, such as in the case of H 100 , which indicates a hexadecimal number with a numerical value of 100 .

## Functions of auxiliary relays

Like an output relay Y , an auxiliary relay M has an output coil and contacts a and b , and the number of times they can be used in a program is unrestricted. Users can use an auxiliary relay M to configure the control circuit, but cannot use it to directly drive an external load. Auxiliary relays have the following two types of characteristics:

- Ordinary auxiliary relays: Ordinary auxiliary relays will all revert to the Off state if a power outage occurs while the PLC is running, and will remain in the Off state if power is again turned down.
- Special purpose auxiliary relays: Each special purpose auxiliary relay has its own specific use. Do not use any undefined special purpose auxiliary relays.


## Timer functions

Timers take 100 ms as their timing units. When the timing method is an upper time limit, when the current timer value = set value, power will be sent to the output coil. Timer setting values consist of decimal K values, and the data register D can also serve as a setting value.
Actual timer setting time $=$ timing units * set value
Counter features

| Item |  |
| :---: | :--- |
| Type | General Type |
| CT Direction: | Score |
| Setting | $0-32,767$ |
| Designation of set value | Constant K or data register D |
| Change in current value | When the count reaches the set value, there is no longer a count |
| Output contact | When the count reaches the set value, the contact comes On and stays On |
| Reset | The current value reverts to 0 when an RST command is executed, and the <br> contact reverts to Off |
| Contact actuation | All are actuated after the end of scanning |

Counter functions
When a counters counting pulse input signal goes Off $\rightarrow$ On, if the counters current value is equal to the set value, the output coil will come On. The setting value will be a decimal $K$ values, and the data register D can also serve as a setting value.

16-bit counter C0-C79:

- 16-bit counter setting range: K0-K32,767. (when K0 and K1 are identical, the output contact will immediately be On during the first count.)
- The current counter value will be cleared from an ordinary counter when power is shut off to the PLC.
- If the MOV command or WPLSoft is used to transmit a value greater than the set value to the C 0 current value register, when the next X 1 goes from Off $\rightarrow$ On, the C 0 counter contact will change to On, and the current value will change to the set value.
- A counters setting value may be directly set using a con stant $K$ or indirectly set using the value in register D (not including special data registers D1000-D1199 or D2000-D2799).
- If the set value employs a constant $K$, it may only be a positive number; the set value may be either a positive or a negative number if the value in data register $D$ is used. The current counter value will change from 32,767 to $-32,768$ as the count continues to accumulate.

Example
LD X0
RST
LD
CNT
LD


OUT

1. When $X 0=O n$ and the RST command is executed, the current value of CO will revert to 0 , and the output contact will revert to Off.
2. When X 1 changes from Off $\rightarrow$ On, the current value of the counter will execute an increase (add one).
3. When the count of counter CO reaches 4.

the set value K 5 , the contact C 0 will come On, and the current value of $\mathrm{CO}=$ set value $=\mathrm{K} 5$. Afterwards, signal C0 triggered by X1 cannot be received, and the current value of C 0 will remain K5.

## 13-5-2 Introduction to Special Relay Functions (Special M)

R/W items: RO: read only function; RW: read and write function

| Special M | Description of Function | R/W * |
| :---: | :---: | :---: |
| M1000 | Operates monitor NO contact (contact a). NO while RUN, contact a. This contact is On while in the RUN state. | RO |
| M1001 | Operates monitor NC contact (contact b). NC while RUN, contact b. This contact is Off while in the RUN state. | RO |
| M1002 | Initiates a forward (the instant RUN is On) pulse. Initial pulse, contact a. Produces a forward pulse the moment RUN begins; its width = scan cycle | RO |
| M1003 | Initiates a reverse (the instant RUN is Off) pulse. Initial pulse, contact a. Produces a reverse pulse the moment RUN ends; the pulse width = scan cycle | RO |
| M1004 | Reserved | RO |
| M1005 | Drive malfunction instructions | RO |
| M1006 | Converter has no output ( $1=$ no output, $0=$ output) | RO |
| M1007 | Drive direction FWD(0)/REV(1) | RO |
| M1008 <br> M1010 | -- | -- |
| M1011 | $10 \mathrm{~ms} \mathrm{clock} \mathrm{pulse} ,5 \mathrm{~ms} \mathrm{On} / 5 \mathrm{~ms} \mathrm{Off}$ | RO |
| M1012 | 100 ms clock pulse, 50 ms On / 50ms Off | RO |
| M1013 | 1 sec. clock pulse, 0.5 s On $/ 0.5 \mathrm{~s}$ Off | RO |
| M1014 | 1 min . clock pulse, 30s On / 30s Off | RO |
| M1015 | Frequency attained (when used together with M1025) | RO |
| M1016 | Parameter read/write error | RO |
| M1017 | Parameter write successful | RO |
| M1018 | -- | -- |
| M1019 | -- | -- |
| M1020 | Zero flag | RO |
| M1021 | Borrow flag | RO |
| M1022 | Carry flag | RO |
| M1023 | Divisor is 0 | RO |
| M1024 | -- | -- |
| M1025 | $\begin{aligned} & \text { Target drive frequency }=\text { set frequency (ON) } \\ & \text { Target drive frequency }=0 \text { (OFF) } \end{aligned}$ | RW |
| M1026 | Drive operating direction FWD(OFF)/REV(ON) | RW |
| M1027 | Drive Reset | RW |
| M1028 | -- | -- |
| M1029 | -- | -- |
| M1030 | -- | -- |
| M1031 | Compulsory setting of the current PID integral value equal to D1019 (0 change, 1 valid) | RW |
| M1032 | Compulsory definition of FREQ command after PID control | RW |
| M1033 | -- | -- |
| M1034 | Initiates CANopen real-time control | RW |
| M1035 | Initiates internal communications control | RW |
| M1036 | Ignore calendar error | RW |
| M1037 | -- | -- |
| M1038 | M18 count begins | RW |
| M1039 | Reset M18 count value | RW |
| M1040 | Excitation (Servo On) | RW |
| M1041 | -- | -- |
| M1042 | Quick stop | RW |
| M1043 | -- | -- |
| M1044 | Pause (Halt) | RW |


| Special M | Description of Function | R/W * |
| :---: | :---: | :---: |
| $\begin{gathered} \hline \text { M1045 } \\ - \\ \text { M1047 } \end{gathered}$ | -- | -- |
| M1048 | Move to new position | RW |
| M1049 | -- | -- |
| M1050 | Absolute position / relative position (0: relative/1: absolute) | RW |
| M1051 | -- | -- |
| M1052 | Lock frequency (lock, frequency locked at the current operating frequency) | RW |
| M1053 | -- | -- |
| M1054 | Compulsory reset of absolute position | RW |
| M1055 | Search Origin | RW |
| M1056 | Excitation ready (Servo On Ready) | RO |
| M1057 | -- | -- |
| M1058 | On Quick Stopping | RO |
| M1059 | CANopen Master setting complete | RO |
| M1060 | CANopen Currently initializing slave station | RO |
| M1061 | CANopen Slave station initialization failure | RO |
| M1062 | -- | -- |
| M1063 | Torque attained | RO |
| M1064 | Target reached | RO |
| M1065 | Read/write CANopen data time out | RO |
| M1066 | Read/write CANopen data complete | RO |
| M1067 | Read/write CANopen data successful | RO |
| M1068 | Calendar calculation error | RO |
| M1069 | -- | -- |
| M1070 | Return home complete | RO |
| M1071 | Homing error | RO |
| $\begin{gathered} \text { M1072 } \\ -\quad \\ \text { M1075 } \\ \hline \end{gathered}$ | -- | -- |
| M1076 | Calendar time error or refresh time out | RO |
| M1077 | 485 Read/write complete | RO |
| M1078 | 485 Read-write error | RO |
| M1079 | 485 Communications time out | RO |
| M1090 | OFF (Refer to Pr.00-29 for more information) | RO |
| M1091 | HAND (Refer to Pr.00-29 for more information) | RO |
| M1092 | AUTO (Refer to Pr.00-29 for more information) | RO |
| M1100 | LOCAL (Refer to Pr.00-29 for more information) | RO |
| M1101 | REMOTE (Refer to Pr.00-29 for more information) | RO |
| M1168 | SMOV BCD and BIN mode switch | RW |
| M1260 | PLC PID1 Enable | RW |
| M1262 | PLC PID1 integral positive value limit | RW |
| M1270 | PLC PID2 Enable | RW |
| M1272 | PLC PID2 integral positive value limit | RW |

13-5-3 Introduction to Special Register Functions (Special D)

| Special D | Description of Function | R/W * |
| :---: | :---: | :---: |
| D1000 | -- | -- |
| D1001 | Device system program version | RO |
| D1002 | Program capacity | RO |
| D1003 | Total program memory content | RO |
| $\begin{gathered} \text { D1004 } \\ \text { D1009 } \end{gathered}$ | -- | -- |
| D1010 | Current scan time (units: 0.1 ms ) | RO |
| D1011 | Minimum scan time (units: 0.1 ms ) | RO |
| D1012 | Maximum scan time (units: 0.1 ms ) | RO |
| $\begin{gathered} \text { D1013 } \\ -\quad \\ \text { D1017 } \end{gathered}$ | -- | -- |
| D1018 | Current integral value | RO |
| D1019 | Compulsory setting of PID I integral | RW |
| D1020 | Output frequency ( $0.000-600.00 \mathrm{~Hz}$ ) | RO |
| D1021 | Output current (\#\#\#\#.\#A) | RO |
| D1022 | AI AO DI DO Expansion card number <br> 0 : No expansion card <br> 4: AC input card (6 in) (EMC-D611A) <br> 5: Digital I/O Card (4 in 2 out ) (EMC-D42A) <br> 6: Relay card (6 out) (EMC-R6AA) <br> 11: Analog I/O Card (2 in 2 out) (EMC-A22A) | RO |
| D1023 | Communication expansion card number <br> 0: No expansion card <br> 1: DeviceNet Slave (CMC-DN01) <br> 2: Profibus-DP Slave (CMC-PD01) <br> 3: CANopen Slave (EMC-COP01) <br> 5: EtherNet/IP Slave (CMC-EIP01) <br> 12: PROFINET Slave (CMC-PN01) | RO |
| $\begin{gathered} \text { D1024 } \\ -\quad \\ \text { D1026 } \end{gathered}$ | -- | -- |
| D1027 | PID calculation frequency command (frequency command after PID calculation) | RO |
| D1028 | AVI value (0.00-100.00\%) | RO |
| D1029 | ACI value (0.0-100.00\%) | RO |
| D1030 | AUI value (-100.0-100.00\%) | RO |
| D1031 | C series: extension card Al10 (0.0-100.0\%) | RO |
| D1032 | C series: extension card Al11 (0.0-100.0\%) | RO |
| $\begin{gathered} \hline \text { D1033 } \\ - \\ \text { D1035 } \end{gathered}$ | -- | -- |
| D1036 | Servo error bit | RO |
| D1037 | Drive output frequency | RO |
| D1038 | DCBUS voltage | RO |
| D1039 | Output voltage | RO |
| D1040 | Analog output value AFM1 (-100.00-100.00\%) | RW |
| D1041 | C series: extension card AO10 (0.0-100.0\%) | RW |
| D1042 | C series: extension card AO11 (0.0-100.0\%) | RW |
| D1043 | Can be user-defined (will be displayed on panel when Pr. 00-04 is set as 28; display method is Cxxx ) | RW |
| D1044 | -- | - |
| D1045 | Analog output value AFM2 (-100.00-100.00\%) | RW |

Chapter 13 PLC Function Applications | LTC

| Special D | Description of Function | R/W * |
| :---: | :---: | :---: |
| $\begin{gathered} \text { D1046 } \\ - \\ \text { D1049 } \end{gathered}$ | -- | -- |
| D1050 | Actual Operation Mode <br> 0 : Speed <br> 1: Position <br> 2: Torque <br> 3: Homing Origin | RO |
| D1051 | Encoder Pulses L | RO |
| D1052 | Encoder Pulses H | RO |
| D1053 | Actual torque | RO |
| D1054 | MI8 current calculated count value (Low Word) | RO |
| D1055 | M18 current calculated count value (High Word) | RO |
| D1056 | Rotational speed corresponding to MI8 | RO |
| D1057 | MI8s rotational speed ratio | RW |
| D1058 | M18 refresh rate (ms) corresponding to rotational speed | RW |
| D1059 | Number of nibbles of rotational speed corresponding to MI8 (0-3) | RW |
| D1060 | Operation Mode setting <br> 0: Speed <br> 1: Position <br> 2: Torque <br> 3: Homing Origin | RW |
| D1061 | 485 COM1 communications time out time (ms) | RW |
| D1062 | Torque command (torque limit in speed mode) | RW |
| D1063 | Year (Western calendar) (display range 2000-2099) (must use KPC-CC01) | RO |
| D1064 | Week (display range 1-7) (must use KPC-CC01) | RO |
| D1065 | Month (display range 1-12) (must use KPC-CC01) | RO |
| D1066 | Day (display range 1-31) (must use KPC-CC01) | RO |
| D1067 | Hour (display range 0-23) (must use KPC-CC01) | RO |
| D1068 | Minute (display range 0-59) (must use KPC-CC01) | RO |
| D1069 | Second (display range 0-59) (must use KPC-CC01) | RO |
| D1100 | Target frequency | RO |
| D1101 | Target frequency (must be operating) | RO |
| D1102 | Reference frequency | RO |
| D1103 | Target L | RO |
| D1104 | Target H | RO |
| D1105 | Target torque | RO |
| D1106 | -- | -- |
| D1107 | $\pi(\mathrm{Pi})$ Low word | RO |
| D1108 | $\pi(\mathrm{Pi})$ High word | RO |
| D1109 | Random number | RO |
| D1110 | Internal node communications number (set number of slave stations to be controlled) | RW |
| D1111 | Actual position (Low word) | RO |
| D1112 | Actual position (High word) | RO |
| D1113 | -- | RO |
| D1114 | -- | -- |
| D1115 | Internal node synchronizing cycle (ms) | RO |
| D1116 | Internal node error (bit0 = Node 0, bit1 = Node 1, ...bit7 = Node 7) | RO |
| D1117 | Internal node online correspondence (bit0 = Node 0, bit1 = Node 1, ...bit7 = Node 7) | RO |
| D1118 | -- | -- |
| D1119 | -- | -- |
| D1120 | Internal node 0 control command | RW |
| D1121 | Internal node 0 mode | RW |
| D1122 | Internal node 0 reference command L | RW |


| Special D | Description of Function | R/W * |
| :---: | :---: | :---: |
| D1123 | Internal node 0 reference command H | RW |
| D1124 | -- | -- |
| D1125 | -- | -- |
| D1126 | Internal node 0 status | RO |
| D1127 | Internal node 0 reference status L | RO |
| D1128 | Internal node 0 reference status H | RO |
| D1129 | -- | -- |
| D1130 | Internal node 1 control command | RW |
| D1131 | Internal node 1 mode | RW |
| D1132 | Internal node 1 reference command L | RW |
| D1133 | Internal node 1 reference command H | RW |
| D1134 | -- | -- |
| D1135 | -- | -- |
| D1136 | Internal node 1 status | RO |
| D1137 | Internal node 1 reference status L | RO |
| D1138 | Internal node 1 reference status H | RO |
| D1139 | -- | -- |
| D1140 | Internal node 2 control command | RW |
| D1141 | Internal node 2 mode | RW |
| D1142 | Internal node 2 reference command L | RW |
| D1143 | Internal node 2 reference command H | RW |
| D1144 | -- | -- |
| D1145 | -- | -- |
| D1146 | Internal node 2 status | RO |
| D1147 | Internal node 2 reference status L | RO |
| D1148 | Internal node 2 reference status H | RO |
| D1149 | -- | -- |
| D1150 | Internal node 3 control command | RW |
| D1151 | Internal node 3 mode | RW |
| D1152 | Internal node 3 reference command L | RW |
| D1153 | Internal node 3 reference command H | RW |
| D1154 | -- | -- |
| D1155 | -- | -- |
| D1156 | Internal node 3 status | RO |
| D1157 | Internal node 3 reference status L | RO |
| D1158 | Internal node 3 reference status H | RO |
| D1159 | -- | -- |
| D1160 | Internal node 4 control command | RW |
| D1161 | Internal node 4 mode | RW |
| D1162 | Internal node 4 reference command L | RW |
| D1163 | Internal node 4 reference command H | RW |
| D1164 | -- | -- |
| D1165 | -- | -- |
| D1166 | Internal node 4 status | RO |
| D1167 | Internal node 4 reference status L | RO |
| D1168 | Internal node 4 reference status H | RO |
| D1169 | -- | -- |
| D1170 | Internal node 5 control command | RW |
| D1171 | Internal node 5 mode | RW |
| D1172 | Internal node 5 reference command L | RW |
| D1173 | Internal node 5 reference command H | RW |
| D1174 | -- | RW |
| D1175 | -- | -- |
| D1176 | Internal node 5 status | -- |
| D1177 | Internal node 5 reference status L | RO |


| Special D | Description of Function | R/W * |
| :---: | :--- | :---: |
| D1178 | Internal node 5 reference status H | RO |
| D1179 | -- | -- |
| D1180 | Internal node 6 control command | RW |
| D1181 | Internal node 6 mode | RW |
| D1182 | Internal node 6 reference command L | RW |
| D1183 | Internal node 6 reference command H | RW |
| D1184 | -- | -- |
| D1185 | -- | -- |
| D1186 | Internal node 6 status | RO |
| D1187 | Internal node 6 reference status L | RO |
| D1188 | Internal node 6 reference status H | -- |
| D1189 | -- | RW |
| D1190 | Internal node 7 control command | RW |
| D1191 | Internal node 7 mode | RW |
| D1192 | Internal node 7 reference command L | RW |
| D1193 | Internal node 7 reference command H | -- |
| D1194 | -- | -- |
| D1195 | -- | RO |
| D1196 | Internal node 7 status | RO |
| D1197 | Internal node 7 reference status L | RO |
| D1198 | Internal node 7 reference status H | -- |
| D1199 | -- |  |


| Special D | Description of Function | Default | R/W * |
| :---: | :---: | :---: | :---: |
| D1200 | PID 1 Mode: 0 : Basic mode | 0 | RW |
| D1201 | PID 1 Target selection: <br> 0: Refer to D1202 <br> 1: AVI <br> 2: ACI <br> 3: AUI | 0 | RW |
| D1202 | PID 1 Target value (0.00\%-100.00\%) | 5000 | RW |
| D1203 | PID 1 Feedback selection: <br> 0: Refer to D1204 <br> 1: AVI <br> 2: ACI <br> 3: AUI | 1 | RW |
| D1204 | PID 1 Feedback value (0.00\%-100.00\%) | 0 | RW |
| D1205 | PID 1 P value (decimal 2 points) | 10 | RW |
| D1206 | PID 1 I value (decimal 2 points) | 1000 | RW |
| D1207 | PID 1 D value (decimal 2 points) | 0 | RW |
| D1209 | PID 1 Max. limit | 10000 | RW |
| D1215 | PID 1 Calculation (decimal 2 points) | 0 | RO |
| D1220 | PID2 Mode: <br> 0 : Basic mode | 0 | RW |
| D1221 | PID 2 Target selection: <br> 0: Refer to D1202 <br> 1: AVI <br> 2: ACI <br> 3: AUI | 0 | RW |
| D1222 | PID 2 Target value (0.00\%-100.00\%) | 5000 | RW |
| D1223 | PID 2 Feedback selection: <br> 0: Refer to D1204 <br> 1: AVI <br> 2: ACI | 1 | RW |


| Special <br> D | Description of Function | Default | R/W * |
| :---: | :--- | :---: | :---: |
|  | 3: AUI |  |  |
| D1224 | PID 2 Feedback value (0.00\%-100.00\%) | 0 | RW |
| D1225 | PID 2 P value (decimal 2 points) | 10 | RW |
| D1226 | PID 2 I value (decimal 2 points) | 1000 | RW |
| D1227 | PID 2 D value (decimal 2 points) | 0 | RW |
| D1229 | PID 2 Max. limit | 10000 | RW |
| D1235 | PID 2 Calculation (decimal 2 points) | 0 | RO |



The following is CANopen Masters special D (Allow writing only when PLC is in STOP state)
$\mathrm{n}=0-7$

| Special D | Description of Function | PDO | Power off Memory | Default | R/W |
| :---: | :---: | :---: | :---: | :---: | :---: |
| D1070 | Channel opened by CANopen initialization (bit0=Machine code0 ...) | NO | NO | 0 | R |
| D1071 | Error channel occurring in CANopen initialization process (bit0=Machine code0 ...) | NO | NO | 0 | R |
| D1072 | Reserved |  | - |  |  |
| D1073 | CANopen break channel (bit0=Machine code0 ...) | NO | NO |  | R |
| D1074 | Error code of master error <br> 0 : No error <br> 1: Slave station setting error <br> 2: Synchronizing cycle setting error (too small) | NO | NO | 0 | R |
| D1075 | Reserved | - | - |  | - |
| D1076 | SDO error message (main index value) | NO | NO |  | R |
| D1077 | SDO error message (secondary index value) | NO | NO |  | R |
| D1078 | SDO error message (error code) | NO | NO |  | R |
| D1079 | SDO error message (error code) | NO | NO |  | R |
| D1080 | Reserved | - | - |  | - |
| $\begin{gathered} \text { D1081 } \\ - \\ \text { D1086 } \end{gathered}$ | Reserved | - | - |  | - |
| $\begin{gathered} \text { D1087 } \\ - \\ \text { D1089 } \\ \hline \end{gathered}$ | Reserved | - | - |  | - |
| D1090 | Synchronizing cycle setting | NO | YES | 4 | RW |
| D1091 | Sets slave station On or Off (bit 0-bit 7 correspond to slave stations number 0-7) | NO | YES | FFFFH | RW |
| D1092 | Delay before start of initialization | NO | YES | 0 | RW |
| D1093 | Break time detection | NO | YES | 1000ms | RW |
| D1094 | Break number detection | NO | YES | 3 | RW |

## Chapter 13 PLC Function Applications | LTC

| Special D | Description of Function | PDO <br> Map | Power <br> off <br> Memory | Default | R/W |
| :---: | :--- | :---: | :---: | :---: | :---: |
| D1095 <br> - <br> D1096 | Reserved | - | - |  | - |
| D1097 | Corresponding real-time transmission type (PDO) <br> Setting range: $1-240$ | NO | YES | 1 | RW |
| D1098 | Corresponding real-time receiving type (PDO) <br> Setting range: $1-240$ | NO | YES | 1 | RW |
| D1099 | Initialization completion delay time <br> Setting range: $1-60000$ sec. | NO | YES | 15 sec. | RW |
| D2000+100*nStation number n of slave station <br> Setting range: 0-127 <br> 0: No CANopen function | NO | YES | 0 | RW |  |

The LTC supports 8 slave stations under the CANopen protocol; each slave station occupies 100 special D locations; stations are numbered 1-8, total of 8 stations.


1. The range of $n$ is $0-7$
2.     - Indicates PDOTX, $\Delta$ Indicates PDORX; unmarked special D can be refreshed using the CANFLS command

| Special D | Description of Function | Default: | R/W |
| :---: | :---: | :---: | :---: |
| D2000+100*n | Station number n of slave station Setting range: 0-127 <br> 0 : No CANopen function | 0 | RW |
| D2002+100*n | Manufacturer code of slave station number $\mathrm{n}(\mathrm{L})$ | 0 | R |
| D2003+100*n | Manufacturer code of slave station number $\mathrm{n}(\mathrm{H})$ | 0 | R |
| D2004+100*n | Manufacturers product code of slave station number $\mathrm{n}(\mathrm{L})$ | 0 | R |
| D2005+100*n | Manufacturers product code of slave station number $\mathrm{n}(\mathrm{H})$ | 0 | R |

Basic definitions

| Special D | Description of Function | Default: | PDO Mapping | PDO Default: |  |  |  | R/W |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 | 2 | 3 | 4 |  |
| D2006+100*n | Communications break handling method of slave station number n | 0 | $6007 \mathrm{H}-0010 \mathrm{H}$ |  |  |  |  | RW |
| D2007+100*n | Error code of slave station number n error | 0 | $603 \mathrm{FH}-0010 \mathrm{H}$ |  |  |  |  | R |
| D2008+100*n | Control word of slave station number $n$ | 0 | $6040 \mathrm{H}-0010 \mathrm{H}$ | $\bullet$ |  | $\bullet$ | - | RW |
| D2009+100*n | Status word of slave station number n | 0 | $6041 \mathrm{H}-0010 \mathrm{H}$ | $\triangle$ |  | - | - | R |
| D2010+100*n | Control mode of slave station number n | 2 | $6060 \mathrm{H}-0008 \mathrm{H}$ |  |  |  |  | RW |
| D2011+100*n | Actual mode of slave station number n | 2 | $6061 \mathrm{H}-0008 \mathrm{H}$ |  |  |  |  | R |

## Velocity Control

Slave station number $n=0-7$

| Special D | Description of Function | Default: | PDO Mapping | PDO Default: |  |  |  | R/W |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 | 2 | 3 | 4 |  |
| D2001+100*n | Torque restriction on slave station number n | 0 | $6072 \mathrm{H}-0010 \mathrm{H}$ |  |  |  |  | RW |
| D2012+100*n | Target speed of slave station number $n$ | 0 | $6042 \mathrm{H}-0010 \mathrm{H}$ | $\bullet$ |  |  |  | RW |
| D2013+100*n | Actual speed of slave station number $n$ | 0 | $6043 \mathrm{H}-0010 \mathrm{H}$ | $\triangle$ |  |  |  | R |
| D2014+100*n | Error speed of slave station number $n$ | 0 | $6044 \mathrm{H}-0010 \mathrm{H}$ |  |  |  |  | R |
| D2015+100*n | Acceleration time of slave station number n | 1000 | $604 \mathrm{FH}-0020 \mathrm{H}$ |  |  |  |  | R |
| D2016+100*n | Deceleration time of slave station number $n$ | 1000 | $6050 \mathrm{H}-0020 \mathrm{H}$ |  |  |  |  | RW |

Torque control
Slave station number $n=0-7$

| Special D | Description of Function | Default: | PDO Mapping | PDO Default: |  |  |  | R/W |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 | 2 | 3 | 4 |  |
| D2017+100*n | Target torque of slave station number n | 0 | $6071 \mathrm{H}-0010 \mathrm{H}$ |  |  |  | $\bullet$ | RW |
| D2018+100*n | Actual torque of slave station number n | 0 | $6077 \mathrm{H}-0010 \mathrm{H}$ |  |  |  | A | R |
| D2019+100*n | Actual current of slave station number n | 0 | $6078 \mathrm{H}-0010 \mathrm{H}$ |  |  |  |  | R |

## Position control

Slave station number $\mathrm{n}=0-7$

| Special D | Description of Function | Default: | PDO Mapping | PDO Default: |  |  |  | R/W |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 | 2 | 3 | 4 |  |
| D2020+100*n | Target of slave station number n (L) | 0 | 607AH-0020H |  |  | - |  | RW |
| D2021+100*n | Target of slave station number $\mathrm{n}(\mathrm{H})$ | 0 |  |  |  |  |  | RW |
| D2022+100*n | Actual position of slave station number $n$ (L) | 0 | $6064 \mathrm{H}-0020 \mathrm{H}$ |  |  | - |  | R |
| D2023+100*n | Actual position of slave station number $n$ (H) | 0 |  |  |  |  |  | R |
| D2024+100*n | Speed chart of slave station number n (L) | 10000 | 6081H-0020H |  |  |  |  | RW |
| D2025+100*n | Speed chart of slave station number $\mathrm{n}(\mathrm{H})$ | 0 |  |  |  |  |  | RW |

## 20XXH correspondences: MI MO AI AO

Slave station number $\mathrm{n}=0-7$

| Special D | Description of Function | Default: | PDO Mapping | PDO Default: |  |  |  | R/W |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 | 2 | 3 | 4 |  |
| D2026+100*n | Ml status of slave station number n | 0 | $2026 \mathrm{H}-0110 \mathrm{H}$ |  | - |  |  | RW |
| D2027+100*n | MO setting of slave station number n | 0 | $2026 \mathrm{H}-4110 \mathrm{H}$ |  | $\bullet$ |  |  | RW |
| D2028+100*n | Al1 status of slave station number $n$ | 0 | $2026 \mathrm{H}-6110 \mathrm{H}$ |  | - |  |  | RW |
| D2029+100*n | Al2 status of slave station number $n$ | 0 | $2026 \mathrm{H}-6210 \mathrm{H}$ |  | A |  |  | RW |
| D2030+100*n | Al3 status of slave station number n | 0 | $2026 \mathrm{H}-6310 \mathrm{H}$ |  | A |  |  | RW |
| D2031+100*n | AO1 status of slave station number n | 0 | $2026 \mathrm{H}-\mathrm{A} 110 \mathrm{H}$ |  | $\bullet$ |  |  | RW |
| D2032+100*n | AO2 status of slave station number n | 0 | $2026 \mathrm{H}-\mathrm{A} 210 \mathrm{H}$ |  | $\bullet$ |  |  | RW |
| D2033+100*n | AO3 status of slave station number n | 0 | $2026 \mathrm{H}-\mathrm{A} 310 \mathrm{H}$ |  | $\bullet$ |  |  | RW |

PDO reflection length setting:

| Special D | Description of Function | Default: | R/W |
| :---: | :--- | :---: | :---: |
| D2034+100*n | Real-time transmission setting of slave station number n | 000 AH | RW |
| D2067+100*n | Real-time reception setting of slave station number n | 0000 H | RW |

13-5-4 PLC Communication Address

| Device | Range | Type | Address (Hex) |
| :---: | :---: | :---: | :---: |
| X | $00-37$ (Octal) | bit | $0400-041 \mathrm{~F}$ |
| Y | $00-37$ (Octal) | bit | $0500-051 \mathrm{~F}$ |
| T | $00-159$ | bit/word | $0600-069 \mathrm{~F}$ |
| M | $000-799$ | bit | $0800-0 \mathrm{~B} 1 \mathrm{~F}$ |
| M | $1000-1079$ | bit | $0 B E 8-0 C 37$ |
| C | $0-79$ | bit/word | 0 E00-0E47 |
| D | $00-399$ | word | $1000-118 \mathrm{~F}$ |
| D | $1000-1099$ | word | $13 E 8-144 \mathrm{~B}$ |
| D | $2000-2799$ | word | $17 D 0-1$ AEF |

Command code that can be used

| Function Code | Description of Function | Function target |
| :---: | :--- | :---: |
| 01 | Coil status read | Y,M,T,C |
| 02 | Input status read | X,Y,M,T,C |
| 03 | Read single unit of data | T,C,D |
| 05 | Compulsory single coil status change | Y,M,T,C |
| 06 | Write single unit of data | T,C,D |
| 0 F | Compulsory multiple coil status change | Y,M,T,C |
| 10 | Write multiple units of data | T,C,D |

NOTE: When PLC functions have been activated, LTC can match PLC and drive parameters; this method employs different addresses, drives (default station number is 1, PLC sets station number as 2 )

## 13-6 Introduction to the Command Window

## 13-6-1 Overview of Basic Commands

Ordinary commands

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| LD | Load contact a | $\mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{T}, \mathrm{C}$ | 0.8 |
| LDI | Load contact b | $\mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{T}, \mathrm{C}$ | 0.8 |
| AND | Connect contact a in series | $\mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{T}, \mathrm{C}$ | 0.8 |
| ANI | Connect contact b in series | $\mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{T}, \mathrm{C}$ | 0.8 |
| OR | Connect contact a in parallel | $\mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{T}, \mathrm{C}$ | 0.8 |
| ORI | Connect contact b in parallel | $\mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{T}, \mathrm{C}$ | 0.8 |
| ANB | Series circuit block | $\mathrm{N} / \mathrm{A}$ | 0.3 |
| ORB | Parallel circuit block | $\mathrm{N} / \mathrm{A}$ | 0.3 |
| MPS | Save to stack | $\mathrm{N} / \mathrm{A}$ | 0.3 |
| MRD | Stack read (pointer does not change) | $\mathrm{N} / \mathrm{A}$ | 0.3 |
| MPP | Read stack | $\mathrm{N} / \mathrm{A}$ | 0.3 |

Output command

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| OUT | Drive coil | Y, M | 1 |
| SET | Action continues (ON) | Y, M | 1 |
| RST | Clear contact or register | Y, M, T, C, D | 1.2 |

Timer, counter

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| TMR | 16-bit timer | T-K or T-D commands | 1.1 |
| CNT | 16 -bit counter | C-K or C-D (16-bit) | 0.5 |

Main control command

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| MC | Common series contact connection | N0-N7 | 0.4 |
| MCR | Common series contact release | N0-N7 | 0.4 |

Contact rising edge / falling edge detection command

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| LDP | Start of forward edge detection action | X, Y, M, T, C | 1.1 |
| LDF | Start of reverse edge detection action | X, Y, M, T, C | 1.1 |
| ANDP | Forward edge detection series connection | X, Y, M, T, C | 1.1 |
| ANDF | Reverse edge detection series connection | X, Y, M, T, C | 1.1 |
| ORP | Forward edge detection parallel connection | X, Y, M, T, C | 1.1 |
| ORF | Reverse edge detection parallel connection | X, Y, M, T, C | 1.1 |

Upper / lower differential output commands

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| PLS | Upper differential output | Y, M | 1.2 |
| PLF | Lower differential output | Y, M | 1.2 |

Stop command

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| END | Program conclusion | N/A | 0.2 |

Other commands

| Command <br> Code | Function | OPERAND | Execution <br> Speed (us) |
| :---: | :--- | :---: | :---: |
| NOP | No action | N/A | 0.2 |
| INV | Inverse of operation results | N/A | 0.2 |
| P | Index | P | 0.3 |

13-6-2 Detailed Explanation of Basic Commands

| Command | Function |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| LD | Load contact a |  |  |  |  |  |  |
| Operand | X0-X17 | Y0-Y17 | M0-M799 | T0-159 |  | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | - |
| Explanation | The LD command is used for contact a starting at the left busbar or contact a starting at a contact circuit block; its function is to save current content and save the acquired contact status in the cumulative register. |  |  |  |  |  |  |
| Example | Ladder diagram: |  |  | Command code: |  | Description: |  |
|  |  |  |  | LD | X0 | Load Contact a of XO |  |
|  |  |  |  | AND | X1 | Create series connection to contact a of X1 |  |
|  |  |  |  | OUT | Y1 | Drive Y1 coil |  |
| Command | Function |  |  |  |  |  |  |
| LDI | Load contact b |  |  |  |  |  |  |
| Operand | X0-X17 | Y0-Y17 | M0-M799 | T0-159 |  | C0-C79 | D0-D399 |
|  | The $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | - |
| The LDI command is used for contact $b$ starting at the left busbar or contact $b$ starting $a$ a contact circuit block; its function is to save current content and save the acquired contact status in the cumulative register. <br> Ladder diagram: |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| Example | Ladder diag |  |  | LDI | X0 | Load Contact b of X0 |  |
|  |  |  |  | AND | X1 | Create series connection to contact a of X1 |  |
|  |  |  |  | OUT | Y1 | Drive Y1 coil |  |
| Command | Function |  |  |  |  |  |  |
| AND | Connect contact a in series |  |  |  |  |  |  |
| Operand | X0-X17 | Y0-Y17 | M0-M799 | T0-159 |  | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | - |

The AND command is used to create a series connection to contact a; first reads current Explanation status of the designated series contact and logical operation results before contact in order to perform "AND" operation; saves results in cumulative register.

| Example | Ladder diagram | Comm LDI | $\begin{aligned} & \text { code } \\ & \text { X1 } \end{aligned}$ | Description: <br> Load Contact b of X1 |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AND | X0 | Create series connection to contact a of XO |
|  |  | OUT | Y1 | Drive Y1 coil |


| Command | Function |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ANI | Connect contact b in series |  |  |  |  |  |  |
| Operand | X0-X17 | Y0-Y17 | M0-M799 | T0-159 |  | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | - |
| Explanation | The ANI command is used to create a series connection to contact b ; its function is to first read current status of the designated series contact and logical operation results before contact in order to perform "AND" operation; saves results in cumulative register. |  |  |  |  |  |  |
| Example | Ladder diagram: |  |  | Command code: |  | Description: |  |
|  |  |  |  | LD | X1 | Load Co | a of X1 |
|  |  |  |  | ANI | X0 | Create s to contac | $\begin{aligned} & \text { connection } \\ & \text { f X0 } \end{aligned}$ |
|  |  |  |  | OUT |  | Drive Y1 |  |



| Command | Function |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ORI | Connect contact b in parallel |  |  |  |  |  |
| Operand | X0-X17 | $\mathrm{Y} 0-\mathrm{Y} 17$ | M0-M799 | T0-159 | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | - |
| Explanation | The ORI command is used to establish a parallel connection to contact a; its function is to first read current status of the designated series contact and logical operation results before contact in order to perform "OR" operation; saves results in cumulative register. |  |  |  |  |  |
| Example | Ladder diagram:$\times 0$ |  |  | Command code: |  | cription: |
|  |  |  |  | LD | X0 Load | act a of X0 |
|  | $\times 1$ |  |  | ORI | X1Create <br> conne <br> of $\mathbf{X 1}$ | ies to contact b |
|  |  |  |  | OUT | Y1 Drive |  |


| Command | Function |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| ANB | Series circuit block |  |  |  |
| Operand | N/A |  |  |  |
| Explanation | ANB performs an "AND" operation on the previously saved logic results and the current cumulative register content. |  |  |  |
| Example | Ladder diagram: | Command code: |  | Description: |
| Example | - ${ }^{1} 0$ ANB ${ }^{\text {X1 }}$ | LD | X0 | Load Contact a of X0 |
|  |  | ORI | X2 | connection to contact $b$ of X2 |
|  | - 1 | LDI | X1 | Load Contact b of X1 |
|  | Block A Block B | OR | X3 | Establish parallel connection to contact a of X3 |
|  |  | ANB |  | Series circuit block |
|  |  | OUT | Y1 | Drive Y1 coil |



| Command | Function |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OUT | Drive coil |  |  |  |  |  |
| Operand | X0-X17 | Y0-Y17 | M0-M799 | T0-159 | C0-C79 | D0-D399 |
|  | - | $\checkmark$ | $\checkmark$ | - | - | - |


| Explanation | Outputs result of logical operation before OUT command to the desig Coil contact action: |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Result: | Out command |  |  |
|  |  | Coil | Access Point: |  |
|  |  |  | Contact a (NO) | Contact b (NC) |
|  | FALSE | Off | Not conducting | Conducting |
|  | TRUE | On | Conducting | Not conducting |


| Examp | Ladder diagram: | Comm | code | Description: |
| :---: | :---: | :---: | :---: | :---: |
| Example |  | LD | X0 | Load Contact b of X0 |
|  |  | AND | X1 | Establish parallel connection to contact a of X1 |
|  |  | OUT | Y1 | Drive Y1 coil |





Explanation
When the CNT command is executed from Off $\rightarrow$ On, this indicates that the designated counter coil goes from no power $\rightarrow$ electrified, and 1 will be added to the counters count value; when the count reaches the designated value (count value $=$ set value), the contact will have the following action:

| NO (Normally Open) contact | Closed |
| :---: | :---: |
| NC (Normally Close) contact | Open |

After the count value has been reached, the contact and count value will both remain unchanged even if there is continued count pulse input. Please use the RST command if you wish to restart or clear the count.

| Example | Ladder diagram:X0 |  |  | Command code: <br> LD X0 |  | Description: |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | Load | act a of X0 |
|  | CNT | T C2 | K100 |  |  | CNT C2 K100 |  | C2counter Set value as K100 |  |
| Command | Function |  |  |  |  |  |  |
| LDP | Start of forward edge detection action |  |  |  |  |  |  |
| Operand | X0-X17 Y0 | Y0-Y17 | M0-M799 | T0-159 |  | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | - |
| Explanation | The LDP command has the same usage as LD, but its action is different; its function is to save current content, while also saving the detected state of the rising edge of the contact to the cumulative register. |  |  |  |  |  |  |
| Example | Ladder diagram: |  |  | Command code: |  | Description: |  |
|  |  |  |  | LDP | X0 | Start of edge d | forward ction action |
|  |  |  |  | AND | X1 | Create connec a of X1 | ries to contact |
|  |  |  |  | OUT | Y1 | Drive $Y$ |  |
| Remark | Please refer to the function specifications table for each device in series for the scope of usage of each operand. <br> A rising edge contact will be TRUE after power is turned on if the rising edge contact is On before power is turned on to the PLC. |  |  |  |  |  |  |





| Command | Function |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ORP | Forward edge detection parallel connection |  |  |  |  |  |
|  | $\mathrm{X} 0-\mathrm{X} 17$ | $\mathrm{Y} 0-\mathrm{Y} 17$ | $\mathrm{M} 0-\mathrm{M} 799$ | T0-159 | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | - |

Explanation The ORP command is used for a contact rising edge detection parallel connection.
Command code:
Cxample

| Command | Function |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ORF | Reverse edge detection parallel connection |  |  |  |  |  |
| Operand | $\mathrm{X} 0-\mathrm{X} 17$ | $\mathrm{Y} 0-\mathrm{Y} 17$ | $\mathrm{M} 0-\mathrm{M} 799$ | T0-159 | C0-C79 | D0-D399 |
|  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | - |

Explanation The ORF command is used for contact falling edge detection parallel connection.
Command code:
Ladder diagram:
Example



| Command | Function |
| :---: | :--- |
| END | Program conclusion |
| Operand | N/A |
|  | An END command must be added to the end of a ladder diagram program or command |
| Explanation |  |
| address. The PLC will scan from address 0 to the END command, and will return to |  |
| addins scanning again after execution. |  |


| Command | Function |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| NOP | No action |  |  |  |
| Operand | N/A |  |  |  |
| Explanation | The command NOP does not perform any operation in the program. Because execution of this command will retain the original logical operation results, it can be used in the following situation: the NOP command can be used instead of a command that is deleted without changing the program length. |  |  |  |
| Example | Ladder diagram: <br> NOP command will be simplified and not displayed when the ladder diagram is displayed. | Comm | code: | Description: |
|  |  | LD | X0 | Load Contact b of X0 |
|  |  | NOP |  | No action |
|  |  | OUT | Y1 | Drive Y1 coil |


| Command <br> INV | Inverse of operation results | Function |
| :---: | :--- | :--- | :--- | :--- |
| Operand |  | N/A |
| Explanation | Saves the result of the logic inversion operation prior to the INV command in the <br> cumulative register. |  |


| Command | Function |  |
| :---: | :--- | :--- |
| $\mathbf{P}$ | Index | P0-P255 |
| Operand |  |  |

Pointer P is used to subprogram call command API 01 CALL. User does not require
Explanation starting from zero, but the number cannot be used repeatedly, otherwise an unpredictable error will occur.


13-6-3 Overview of Application Commands

| Classification | API | Command Code |  | P command | Function | STEPS |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 16 bit | 32 bit |  |  | 16 bit | 32 bit |
| Circuit control | 01 | CALL | - | $\checkmark$ | Call subprogram | 3 | - |
|  | 2 | SRET | - | - | Conclusion of subprogram | 1 | - |
|  | 06 | FEND | - | - | Conclusion a main program | 1 | - |
| Send comparison | 10 | CMP | DCMP | $\checkmark$ | Compares set output | 7 | 13 |
|  | 11 | ZCP | DZCP | $\checkmark$ | Range comparison | 9 | 17 |
|  | 12 | MOV | DMOV | $\checkmark$ | Data movement | 5 | 9 |
|  | 13 | SMOV | DSMOV | $\checkmark$ | Nibble movement | 11 | 21 |
|  | 15 | BMOV | - | $\checkmark$ | Send all | 7 | - |
| Four logical operations | 18 | BCD | DBCD | $\checkmark$ | BIN to BCD transformation | 5 | 9 |
|  | 19 | BIN | DBIN | $\checkmark$ | BCD to BIN transformation | 5 | 9 |
|  | 20 | ADD | DADD | $\checkmark$ | BIN addition | 7 | 13 |
|  | 21 | SUB | DSUB | $\checkmark$ | BIN subtraction | 7 | 13 |
|  | 22 | MUL | DMUL | $\checkmark$ | BIN multiplication | 7 | 13 |
|  | 23 | DIV | DDIV | $\checkmark$ | BIN division | 7 | 13 |
|  | 24 | INC | DINC | $\checkmark$ | BIN add one | 3 | 5 |
|  | 25 | DEC | DDEC | $\checkmark$ | BIN subtract one | 3 | 5 |
| Rotational displacement | 30 | ROR | DROR | $\checkmark$ | Right rotation | 5 | - |
|  | 31 | ROL | DROL | $\checkmark$ | Left rotation | 5 | - |
| Data Process | 40 | ZRST | - | $\checkmark$ | Clear range | 5 | - |
|  | 41 | DECO | DDECO | $\checkmark$ | Decoder | 7 | 13 |
|  | 42 | ENCO | DENCO | $\checkmark$ | Encoder | 7 | 13 |
|  | 43 | SUM | DSUM | $\checkmark$ | ON bit number | 5 | 9 |
|  | 44 | BON | DBON | $\checkmark$ | ON bit judgement | 7 | 13 |
|  | 49 | FLT | DFLT | $\checkmark$ | BIN whole number $\rightarrow$ binary floating point number transformation | 5 | 9 |
| Floating point operation | 110 | - | DECMP | $\checkmark$ | Comparison of binary floating point numbers | - | 13 |
|  | 111 | - | DEZCP | $\checkmark$ | Comparison of binary floating point number range | - | 17 |
|  | 116 | - | DRAD | $\checkmark$ | Angle $\rightarrow$ Diameter | - | 9 |
|  | 117 | - | DDEG | $\checkmark$ | Diameter $\rightarrow$ angle | - | 9 |
|  | 120 | - | DEADD | $\checkmark$ | Binary floating point number addition | - | 13 |
|  | 121 | - | DESUB | $\checkmark$ | Binary floating point number subtraction | - | 13 |
|  | 122 | - | DEMUL | $\checkmark$ | Binary floating point number multiplication | - | 13 |
|  | 123 | - | DEDIV | $\checkmark$ | Binary floating point number division | - | 13 |
|  | 124 | - | DEXP | $\checkmark$ | Binary floating point number obtain exponent | - | 9 |
|  | 125 | - | DLN | $\checkmark$ | Binary floating point number obtain logarithm | - | 9 |
|  | 127 | - | DESQR | $\checkmark$ | Binary floating point number find square root | - | 9 |
|  | 129 | INT | DINT | $\checkmark$ | Binary floating point number $\rightarrow$ BIN whole number transformation | 5 | 9 |
|  | 130 | - | DSIN | $\checkmark$ | Binary floating point number SIN operation | - | 9 |
|  | 131 | - | DCOS | $\checkmark$ | Binary floating point number COS operation | - | 9 |
|  | 132 | - | DTAN | $\checkmark$ | Binary floating point number TAN operation | - | 9 |
|  | 133 | - | DASIN | $\checkmark$ | Binary floating point number ASIN operation | - | 9 |


| Classification | API | Command Code |  | $P$ command | Function | STEPS |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 16 bit | 32 bit |  |  | 16 bit | 32 bit |
|  | 134 | - | DACOS | $\checkmark$ | Binary floating point number ACOS operation | - | 9 |
|  | 135 | - | DATAN | $\checkmark$ | Binary floating point number ATAN operation | - | 9 |
|  | 136 | - | DSINH | $\checkmark$ | Binary floating point number SINH operation | - | 9 |
|  | 137 | - | DCOSH | $\checkmark$ | Binary floating point number COSH operation | - | 9 |
|  | 138 | - | DTANH | $\checkmark$ | Binary floating point number TANH operation | - | 9 |
| Other | 147 | SWAP | DSWAP | $\checkmark$ | Exchange the up/down 8 bits | 3 | 5 |
| $\begin{gathered} \text { communicatio } \\ \mathrm{n} \end{gathered}$ | 150 | MODRW | - | $\checkmark$ | MODBUS read/write | 7 | - |
| Calendar | 160 | TCMP | - | $\checkmark$ | Compare calendar data | 11 | - |
|  | 161 | TZCP | - | $\checkmark$ | Compare calendar data range | 9 | - |
|  | 162 | TADD | - | $\checkmark$ | Calendar data addition | 7 | - |
|  | 163 | TSUB | - | $\checkmark$ | Calendar data subtraction | 7 | - |
|  | 166 | TRD | - | $\checkmark$ | Calendar data read | 3 | - |
| GRAY code | 170 | GRY | DGRY | $\checkmark$ | $\mathrm{BIN} \rightarrow$ GRY code transformation | 5 | 9 |
|  | 171 | GBIN | DGBIN | $\checkmark$ | GRY code $\rightarrow$ BIN transformation | 5 | 9 |
| Contact form logical operation | 215 | LD\& | DLD\& | - | Contact form logical operation LD\# | 5 | 9 |
|  | 216 | LD\| | DLD\| | - | Contact form logical operation LD\# | 5 | 9 |
|  | 217 | LD^ | DLD^ | - | Contact form logical operation LD\# | 5 | 9 |
|  | 218 | AND\& | DAND\& | - | Contact form logical operation AND\# | 5 | 9 |
|  | 219 | ANDI | DANDI | - | Contact form logical operation AND\# | 5 | 9 |
|  | 220 | AND^ | DAND^ | - | Contact form logical operation AND\# | 5 | 9 |
|  | 221 | OR\& | DOR\& | - | Contact form logical operation OR\# | 5 | 9 |
|  | 222 | OR\| | DOR\| | - | Contact form logical operation OR\# | 5 | 9 |
|  | 223 | OR^ | DOR^ | - | Contact form logical operation OR\# | 5 | 9 |
| Contact form compare command | 224 | LD = | DLD = | - | Contact form compare LD* | 5 | 9 |
|  | 225 | LD > | DLD > | - | Contact form compare LD* | 5 | 9 |
|  | 226 | LD < | DLD < | - | Contact form compare LD* | 5 | 9 |
|  | 228 | LD < > | DLD < > | - | Contact form compare LD* | 5 | 9 |
|  | 229 | LD < = | DLD < = | - | Contact form compare LD* | 5 | 9 |
|  | 230 | LD > = | DLD > = | - | Contact form compare LD* | 5 | 9 |
|  | 232 | AND = | DAND = | - | Contact form compare AND* | 5 | 9 |
|  | 233 | AND > | DAND > | - | Contact form compare AND* | 5 | 9 |
|  | 234 | AND < | DAND < | - | Contact form compare AND* | 5 | 9 |
|  | 236 | AND < > | DAND < > | - | Contact form compare AND* | 5 | 9 |
|  | 237 | AND < = | DAND < = | - | Contact form compare AND* | 5 | 9 |
|  | 238 | AND > = | DAND > = | - | Contact form compare AND* | 5 | 9 |
|  | 240 | OR = | DOR = | - | Contact form compare OR* | 5 | 9 |
|  | 241 | OR > | DOR > | - | Contact form compare OR* | 5 | 9 |
|  | 242 | OR < | DOR < | - | Contact form compare OR* | 5 | 9 |


| Classification | API | Command Code |  | $\begin{gathered} P \\ \text { command } \end{gathered}$ | Function | STEPS |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 16 bit | 32 bit |  |  | 16 bit | 32 bit |
|  | 244 | OR < > | DOR < > | - | Contact form compare OR* | 5 | 9 |
|  | 245 | OR< = | DOR<= | - | Contact form compare OR* | 5 | 9 |
|  | 246 | OR > = | DOR > = | - | Contact form compare OR* | 5 | 9 |
| Floating point contact form | 275 | - | FLD $=$ | - | Floating point number contact form compare LD* | - | 9 |
|  | 276 | - | FLD > | - | Floating point number contact form compare LD* | - | 9 |
|  | 277 | - | FLD < | - | Floating point number contact form compare LD* | - | 9 |
| Compare command | 278 | - | FLD < > | - | Floating point number contact form compare LD* | - | 9 |
|  | 279 | - | FLD < = | - | Floating point number contact form compare LD* | - | 9 |
|  | 280 | - | FLD > = | - | Floating point number contact form compare LD* | - | 9 |
|  | 281 | - | FAND = | - | Floating point number contact form compare AND* | - | 9 |
|  | 282 | - | FAND > | - | Floating point number contact form compare AND* | - | 9 |
|  | 283 | - | FAND < | - | Floating point number contact form compare AND* | - | 9 |
|  | 284 | - | FAND < > | - | Floating point number contact form compare AND* | - | 9 |
|  | 285 | - | FAND < = | - | Floating point number contact form compare AND* | - | 9 |
|  | 286 | - | FAND > = | - | Floating point number contact form compare AND* | - | 9 |
|  | 287 | - | FOR = | - | Floating point number contact form compare OR* | - | 9 |
|  | 288 | - | FOR > | - | Floating point number contact form compare OR* | - | 9 |
|  | 289 | - | FOR < | - | Floating point number contact form compare $\mathrm{OR}^{*}$ | - | 9 |
|  | 290 | - | FOR < > | - | Floating point number contact form compare OR* | - | 9 |
|  | 291 | - | FOR < = | - | Floating point number contact form compare OR* | - | 9 |
|  | 292 | - | FOR > = | - | Floating point number contact form compare OR* | - | 9 |
| Drive special command | 139 | RPR | - | $\checkmark$ | Read servo parameter | 5 | - |
|  | 140 | WPR | - | $\checkmark$ | Write servo parameter | 5 | - |
|  | 141 | FPID | - | $\checkmark$ | Drive PID control mode | 9 | - |
|  | 142 | FREQ | - | $\checkmark$ | Drive torque control mode | 7 | - |
|  | 262 | - | DPOS | $\checkmark$ | Set target | - | 5 |
|  | 263 | TORQ | - | $\checkmark$ | Set target torque | 5 | - |
|  | 261 | CANRX | - | $\checkmark$ | Read CANopen slave station data | 9 | - |
|  | 264 | CANTX | - | $\checkmark$ | Write CANopen slave station data | 9 | - |
|  | 265 | CANFLS | - | $\checkmark$ | Refresh special D corresponding to CANopen | 3 | - |
|  | 320 | ICOMR | DICOMR | $\checkmark$ | Internal communications read | 9 | 17 |
|  | 321 | ICOMW | DICOMW | $\checkmark$ | Internal communications write | 9 | 17 |
|  | 323 | WPRA | - | - | RAM write in drive parameters | 5 | - |

13-6-4 Detailed Explanation of Applications Commands

| $\frac{\text { API }}{01}$ |  | CALL |  | P | S |  |  |  |  | Call subprogram |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit device |  |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (3 STEP) |  |  |  |
| Notes on operand usage: <br> The S operand can d |  |  |  |  | H | KnX | KnY | KnM | T | C | D | CALL | Continuous execution type | CALLP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | 32-bit command |  |  |  |
|  |  |  |  |  | LTC series device: The S operand can designate P0-P63 |  |  |  |  |  |  |  |  |  |  |  | - | - | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |  |

Explanation

- $\mathbf{S}$ : Call subprogram pointer.
- Write the subprogram after the FEND command.
- The subprogram must end after the SRET command.
- Refer to the FEND command explanation and sample content for detailed command functions.



Explanation

- A contact-driven command is not needed. Automatically returns next command after CALL command
- Indicates end of subprogram. After end of subprogram, SRET returns to main program, and executes next command after the original call subprogram CALL command.
- Refer to the FEND command explanation and sample content for detailed command functions.


Explanation - This command indicates the end of the main program. It is the same as the END command when the PLC executes this command.

- The CALL command program must be written after the FEND command, and the SRET command added to the end of the subprogram.
- When using the FEND command, an END command is also needed. However, the END command must be placed at the end, after the main program and subprogram.

CALL command process

| API | D CMP | $\mathbf{P}$ | S1 S2 (D) | Compares set output |
| :---: | :--- | :--- | :--- | :--- | :--- | :--- |
| 10 | D |  |  |  |


|  |  | dev |  |  |  |  | Vord | devic |  |  |  | 16-bit co | mand (7 STE |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | CMP | Continuous | CMPP | Pulse |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  | execution type |
| S2 |  |  |  | * | * | * | * | * | * | * | * |  |  |  |  |
| D |  | * | * |  |  |  |  |  |  |  |  | 32-bit command (13 STEP) |  |  |  |
| Notes on operand usage:The operand D occupies three consecutive points |  |  |  |  |  |  |  |  |  |  |  | DCMP | Continuous execution type | DCMPP | Pulse <br> execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag sign | l: none |  |  |

- Compares the size of the content of operand
(S1) and S2; the results of comparison are expressed in (D.
- Size comparison is performed algebraically. All data is compared in the form of numerical binary values. Because this is a 16 -bit command, when b15 is 1 , this indicates a negative number.
- When the designated device is Y0, it automatically occupies Y0, Y1 and Y2.
- When $\mathrm{X} 10=$ On, the CMP command executes, and Y0, Y1 or Y2 will be On. When $\mathrm{X} 10=\mathrm{Off}$, the CMP command will not execute, and the state of $\mathrm{Y} 0, \mathrm{Y} 1$ and Y 2 will remain in the state prior to X10=Off.
- If $\geq, \leq$, or $\neq$ results are needed, they can be obtained via series/parallel connections of $\mathrm{YO}-\mathrm{Y} 2$.

- To clear results of comparison, use the RST or ZRST command.


| API | D | ZCP | $\mathbf{P}$ | S1 S2 S S S | Sange comparison |
| :---: | :--- | :--- | :--- | :--- | :--- | :--- |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (9 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | ZCP | Continuous | ZCPP | Pulse |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  | execution type |
| S2 |  |  |  | * | * | * | * | * | * | * | * |  |  |  |  |
| S |  |  |  | * | * | * | * | * | * | * | * | 32-bit co | mand (17 ST |  |  |
| D |  | * | * |  |  |  |  |  |  |  |  | DZCP | Continuous execution type | DZCPP | Pulse execution type |

## Notes on operand usage:

The content value of operand S 1 is less than the content value of Flag signal: none S2 operand
The operand D occupies three consecutive points

- S1): Lower limit of range comparison. S2): Upper limit of range comparison. (S): Comparative value. D: Results of comparison.
- When the comparative value $S$ is compared with the lower limit S1 and upper limit S 2 , the results of comparison are expressed in D .
- When lower limit S1 > upper limit S2, the command will use the lower limit (S1) to perform comparison with the upper and lower limit.
- Size comparison is performed algebraically. All data is compared in the form of numerical binary values. Because this is a 16-bit command, when b15 is 1 , this indicates a negative number.


## Example

- When the designated device is M0, it automatically occupies M0, M1 and M2.
- When $\mathrm{X} 0=\mathrm{On}$, the ZCP command executes, and M0, M1 or M2 will be On. When X0=Off, the ZCP command will not execute, and the state of MO, M1 or M2 will remain in the state prior to $\mathrm{X} 0=\mathrm{Off}$.
- If $\geq, \leq$, or $\neq$ results are needed, they can be obtained via series/parallel connections of M0-M2.

- To clear results of comparison, use the RST or ZRST command.


| API |  | MOV | $\mathbf{P}$ | S | D |
| :---: | :--- | :--- | :--- | :--- | :--- |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (5 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | MOV | Continuous | MOVP | Pulse |
| S |  |  |  | * | * | * | - | * | * |  | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  | * | * | * | * | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DMOV | Continuous execution type | DMOVP | Pulse execution type |

Flag signal:
Explanation - S: Data source. D: Destination of data movement.

- When this command is executed, the content of $S$ will be directly moved to D. When the command is not executed, the content of will not change.

Example

- When $\mathrm{X} 0=\mathrm{Off}$, the content of D 10 will not change; if $\mathrm{X} 0=\mathrm{On}$, the value K 10 will be sent to data register D10.
- When $\mathrm{X} 1=$ Off, the content of D10 will not change; if $\mathrm{X} 1=\mathrm{On}$, the current value of T0 will be sent to data register D10.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (11 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | MOV | Continuous | SMOVP | Pulse |
| S |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  | * | * | * | * | * |  |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  | 32-bit command (21 STEP) |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DSMOV | Continuous execution type | $\begin{gathered} \text { DSMOV } \\ \mathrm{P} \end{gathered}$ | Pulse execution type |

Explanation - S: Data source. m 1 : The data source transfers starting bit number. (m2) : The data source transfers individual bit number. D: Transfer destination. n Transferring starting bit number of the destination.

- BCD mode (M1168 = Off): SMOV enables and operates BCD under this mode, the operation is similar to the way SMOV operates decimal numbers. The command copies specific bit number of arithmetic element $S$ ( $S$ is a 4-figure decimal number), and sends the bit number to arithmetic element $D$ ( $D$ is also a 4 -figure decimal number). The current data on the target register will be covered.
- $m_{1}$ range: $1-4$
- $m_{2}$ range: $1-m_{1}\left(m_{2}\right.$ cannot be larger than $\left.m_{1}\right)$
- $n$ range: $m_{2}-4$ ( $n$ cannot be smaller than $m_{2}$ )
- When M1168 = Off (BCD mode), X0 is ON, the instruction transfers two digits of the decimal number starting from the fourth digit of the decimal number (the digit in the thousands place of the decimal number) in D10 to the two digits of the decimal number starting from the third digit of the decimal number (the digit in the hundreds place of the decimal number) in D20. After the instruction is executed, the digits in the thousands place of the decimal number $\left(10^{3}\right)$ and the ones place of the decimal number $\left(10^{\circ}\right)$ in D20 are unchanged.


D10 (16-bit binary number)
Automatic conversion
D10 (4-digit binary-coded decimal)
Transferring the digits
D20 (4-digit binary-coded decimal)
Automatic conversion
D20 (16-bit binary number)

Example 2

- When M1168 is On (BIN mode), and the SMOV command is executed, D10 and D20 do not change in BCD mode, but send 4 digits as a unit in BIN mode.


D10 (16-bit binary number)
Transferring the digits
D20 (16-bit binary number)
$4^{\text {th }}$ digit
$3^{\text {rd }}$ digit $\quad 2^{\text {nd }}$ digit $\quad 1^{\text {st }}$ digit

| API |  |  |  |  |
| :---: | :--- | :--- | :--- | :--- |
| 15 | $\square$ | BMOV | $\mathbf{P}$ | S D |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (7STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | BMOV | Continuous | BMOVP | Pulse |
| S |  |  |  |  |  | * | * | * | * | * | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  | * | * | * | * | * |  |  |  |  |
| n |  |  |  | * | * |  |  |  | * | * |  | 32-bit command |  |  |  |
| Notes on operand usage: <br> n operand scope $\mathrm{n}=1$ to 512 |  |  |  |  |  |  |  |  |  |  |  | - | - | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag sign | al: none |  |  |

Explanation - : Initiate source device. D: Initiate destination device. $n$ : Send block length.

- The content of $n$ registers starting from the initial number of the device designated by will be sent to the n registers starting from the initial number of the device designated by $n$; if the number of points referred to by $n$ exceeds the range used by that device, only points within the valid range will be sent.

Example 1

- When X10=On, the content of registers D0-D3 will be sent to the four registers D20 to D23.


- If the designated bit devices $\mathrm{KnX}, \mathrm{KnY}$, and KnM are sent, S and D must have the same number of nibbles, which implies that $n$ must be identical.

- In order to prevent overlap between the transmission addresses of two operands, which would cause confusion, make sure that the addresses designated by the two operands have different sizes, as shown below:
When $S>D$, send in the order $(1) \rightarrow$ (2) $\rightarrow$ (3).


When $S$ ( D , send in the order (3) $\rightarrow$ (2) $\rightarrow$ (1).



Explanation - S: Data source. D: Destination of data movement.

- The content of data source $S$ (BIN value, 0-9999) executes BCD transformation and saves in $D$.
- Arithmetic elements $S$ and $D$ use the $F$ device, it can only use 16-bit command.

Example

- When $\mathrm{X0}$ is ON, and the BIN value of D10 is transformed to BCD value, the digit is saved in 4-bit element of K1Y0 (Y0-Y3).

| B0 | BCD | D10 | K1Y0 |
| :--- | :--- | :--- | :--- |

■ If $\mathrm{D} 10=001 \mathrm{E}(\mathrm{Hex})=0030$ (Decimal), the executed result will be $\mathrm{Y} 0-\mathrm{Y} 3=0000$ (BIN).

| API | BIN | P | S (D) | BCD to BIN transformation |
| :---: | :---: | :--- | :--- | :--- | :--- |
| 19 | $\mathbf{D}$ | (D | $\mathbf{P}$ |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (5STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | BIN | Continuous | BINP | Pulse |
| S |  |  |  |  |  | * | * | * | * |  | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  | * | * | * | * | * |  |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  | 32-bit command (9 STEP) |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DBIN | Continuous execution type | DBINP | Pulse execution type |

Explanation - S: Data source. D: Transformation result.

- The content of data source (SCD: 0-9,999) executes BIN transformation and saves in D.
- Valid number range of the data source S: BCD (0-9,999), DBCD (0-99,999,999).

Example

- When X0 is ON, and the BCD value of K1X20 is transformed to BIN value, the result saves in D10.

- When PLC reads a BCD type switch-off from the outside, it has to use the BIN command to transform the read data to BIN value, then saves the value into PLC.

| API | D | ADD | $\mathbf{P}$ | (S1) S2) (D) | BIN addition |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 20 | D |  |  |  |  |


|  |  | devid |  |  |  |  | Vord | devic |  |  |  | 16-bit co | mmand (7 ST |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | ADD | Continuous | ADDP | Pulse |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  | execution type |
| S2 |  |  |  | * | * | * | * | * | * | * | * |  |  |  |  |
| D |  |  |  |  |  |  | * | * | * | * | * | 32-bit command (13 STEP) |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  | DADD | Continuous execution type | DADDP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: M1020 Zero flag <br> M1021 Borrow flag M1022 Carry flag Please refer to the following supplementary explanation |  |  |  |

Explanation - S1: Augend. S2: Addend. D: Sum.

- Using two data sources: The result of adding S1 and S2 using the BIN method will be stored in D.
- The highest bit of any data is symbolized as bit 0 indicating (positive) 1 indicating (negative), enabling the use of algebraic addition operations. (for instance: $3+(-9)$ $=-6$ )
- Flag changes connected with the addition.

1. When calculation results are 0 , the zero flag M1020 will be On.
2. When calculation results are less than $-32,768$, the borrow flag M1021 will be On.
3. When calculation results are greater than 32,767 , the carry flag M1022 will be On.

16-bit BIN addition: When $\mathrm{XO}=\mathrm{On}$, the result of the content of addend DO plus the content of augend D10 will exist in the content of D20.


Remark

- Relationship between flag actions and negative/positive numbers:

16-bit:


32-bit:


| API | D | SUB | $\mathbf{P}$ | S1 S2 (D) | BIN subtraction |
| :---: | :---: | :--- | :--- | :--- | :--- | :--- |
| 21 | D |  | P |  |  |



Explanation

- S1: Minuend. S2: Subtrahend.

D : Difference.

- Using two data sources: The result of subtraction of S1 and us2 using the BIN method is stored in D.
- The highest bit of any data is symbolized as bit 0 indicating (positive) 1 indicating (negative), enabling the use of algebraic subtraction operations.
- Flag changes connected with subtraction.

1. When calculation results are 0 , the zero flag M1020 will be On.
2. When calculation results are less than $-32,768$, the borrow flag M 1021 will be On.
3. When calculation results are greater than 32,767 , the carry flag M1022 will be On.

## Example

- 16-bit BIN subtraction: When $\mathrm{X} 0=\mathrm{On}$, the content of D10 is subtracted from the content of D0, and the difference is stored in D20.


| API | M | MUL | $\mathbf{P}$ | S1 | S2 | D |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 22 | BIN multiplication |  |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (7 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | MUL | Continuous | MULP | Pulse |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  | execution type |

Notes on operand usage:
The 16 -bit command operand D will occupy 2 consecutive points
32-bit command (13 STEP)

| DMUL | Continuous | DMULP | Pulse |
| :---: | :---: | :---: | :---: |
| execution type |  |  |  |

Flag signal: none
D : Product.
and S2 are multiplied using the BIN method, the product is stored in $D$. method, the product is stored in .
16-bit BIN multiplication operation:

b 15 is a symbol bit b 15 is a symbol bit b 31 is a symbol bit (b15 of $\mathrm{D}+1$ )
Symbol bit $=0$ refers to a positive value Symbol bit = 1 refers to a negative vlalue

When $D$ is a bit device, K1-K4 can be designated as a hexadecimal number, which will occupy 2 consecutive units.

- When 16 -bit DO is multiplied by 16 -bit D10, the result will be a 32 -bit product; the upper 16 bits will be stored in D21, and the lower 16 bits will be stored in D20. Whether the bit at the farthest left is Off or On will indicate the sign of the result.


| API | D | DIV | $\mathbf{P}$ | S1 S2 (D) | BIN division |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 23 | $\mathbf{D}$ |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit co | nmand (7 STE |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | c | D | DIV | Continuous | DIVP | Pulse |
| S1 |  |  |  |  |  |  |  | * | * |  |  |  |  |  | execution type |
| S2 |  |  |  | * | * | * | * | * | * | * | * |  |  |  |  |
| D |  |  |  |  |  |  | * | * | * | * | * | 32-bit command (13 STEP) |  |  |  |
| Notes on operand usage: <br> The 16 -bit command operand $D$ will occupy 2 consecutive points |  |  |  |  |  |  |  |  |  |  |  | DDIV | Continuous execution type | DDIVP | $\begin{array}{\|c\|} \hline \text { Pulse } \\ \text { execution type } \end{array}$ |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

- S1: Dividend. S2: Divisor. D: Quotient and remainder.
- Using two data sources: The quotient and remainder will be stored in $D$ when S1 and S2 are subjected to division using the BIN method. The sign bit for (S1), S2 and D must be kept in mind when performing a 16-bit operation.

16-bit BIN division:


If $D$ is a bit device, $K 1-K 4$ can be designated 16 bits, which will occupy 2 consecutive units and yield the quotient and remainder.

## Example

- When $\mathrm{X0} 0=\mathrm{On}$, the quotient resulting from division of dividend D0 by divisor D10 will be placed in D20, and the remainder will be placed in D21. Whether the highest bit is Off or On will indicate the sign of the result.


| API |  | INC |  | D | D |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 24 | D | PIN add one |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16 -bit command (3 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D |  | Continuous | INCP |  |
| D |  |  |  |  |  |  |  |  |  |  |  |  | execution type |  | execution type |

Notes on operand usage: none

| 32-bit comand |  |  |  |
| :---: | :---: | :---: | :---: |
| DINC | Continuous <br> execution type | DINCP | Pulse <br> execution type |

Flag signal: none
Explanation - D: Destination device.

- If a command is not the pulse execution type, when the command is executed, the program will add 1 to the content of device (D) for each scanning cycle.
- This command is ordinarily used as a pulse execution type command (INCP).
- During 16 -bit operation, $32,767+1$ will change the value to $-32,768$. During 32 bit operation, $2,147,483,647+1$ will change the value to $-2,147,483,648$.

Example

- When $\mathrm{XO}=\mathrm{Off} \rightarrow \mathrm{On}, 1$ is automatically added to the content of DO .

| $\left.\left\lvert\, \begin{array}{\|l\|l\|}\hline \text { INCP } & \text { D0 } \\ \hline\end{array}\right.\right)$ |  |
| :--- | :--- |


| API | DEC |  |  | D | BIN subtract one |
| :---: | :---: | :---: | :---: | :---: | :--- |
| 25 | $\mathbf{D}$ | DE | $\mathbf{P}$ | D |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (3 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | DEC | Continuous | DECP | Pulse |
| D |  |  |  | * | * | * | * | * |  |  |  |  | execution type |  | execution type |
| Notes on operand usage: none $\quad$ - ${ }^{\text {2-bit command }}$ ( 5 STEP) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DDEC | Continuous execution type | DDECP | Pulse execution type |

Flag signal: none
Explanation - D: Destination device.

- If a command is not the pulse execution type, when the command is executed, the program will add 1 to the content of device D for each scanning cycle.
- This command is ordinarily used as a pulse execution type command (DECP).
- During 16-bit operation, $-32,768$ minus 1 will change the value to 32,767 . During 32 bit operation, $-2,147,483,648$ minus 1 will change the value to $-2,147,483,647$.

Example

- When $\mathrm{XO}=\mathrm{Off} \rightarrow \mathrm{On}, 1$ is automatically subtracted from the content of D0.

| $\times 0$ |  |  |
| :---: | :---: | :---: |
|  | DECP | DO |


| AP <br> 30 |  | ROR |  | P | (D) $n$ |  |  |  |  | Right rotation |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (5TEP) |  |  |  |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | ROR | Continuous | RORP | Pulse |
| D |  |  |  |  |  |  | * | * | * | * | * |  | execution type |  | execution type |
| n |  |  |  | * | * |  |  |  |  |  |  | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Only K4 (16-bit) will be valid if the operand $D$ is designated as KnY or KnM. <br> n operand $\mathrm{n}=\mathrm{K} 1-\mathrm{K} 16$ (16-bit) |  |  |  |  |  |  |  |  |  |  |  | 32-bit co | mmand (9 STEP) | DRORP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: M1022 Carry flag |  |  |  |

Explanation - D: Device to be rotated. $n$ : Number of bits for one rotation.

- Rotates the device designated by $D$ to the right $n$ bits.
- This command is ordinarily used as a pulse execution type command (RORP).

Example

- When $\mathrm{X} 0=\mathrm{Off} \rightarrow \mathrm{On}, 4$ of the 16 bits in D10 specify a right rotation; the content of the bit indicated with * (see figure below) will be sent to the carry flag signal M1022.


| API | D | ROL | $\mathbf{P}$ | D | ( |
| :---: | :---: | :---: | :---: | :---: | :--- |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit | mand (5 ST |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | ROL | Continuous | ROLP | Pulse |
| D |  |  |  |  |  |  | * | * | * | * | * |  | execution type |  | execution type |
| n |  |  |  | * | * |  |  |  |  |  |  | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Only K4 (16-bit) will be valid if the operand D is designated as KnY or KnM. <br> n operand $\mathrm{n}=1$ to 16 (16-bit) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DROL | Continuous execution type | DROLP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: M1022 Carry flag |  |  |  |

Explanation - D: Device to be rotated. $n$ : Number of bits for one rotation.

- Rotates the device designated by $D$ to the left $n$ bits.
- This command is ordinarily used as a pulse execution type command (ROLP).

Example

- When $\mathrm{X} 0=\mathrm{Off} \rightarrow \mathrm{On}, 4$ of the 16 bits in D10 specify a left rotation; the content of the bit indicated with * (see figure below) will be sent to the carry flag signal M1022.


Rotate to the left


| API | $\square$ | ZRST | $\mathbf{P}$ | (D1) (D2) |
| :---: | :--- | :--- | :--- | :--- | Clear range | 40 |
| :--- |
|  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit c | mmand (5 ST |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C |  | ZRST | Continuous | ZRSTP | Pulse |
| D1 |  | * | * |  |  |  |  |  | * | * |  |  | execution type |  | execution type |
| D2 |  | * | * |  |  |  |  |  | * | * |  |  |  |  |  |
| Notes on operand usage: <br> Number of operand $\mathrm{D}_{1}$ operand $\leq$ number of operand $\mathrm{D}_{2}$ Operands $D_{1}, D_{2}$ must designate the same type of device Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | 32-bit command |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | - | - | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |

Explanation - $D_{1}$ : Clear ranges initial device. $D_{2}$ : Clear ranges final device.

- When the number of operand $D_{1}>$ number of operand $D_{2}$, only the operand designated by $D_{2}$ will be cleared.


## Example

- When X0 is On, auxiliary relays M300-M399 will be cleared and changed to Off.
- When X1 is On, 16-bit counters C0-C127 will all be cleared. (Writes 0 , and clears and changes contact and coil to Off).
- When X10 is On, timer T0-T127 will all be cleared. (Writes 0, and clears and changes contact and coil to Off).
- When X3 is On, the data in data registers D0-D100 will be cleared and set as 0 .


Remar

- Devices can independently use the clear command (RST), such as bit device $\mathrm{Y}, \mathrm{M}$ and word device T, C, D.


| API | D DECO | $\mathbf{P}$ | S (D) $n$ | Decoder |
| :---: | :--- | :--- | :--- | :--- | :--- |
| 41 | $\mathbf{D}$ |  |  |  |


|  |  | de |  |  |  |  | /ord | devic |  |  |  | 16-bit com | mand (7 STE |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | DECO | Continuous | DECOP | Pulse |
| S | * | * | * | * | * |  |  |  | * | * | * |  | execution type |  | execution type |
| D |  | * | * |  |  |  | * | * | * | * | * |  |  |  |  |
| n |  |  |  | * | * |  |  |  |  |  |  | 32-bit command (13-TTEP) |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  | DDECO | Continuous execution type | DDECOP | Pulse <br> execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signa | l: none |  |  |

Explanation - S: Decoding source device. D: Device that saves the decoding result. (n) : Length of decoding bit.

- Decodes with the lower " $n$ " bit, and saves the length of " 2 " " bit in $D$.
- This command usually uses pulse execution type command (DECOP).
- When $D$ is the bit device, $n=1-8$, when $D$ is the word device, $n=1-4$.
- When Dis the bit device, the valid range of $n$ is $0<n \leqq 8$. If $n=0$ or $n>8$, a fault will occur.
- When $\mathrm{n}=8$, the maximum decoding will be $2^{8}=256$ points.
- When M200 switches from Off to On, the content of X0-X2 is decoded to M100M107.
- If $S=3$, M103 (the third digit starting from M100) $=$ On.
- When the command is executed, M200 turns to Off. The ones that are decoded and outputted act as usual.

- When $D$ is word device, the valid range of $n$ is $0<n \leqq 4$. If $n=0$ or $n>4$, the fault occurs.
- When $n=4$, the maximum decoding will be $2^{4}=16$ points.
- When M200 switches from Off to On, the content of D10 (b2-b0) is decoded to D20 (b7-b0). The unused digits (b15-b8) of D20 become 0.
- The lower 3 digits of D10 are decoded and saved in the lower 8 digits of D20, the upper 8 digits are 0 .
- When the command is executed, M200 turns to Off. The ones that are decoded and outputted act as usual.


| API | ENCO | $\mathbf{P}$ | $S$ D |
| :--- | :--- | :--- | :--- | :--- |
| 42 | $D$ | Encoder |  |


|  |  | de |  |  |  |  | ord | devic |  |  |  | 16-bit com | mand (7 STE |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | ENCO | Continuous | ENCOP | Pulse |
| S | * | * | * |  |  |  |  |  | * | * | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  | * | * | * | * | * | 3-bit command (13----------1) |  |  |  |
| n |  |  |  | * | * |  |  |  |  |  |  | 32-bit command (13 STEP) |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  | DENCO | Continuous execution type | DENCOP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signat | l: none |  |  |

[^0] : Length of encoding bit.

- Encodes the data of lower " 2 "" bit length from encoding source device $S$, and saves the encoding result in D .
- If multiple digits of encoding source device are 1 , the command will process the first digit starting from high digit.
- This command usually uses pulse execution type command (ENCOP).
- When $S$ is the bit device, $\mathrm{n}=1-8$, when S is the word device, $\mathrm{n}=1-4$.
- When $S$ is the bit device, the valid range of $n$ is $0<n \leqq 8$. If $n=0$ or $n>8$, a fault will occur.
- When $\mathrm{n}=8$, the maximum decoding will be $2^{8}=256$ points.
- When X0 switches from Off to On, the content of $2^{3}$ digit (M0-M7) is encoded and saved in the lower 3 digits (b2-b0). The unused digits (b15-b3) in D0 become 0.
- When the command is executed, X0 turns to Off. The data in D is unchanged.


The value becomes 0

- When $S$ is word device, the valid range of $n$ is $0<n \leqq 4$. If $n=0$ or $n>4$, the fault occurs.
- When $n=4$, the maximum decoding will be $2^{4}=16$ points.
- When X0 switches from Off to On, $2^{3}$ digit data of D10 (b0-b7) is encoded and saved in the lower 3 digits (b2-b0) of D20. The unused digits (b15-b3) of D20 become 0. (b8-b15 in D10 are invalid data)
- When the command is executed, X0 turns to Off. The data in D is unchanged.


| API | SUM | $\mathbf{P}$ | S | D | ON bit number |
| :---: | :---: | :---: | :---: | :---: | :--- |
| 43 | $\mathbf{D}$ | S |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (5 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | $Y$ | M | K | H | KnX | KnY | KnM | T | C | D | SUM | Continuous | SUMP | Pulse |
| S |  |  |  | * | * | * | * | * | * |  | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  |  |  | * | * | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DSUM | Continuous execution type | DSUMP | Pulse execution type |

Flag signal: M1020
Explanation - S: Source device. D: Destination of saving counter values.

- The total amount of all digits that is " 1 " in $S$ will be saved in $D$.
- D will use 2 registers when use the 32 -bit command.
- Arithmetic elements $S$ and $D$ use $F$ device, and can only use 16-bit command.
- If there is no bit is ON, the flag signal M1020 will be ON.

Example

- When M200 = On, the total amount of content "1" digit in D0's 16-bit command will be saved in D2.


| API | D | BON | $\mathbf{P}$ | S | D | n |
| :---: | :--- | :--- | :--- | :--- | :--- | :--- |
| $\mathbf{L 4}$ | D | ON bit judgement |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (7 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | BON | Continuous | BONP | Pulse |
| s |  |  |  | * | * | * | * |  | * | * | * |  | execution type |  | execution type |
| D |  | * | * |  |  |  |  |  | * | * | * | 32-bit command (9 STEP) |  |  |  |
| n |  |  |  | * | * |  |  |  |  |  |  |  |  |  |  |
| Notes on operand usage: none |  |  |  |  |  |  |  |  |  |  |  | DBON | Continuous execution type | DBONP | $\begin{gathered} \text { Pulse } \\ \text { execution type } \end{gathered}$ |

Explanation - $S$ : Source device. (D): Destination of saving judging result. $n$ : assign judged digit (numbering from 0)

- The status of specific digit from source device is shown on target position.
- Arithmetic element S uses F device, and can only use the 16 -bit command.
- The valid range of arithmetic element $\mathrm{n}: \mathrm{n}=0-15$ (16-bit), $\mathrm{n}=0-31$ (32-bit).

Example

- When $X 0=$ On, if the $15^{\text {th }}$ digit of $D 0$ is " 1 ", MO is On. If it is " 0 ", M0 is Off.
- When X0 turns to Off, MO remains previous status.

|  | X0 |  |  | BON |  |  | D0 |  | M0 |  | K15 |  |  |  |  | $\mathrm{M} 0=\mathrm{Off}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| D0 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| b15 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | $\mathrm{MO}=\mathrm{On}$ |


| API |  |  |  |  |  |  |
| :---: | :---: | :--- | :--- | :--- | :--- | :--- |
| 49 | $\mathbf{D}$ | FLT | $\mathbf{P}$ | S | D | BIN whole number $\rightarrow$ binary decimal <br> transformation |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16 -bit command (5 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | c | D |  | Continuous | FLTP | Pulse |
| S |  | * | * |  |  |  |  |  |  |  |  |  | execution type |  | execution type |
| D |  | * | * |  |  |  |  |  | * | * | * | 32-bit command ( 9 STEP) |  |  |  |
|  |  |  | nd |  | lea | e refe | rto | e fu |  |  |  | $\frac{32-\mathrm{bit} \mathrm{co}}{\text { DFLT }}$ | Continuous execution type | DFLTP | $\begin{gathered} \text { Pulse } \\ \text { execution type } \end{gathered}$ |

The operand $D$ will occupy 2 consecutive points
Flag signal: none
Explanation - S: Transformation source device. D: Device storing transformation results.

- Transforms BIN whole number into a binary decimal value.

Example

- When M200 is On, converts the whole number of values corresponding to D0 and D1 into floating point numbers, which are placed in D20 and D21.



|  |  | devid |  |  |  |  | Vord | devic |  |  |  | 16-bit com | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S1 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| S2 |  |  |  | * | * |  |  |  |  |  | * | 32-bit command (13 STEP) |  |  |  |
| D |  |  |  | * | * |  |  |  |  |  | * | DECMP | Continuous | DECMP | Pulse |
| Notes on operand usage: $\quad$ erection type $P$ execution type |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| The operand D occupies three consecutive points Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation - $\mathbf{S}_{1}$ : Comparison of binary floating point numbers value 1 . $\mathbf{S}_{2}$ : Comparison of binary floating point numbers value 2. D: Results of comparison, occupies 3 consecutive points.

- When binary floating point number 1 is compared with comparative binary floating point number 2, the result of comparison ( $>,=,<$ ) will be expressed in $\mathbf{D}$.
- If the source operand $\mathbf{S}_{\mathbf{1}}$ or $\mathbf{S}_{\mathbf{2}}$ designates a constant K or H , the command will transform the constant to a binary floating-point number for the purpose of comparison.


## Example

- When the designated device is M10, it will automatically occupy M10-M12.
- When X0=On, the DECMP command executes, and one of M10-M12 will be On. When $\mathrm{X} 0=$ Off, the DECMP command will not execute, and M10-M12 will remain in the $\mathrm{X} 0=$ Off state.
- If results in the form of $\geq, \leq$, or $\neq$ are needed, they can be obtained by series and parallel connection of M10-M12.
- Please use the RST or ZRST command to clear the result.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M |  |  |  |  |  |  |  |  |  |  |  |  |
| S1 |  |  |  | * | * |  |  |  |  |  | * | - | - | - | - |
| S2 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| S |  |  |  | * | * |  |  |  |  |  | * | 32-bit command (17 STEP) |  |  |  |
| D |  | * | * |  |  |  |  |  |  |  |  | DEZCP | Continuous | DEZCP | Pulse |
| Notes on operand usage: ${ }^{\text {a }}$ |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  | ope | The operand D occupies three consecutive points |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation - $\mathbf{S}_{1}$ : Lower limit of binary floating point number in range comparison. $\mathbf{S}_{\mathbf{2}}$ : Upper limit of binary floating point number in range comparison. $\mathbf{S}$ : Comparison of binary floating point numerical values. D: Results of comparison, occupies 3 consecutive points.

- Comparison of binary floating point numerical value $\mathbf{S}$ with binary floating point number lower limit value $\mathbf{S}_{\mathbf{1}}$ and binary floating point number upper limit value $\mathbf{S}_{\mathbf{2}}$; the results of comparison are expressed in $\mathbf{D}$.
- If the source operand $\mathbf{S}_{1}$ or $\mathbf{S}_{\mathbf{2}}$ designates a constant K or H , the command will transform the constant to a binary floating-point number for the purpose of comparison.
- When the lower limit binary floating point number $\mathbf{S}_{1}$ is greater than the upper limit binary floating point number $\mathbf{S}_{\mathbf{2}}$, a command will be issued to perform comparison with the upper and lower limits using the binary floating point number lower limit value $\mathbf{S}_{1}$.
- When the designated device is M0, it will automatically occupy M0-M2.
- When X0=On, the DEZCP command will be executed, and one of M0-M2 will be On. When X0=Off, the EZCP command will not execute, and M0-M2 will continue in the $\mathrm{X} 0=\mathrm{Off}$ state.
- Please use the RST or ZRST command to clear the result.


| API | $\square$ | RAD | $\mathbf{P}$ | S © © | Angle $\rightarrow$ Diameter |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 116 | $\mathbf{D}$ | D |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit co | mmand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DRAD | Continuous execution type | DRADP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

[^1]$\mathbf{S}$ : data source (angle). D: result of transformation (diameter).

- Uses the following formula to convert angles to radians.
- $\quad$ Diameter $=$ Angle $\times(\pi / 180)$

Example

- When $\mathrm{X} 0=\mathrm{On}$, the angle of the designated binary floating point number (D1, D0) will be converted to radians and stored in (D11, D10), with the content consisting of a binary floating point number.


| API | DEG | $\mathbf{P}$ | © |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 117 | $\mathbf{D}$ | D | Diameter $\rightarrow$ angle |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - |  | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DDEG | Continuous execution type | DDEGP | Pulse execution type |

Explanation S: data source (diameter). D: results of transformation (angle).

- Uses the following formula to convert radians to an angle.
- Angle $=$ Diameter $\times(180 / \pi)$


## Example

- When $\mathrm{X} 0=\mathrm{On}$, angle of the designated binary floating point number (D1, D0) in radians will be converted to an angle and stored in (D11, D10), with the content consisting of a binary floating point number.

S

(D) $\square$
Angle in degrees $=$ radians $\mathrm{X}(180 / \pi)$ Binary floating point

| API | D | EADD | P | $S_{1} S_{2}$ D | Adding binary floating point numbers |
| :---: | :---: | :---: | :---: | :---: | :---: |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S1 |  |  |  | * | * |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| S2 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | DEADD | Continuous | DEADDP | Pulse execution type |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  |  | execution type |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

## Explanation - $\mathbf{S}_{1}$ : addend. $\mathbf{S}_{2}$ : augend. D: sum.

- When the content of the register designated by $\mathbf{S}_{2}$ is added to the content of the register designated by $\mathbf{S}_{1}$, and the result is stored in the register designated by $\mathbf{D}$. Addition is performed entirely using binary floating-point numbers.
- If the source operand $\mathbf{S}_{\mathbf{1}}$ or $\mathbf{S}_{\mathbf{2}}$ designates a constant K or H , the command will transform that constant into a binary floating point number for use in addition.
- In the situation when $\mathbf{S}_{1}$ and $\mathbf{S}_{\mathbf{2}}$ designate identical register numbers, if a "continuous execution" command is employed, when conditional contact is On, the register will perform addition once during each scan. Pulse execution type commands (DEADDP) are generally used under ordinary circumstances.


## Example

- When $\mathrm{X} 0=\mathrm{On}$, a binary floating point number (D1, D0) will be added to a binary floating point number (D3, D2), and the results stored in (D11, D10).

- When X2 =On, a binary floating point number (D11, D10) will be added to K1234 (which has been automatically converted to a binary floating-point number), and the results stored in (D21, D20).


| API | D | ESUB | P | (S1) (S) | Subtraction of binary floating point numbers |
| :---: | :---: | :---: | :---: | :---: | :---: |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S1 |  |  |  | * | * |  |  |  |  |  | * | 32-bit command (13 STEP) |  |  |  |
| S2 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | DESUB | Continuous execution type | DESUBP | Pulse execution type |
|  | O | per | d |  |  |  |  |  |  |  |  |  |  |  |  |

Explanation

Example
Example

- $\quad \mathbf{S}_{1}$ : minuend. $\mathbf{S}_{2}$ : subtrahend. D: difference.
- When the content of the register designated by $\mathbf{S}_{\mathbf{2}}$ is subtracted from the content of the register designated by $\mathbf{S}_{1}$, the difference will be stored in the register designated by $\mathbf{D}$; subtraction is performed entirely using binary floating-point numbers.
- If the source operand $\mathbf{S}_{1}$ or $\mathbf{S}_{\mathbf{2}}$ designates a constant K or H , the command will transform that constant into a binary floating point number for use in subtraction.
- In the situation when $\mathbf{S}_{\mathbf{1}}$ and $\mathbf{S}_{\mathbf{2}}$ designate identical register numbers, if a "continuous execution" command is employed, when conditional contact is On, the register will perform addition once during each scan. Pulse execution type commands (DESUBP) are generally used under ordinary circumstances.
- When $\mathrm{X} 0=\mathrm{On}$, a binary floating point number (D1, D0) will be subtracted to a binary floating point number (D3, D2), and the results stored in (D11, D10).

| X0 | DESUB | D0 | D2 | D10 |
| :---: | :---: | :---: | :---: | :---: |

- When $\mathrm{X} 2=O n$, the binary floating point number (D1, D0) will be subtracted from K1234 (which has been automatically converted to a binary floating-point number), and the results stored in (D11, D10).

| X 2 | DESUB | K1234 | D0 | D10 |
| :---: | :---: | :---: | :---: | :---: |


| API |  | EMUL |  |  | $\boldsymbol{S}_{1}$ | $\boldsymbol{S}_{2}$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 122 | D | D | Multiplication of binary floating point numbers |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit com | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S1 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| S2 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | DEMUL | Continuous | DEMULP | Pulse |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  |  | execution type |  | execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag sign | l: none |  |  |

## Explanation - $\mathbf{S}_{1}$ : multiplicand. $\mathbf{S}_{2}$ : multiplier. $\mathbf{D}$ : product.

- When the content of the register designated by $\mathbf{S}_{\mathbf{1}}$ is multiplied by the content of the register designated by $\mathbf{S}_{2}$, the product will be stored in the register designated by $\mathbf{D}$; multiplication is performed entirely using binary floating-point numbers.
- If the source operand $\mathbf{S}_{\mathbf{1}}$ or $\mathbf{S}_{\mathbf{2}}$ designates a constant K or H , the command will transform that constant into a binary floating point number for use in multiplication.
- In the situation when $\mathbf{S}_{1}$ and $\mathbf{S}_{\mathbf{2}}$ designate identical register numbers, if a "continuous execution" command is employed, when conditional contact is On, the register will perform multiplication once during each scan. Pulse execution type commands (DEMULP) are generally used under ordinary circumstances.

Example

- When $\mathrm{X} 1=$ On, the binary floating point number (D1, D0) will be multiplied by the binary floating point number (D11, D10), and the product will be stored in the register designated by (D21, D20).

- When $\mathrm{X} 2=O n$, the binary floating point number (D1, D0) will be multiplied from K1234 (which has been automatically converted to a binary floating-point number), and the results stored in (D11, D10).


| API | D | EDIV | P | (S1) (S) | Division of binary floating point numbers |
| :---: | :---: | :---: | :---: | :---: | :---: |


|  |  | dev |  |  |  |  | ord | devic |  |  |  | 16-bit co | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S1 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| S2 |  |  |  | * | * |  |  |  |  |  | * | 32-bit command (13 STEP) |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | DEDIV | Continuous | DEDIVP | Pulse |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  |  | execution type |  | execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag sign | l: none |  |  |

- $\quad \mathbf{S}_{1}$ : dividend. $\mathbf{S}_{2}$ : divisor. D: quotient and remainder.
- When the content of the register designated by $\mathbf{S}_{1}$ is divided by the content of the register designated by $\mathbf{S}_{\mathbf{2}}$, the quotient will be stored in the register designated by D; division is performed entirely using binary floating-point numbers.
- If the source operand $\mathbf{S}_{\mathbf{1}}$ or $\mathbf{S}_{\mathbf{2}}$ designates a constant K or H , the command will transform that constant into a binary floating point number for use in division.

Example

- When $\mathrm{X} 1=$ On, the binary floating point number (D1, D0) will be divided by the binary floating point number (D11, D10), and the quotient stored in the register designated by (D21, D20).

| X1 | DEDIV | D0 | D10 | D20 |
| :--- | :--- | :--- | :--- | :--- |

- When X2=On, the binary floating point number (D1, D0) will be divided by $\mathrm{K} 1,234$ (which has been automatically converted to a binary floating-point number), and the results stored in (D11, D10).

| X 2 | DEDIV | D0 | K1234 | D10 |
| :--- | :--- | :--- | :--- | :--- |


| API | E | EXP | P | S (D) | Binary floating point number obtain exponent |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 124 | D |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | -- | - | - | - |
| S |  |  |  | * | + |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DEXP | Continuous execution type | DEXPP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation

- Taking e $=2.71828$ as a base, S is the exponent in the EXP operation.
- [ D +1, D ]=EXP [ $S+1, S$ ]
- Valid regardless of whether the content of $\mathbf{S}$ has a positive or negative value. The designated register D must have a 32-bit data format. This operation is performed using floating-point numbers, and $\mathbf{S}$ must therefore be converted to a floating point number.
- Content of operand $\mathbf{D}=e^{s} ; e=2.71828, \mathbf{S}$ is the designated source data


## Example

- When M0 is On, the value of (D1, D0) will be converted to a binary floating point number, which will be stored in register (D11, D10).
- When M1 is On, the EXP operation is performed on the exponent of (D11, D10); its value is a binary floating point number stored in register (D21, D20).


| API |  | LN |  | CS | D | Binary floating point number obtain logarithm |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 125 | D | P |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit c | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DLN | Continuous execution type | DLNP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation

- S: operation source device. D: operation results device.
- Taking e $=2.71828$ as a base, $\mathbf{S}$ is the exponent in the EXP operation.
- [ $\mathbf{D}+1, \mathbf{D}]=E X P[\mathbf{S}+\mathbf{1}, \mathbf{S}]$
- Valid regardless of whether the content of $\mathbf{S}$ has a positive or negative value. The designated register $D$ must have a 32-bit data format. This operation is performed using floating-point numbers, and $\mathbf{S}$ must therefore be converted to a floating point number.
- Content of operand $\mathbf{D}=e^{s} ; e=2.71828$, $\mathbf{S}$ is the designated source data


## Example

- When M0 is On, the value of (D1, D0) will be converted to a binary floating point number, which will be stored in register (D11, D10).
- When M1 is On, the EXP operation is performed on the exponent of (D11, D10); its value is a binary floating point number stored in register (D21, D20).


| API |  | ESQR |  | C | (D) |
| :--- | :--- | :--- | :--- | :--- | :--- |$\quad$ Binary floating point number find square root


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit co | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DESQR | Continuous execution type | $\begin{gathered} \text { DESQR } \\ P \end{gathered}$ | $\begin{gathered} \text { Pulse } \\ \text { execution type } \end{gathered}$ |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag sign | : none |  |  |

Explanation - S: source device for which square root is desired $\mathbf{D}$ : result of finding square root.

## Example

- When $\mathrm{X} 0=O n$, the square root is taken of the binary floating point number (D1, D0), and the result is stored in the register designated by (D11, D10).

- When $\mathrm{X} 2=$ On, the square root is taken of $\mathrm{K} 1,234$ (which has been automatically converted to a binary floating-point number), and the results stored in (D11, D10).


| API |  | INT |  |  | S | Binary floating point number $\rightarrow$ BIN whole number |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 129 | $\mathbf{D}$ | IN | $\mathbf{P}$ | transformation |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16 -bit command (5 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | INT | Continuous | INTP | Pulse |
| S |  |  |  |  |  |  |  |  |  |  | * |  | execution type |  | execution type |
| D |  |  |  |  |  |  |  |  |  |  | * |  |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | 32-bit command (9 STEP) |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DINT | Continuous execution type | DINTP | Pulse execution type |

Flag signal: none

Explanation

- S: the source device to be transformed. D: results of transformation.
- The content of the register designated by $\mathbf{S}$ is transformed from a binary floating point number format into a BIN whole number, and is temporarily stored in D. The BIN whole number floating point number will be discarded.
- The action of this command is the opposite of that of command API 49 (FLT).

Example - When $\mathrm{XO}=$ On, the binary floating point number (D1, D0) is transformed into a BIN whole number, and the result is stored in (D10); the BIN whole number floating point number will be discarded.


| $\begin{aligned} & \text { API } \\ & 130 \end{aligned}$ |  | SIN |  | P | (S) D |  |  |  |  | Binary floating point number SIN operation |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DSIN | Continuous execution type | DSINP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation

- $\quad \mathbf{S}$ : the designated source value. $\mathbf{D}$ : the SIN value result.
- $\quad \mathbf{S}$ is the designated source in radians.
- The value in radians (RAD) is equal to (angle $\times \pi / 180$ ).
- The SIN obtained from the source value designated by $\mathbf{S}$ is stored in $\mathbf{D}$.

The following figure displays the relationship between the arc and SIN results:


## Example

When $\mathrm{XO}=\mathrm{On}$, the SIN value of the designated binary floating point number (D1, D0) in radians (RAD) will be stored in (D11, D10), with the content consisting of a binary floating point number.


RAD value ( angle $x \pi /$ 180)
Binary floating point

SIN value
Binary floating point


- S: the designated source value. D: the COS value result.
- The source designated by $S$ can be given as radians or an angle; this is decided by flag M1018.
- When M1018=Off, the operation is in radians mode, where the radians (RAD) value is equal to (angle $\times \pi / 180$ ).
- When M1018=On, the operation is in the angle mode, where the angular range is $0^{\circ} \leq$ angle $<360^{\circ}$.
- When calculation results yield $0, \mathrm{M} 1020=$ On.
- The COS obtained from the source value designated by $\mathbf{S}$ is stored in $\mathbf{D}$.

The following figure displays the relationship between the arc and SIN results:


Example - When $\mathrm{X} 0=$ On, the $\operatorname{COS}$ value of the designated binary floating point number (D1, D0) in radians will be stored in (D11, D10), with the content consisting of a binary floating point number.


| API | $\square$ | TAN |  | P | (S) D | Binary floating point number TAN operation |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 132 | D |  | P |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DTAN | Continuous execution type | DTANP | Pulse execution type |

Explanation

- S: the designated source value. D: the TAN value result.
- The source designated by $\mathbf{S}$ can be given as radians or an angle; this is decided by flag M1018.
- When M1018=Off, the operation is in radians mode, where the radians (RAD) value is equal to (angle $\times \pi / 180$ ).
- When M1018=On, the operation is in the angle mode, where the angular range is $0^{\circ} \leq$ angle $<360^{\circ}$.
- When calculation results yield $0, \mathrm{M} 1020=O n$.
- The TAN obtained from the source value designated by $\mathbf{S}$ is stored in $\mathbf{D}$.

The following figure displays the relationship between the arc and TAN results:


S: Radian
R: Result (TAN value)

Example - When $\mathrm{X} 0=$ On, the TAN value of the designated binary floating point number (D1, D0) in radians (RAD) will be stored in (D11, D10), with the content consisting of a binary floating point number.


| API |  | ASIN |  | © © | Binary floating point number ASIN operation |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 133 | D | P | P |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DASIN | Continuous | DASINP | Pulse |
|  |  |  |  |  |  |  |  |  |  |  |  |  | execution type |  | execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

[^2]- $\quad$ ASIN value $=\sin ^{-1}$

The figure below shows the relationship between input data and result:


Example - When $X 0=$ On, the $A S I N$ value obtained from the designated binary floating point number (D1, D0) will be stored in (D11, D10), with the content consisting of a binary floating point number.


| API |  | ACOS | P | S. (D) | Binary floating point number ACOS operation |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 134 | D |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DACOS | Continuous execution type | $\begin{gathered} \text { DACOS } \\ \mathrm{P} \end{gathered}$ | Pulse execution type |

Explanation - S: the designated source (binary floating point number). D: the ACOS value result.

- $\quad$ ACOS value $=\cos ^{-1}$

The figure below shows the relationship between input data and result:


Example - When $\mathrm{X} 0=$ On, the ACOS value obtained from the designated binary floating point number (D1, D0) will be stored in (D11, D10), with the content consisting of a binary floating point number.


| API | $\square$ | ATAN | P | S © D | Binary floating point number ATAN operation |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 135 | D |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - |  |  |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DATAN | Continuous execution type | DATANP | Pulse execution type |

Explanation

- S: the designated source (binary floating point number).
D: the ATAN value result.
- ATAN value $=\tan ^{-1}$

The figure below shows the relationship between input data and result:


Example - When $X 0=$ On, the TAN value obtained from the designated binary floating point number (D1, D0) will be stored in (D11, D10), with the content consisting of a binary floating point number.


| API |  | SINH |  | S | D |
| :--- | :--- | :--- | :--- | :--- | :--- |$\quad$ Binary floating point number SINH operation


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DSINH | Continuous execution type | DSINHP | Pulse execution type |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation

- $\quad \mathbf{S}$ : the designated source (binary floating point number).

D: the SINH value result.

- $\quad$ SINH value $=\left(e^{s}-e^{-s}\right) / 2$


## Example

- When $\mathrm{XO}=\mathrm{On}$, the SINH value obtained from the designated binary floating point number (D1, D0) will be stored in (D11, D10), with the content consisting of a binary floating point number.


| (S) | D 1 | D 0 |
| :--- | :--- | :--- |
| Binary floating point |  |  |
| (D) | D 11 | D 10 |
| BinH value |  |  |
| Binary floating point |  |  |


| API | COSH | P | S $\mathbb{D}$ | Binary floating point number COSH operation |
| :--- | :--- | :--- | :--- | :--- |
| 137 | D |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DCOSH | Continuous execution type | $\begin{gathered} \mathrm{DCOSH} \\ \mathrm{P} \end{gathered}$ | Pulse execution type |

[^3]- $\operatorname{COSH}$ value $=\left(e^{s}+e^{-s}\right) / 2$

Example - When $\mathrm{XO}=$ On, the COSH value obtained from the designated binary floating point number (D1, D0) will be stored in (D11, D10), with the content consisting of a binary floating point number.


| API |  | TANH | P | S $\mathbb{D}$ | Binary floating point number TANH operation |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 138 | D |  |  |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit co | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  |  |  | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | DTANH | Continuous execution type | $\begin{gathered} \text { DTANH } \\ \text { P } \end{gathered}$ | $\begin{gathered} \text { Pulse } \\ \text { execution type } \end{gathered}$ |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation

- S: the designated source (binary floating point number).
D: the TANH value result.
- TANH value $=\left(e^{s}-e^{-s}\right) /\left(e^{s}+e^{-s}\right)$

Example - When $\mathrm{XO}=$ On, the TANH value obtained from the designated binary floating point number (D1, D0) will be stored in (D11, D10), with the content consisting of a binary floating point number.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D |
| S |  |  |  |  |  | * | * | * | * | * | * |


| 16-bit command (3 STEP) |  | SWAPP | Pulse execution type |
| :---: | :---: | :---: | :---: |
| SWAP | Continuous execution type |  |  |
| 32-bit command (5STEP) |  |  |  |
| DSWAP | Continuous execution type | DSWAPP | Pulse execution type |

Flag signal: none
Explanation - S: The device that going to exchange its up/down 8 bits.

- When using 16-bit command, the upper 8-bit and lower 8-bit exchange.
- When using 32-bit command, the contents of upper 8-bit and lower 8 -bit of the 2 registers exchange.
- This command usually uses pulse execution type (SWAPP, DSWAPP)

| $\begin{array}{\|l\|} \hline \text { API } \\ \hline 150 \end{array}$ |  | MODRW |  |  | (S1) S $\mathbf{S}_{2}$ S ${ }^{\text {S }}$ |  |  |  |  |  | MODBUS data read/write |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16 -bit command (5STEP) |  |  |  |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | MODRW | Continuous | MODRW | Pulse |
| S1 |  |  |  | * | * |  |  |  |  |  | * |  | execution type |  | execution type |
| S2 |  |  |  | * | * |  |  |  |  |  | * |  |  |  |  |
| S3 |  |  |  | * | * |  |  |  |  |  | * | 32-bit com | mand |  |  |
| S |  |  |  |  |  |  |  |  |  |  | * | -- | ------- | -- | - |
| n |  |  |  | * | * |  |  |  |  |  | * | Flag signal | al: M1077 M1078 M | 1079 |  | data to read/write. S: register for data to be read/written is stored. N : length of data to be read/written.

- COM1 must be defined as controlled by the PLC (set Pr.09-31 =-12) before using this command, and the corresponding communications speed and format must also be set (set Pr.09-01 and Pr.09-04). S2: communications function code. Currently only supports the following function code; the remaining function code cannot be executed.

| Function | Description |
| :---: | :--- |
| H 02 | Input read |
| H 03 | Read word |
| H 06 | Write single word |
| H 0F | Write multiple coils |
| H 10 | Write single word |

- After executing this command, M1077, M1078 and M1079 will be immediately changed to 0 .
- As an example, when LTC must control another converter and PLC, if the converter has a station number of 10 and the PLC has a station number of 20, see the following example:
Control slave device converter

| Serial No. | Example | MODRW command |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | S1 | S2 | S3 | S4 | n |
|  |  | Node ID | Function code | Address | Register | Length: |
| 1 | Reads 4 sets of data comprising the converter slave device parameters Pr.01-00 to Pr.0103, and saves the read data in D0 to D3 | K10 | H3 | H100 | D0 | K4 |
| 2 | Reads 3 sets of data comprising the converter slave device addresses H2100 to H2102, and saves the read data in D5 to D7 | K10 | H3 | H2100 | D5 | K3 |
| 3 | Writes 3 sets of data comprising the converter slave device parameters Pr.05-00 to Pr.0503, and writes the values as D10 to D12 | K10 | H10 | H500 | D10 | K3 |
| 4 | Writes 2 sets of data comprising the converter slave device addresses H2OOO to H2001, and writes the values as D15 to D16 | K10 | H10 | H2000 | D15 | K2 |

PLC controlling slave device

| Serial No. | Example | MODRW command |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | S1 | S2 | S3 | S4 | n |
|  |  | Node ID | Function code | Address | Register | Length: |
| 1 | Reads 4 sets of data comprising the PLC slave device's X0 to X3 state, and saves the read data in bits 0 to 3 of D0 | K20 | H2 | H400 | D0 | K4 |
| 2 | Reads 4 sets of data comprising the PLC slave device's Y0 to Y3 state, and saves the read data in bits 0 to 3 of D1 | K20 | H2 | H500 | D1 | K4 |
| 3 | Reads 4 sets of data comprising the PLC slave device's M0 to M3 state, and saves the read data in bits 0 to 3 of D2 | K20 | H2 | H800 | D2 | K4 |
| 4 | Reads 4 sets of data comprising the PLC slave device's T0 to T3 state, and saves the read data in bits 0 to 3 of D3 | K20 | H2 | H600 | D3 | K4 |
| 5 | Reads 4 sets of data comprising the PLC slave device's C0 to C3 state, and saves the read data in bits 0 to 3 of D4 | K20 | H2 | HEOO | D4 | K4 |
| 6 | Reads 4 sets of data comprising the PLC slave device's T0 to T3 count value, and saves the read data of D10 to D13 | K20 | H3 | H600 | D10 | K4 |
| 7 | Reads 4 sets of data comprising the PLC slave device's C0 to C3 count value, and saves the read data of D20 to D23 | K20 | H3 | HEOO | D20 | K4 |
| 8 | Reads 4 sets of data comprising the PLC slave device's D0 to D3 count value, and saves the read data of D30 to D33 | K20 | H3 | H1000 | D30 | K4 |
| 9 | Writes 4 sets of the PLC slave device's YO to Y 3 state, and writes the values as bits 0 to 3 of D1 | K20 | HF | H500 | D1 | K4 |
| 10 | Writes 4 sets of the PLC slave device's M0 to M3 state, and writes the values as bits 0 to 3 of D2 | K20 | HF | H800 | D2 | K4 |
| 11 | Writes 4 sets of the PLC slave device's T0 to T3 state, and writes the values as bits 0 to 3 of D3 | K20 | HF | H600 | D3 | K4 |
| 12 | Writes 4 sets of the PLC slave device's C0 to C3 state, and writes the values as bits 0 to 3 of D4 | K20 | HF | HE00 | D4 | K4 |
| 13 | Writes 4 sets of the PLC slave device's T0 to T3 state, and writes the values of D10 to D13 | K20 | H10 | H600 | D10 | K4 |
| 14 | Writes 4 sets of the PLC slave device's C0 to C3 state, and writes the values of D20 to D23 | K20 | H10 | HEOO | D20 | K4 |
| 15 | Writes 4 sets of the PLC slave device's D0 to D3 state, and writes the values of D30 to D33 | K20 | H10 | H1000 | D30 | K4 |

## Example

- Will trigger MO On when the PLC begins to operate, and sends instruction to execute one MODRW command.
- After receiving the slave devices response, if the command is correct, it will execute one ROL command, which will cause M1 to be On.
- After receiving the slave devices response, will trigger M50 $=1$ after a delay of 10 PLC scanning cycles, and then execute one MODRW command.
- After again receiving the slave devices response, if the command is correct, it will execute one ROL command, and M2 will change to On at this time (and M2 can be defined as a repeat of M); K4M0 will change to K1, and only M0 will remain 1 . Transmission can proceed in a continuous cycle. If you wish to add a command, merely add the desired command in the empty frame, and change repeat $M$ to $\mathrm{Mn}+1$.


| API | $\square$ | TCMP | $\mathbf{P}$ | $\mathbf{S}_{1}$ | $\boldsymbol{S}_{2}$ | $\mathbf{S}_{3}$ | S (D) Comparison of calendar data |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |



Notes on operand usage:
Please refer to the function specifications table for each device in Flag signal: none
series for the scope of device usage
Explanation - $\mathbf{S}_{1}$ : Sets the hours of the comparison time, setting range is "K0-K23." $\mathbf{S}_{2}$ : Sets the minutes of the comparison time, setting range is "K0-K59." $\mathrm{S}_{3}$ : Sets the seconds of the comparison time, setting range is "K0-K59." S: current calendar time. D: Results of comparison.

- Compares the time in hours, minutes, and seconds set in $\mathbf{S}_{1}-\mathbf{S}_{3}$ with the current calendar time in hours, minutes, and seconds, with the results of comparison expressed in $\mathbf{D}$.
- $\mathbf{S}$ The hour content of the current calendar time is "K0-K23." $\mathbf{S}+1$ comprises the minutes of the current calendar time, and consists of "K0-K59." S +2 comprises the seconds of the current calendar time, and consists of "K0-K59."
- The current calendar time designated by $\mathbf{S}$ is usually compared using the TCMP command after using the TRD command to read the current calendar time. If the content value of $\mathbf{S}$ exceeds the range, this is considered an operating error, the command will not execute, and M1068=On.

Example - When X10=On, the command will execute, and the current calendar time in D20D22 will be compared with the preset value of 12:20:45; the results will be displayed in M10-M12. When X10 On $\rightarrow$ Off, the command will not be executed, but the On/Off status prior to M10-M12 will be maintained.

- If results in the form of $\geq, \leq$, or $\neq$ are needed, they can be obtained by series and parallel connection of M10-M12.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (9 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | TZCP | Continuous | TZCPP | Pulse |
| S1 |  |  |  |  |  |  |  |  | * | * | * |  | execution type |  | execution type |
| S2 |  |  |  |  |  |  |  |  | * | * | * |  |  |  |  |
| S |  |  |  |  |  |  |  |  | * | * | * | 32-bit co | mand |  |  |
| D |  | * | * |  |  |  |  |  |  |  |  | - | - | - | - |

Notes on operand usage:
Please refer to the function specifications table for each device in Flag signal: none series for the scope of device usage

- $\mathbf{S}_{1}$ : Sets the lower limit of the comparison time. $\mathbf{S}_{2}$ : Sets the upper limit of the comparison time. S: current calendar time. D: Results of comparison.
- Performs range comparison by comparing the hours, minutes, and seconds of the current calendar time designated by $\mathbf{S}$ with the lower limit of the comparison time set as $\mathbf{S}_{1}$ and the upper limit of the comparison time set as $\mathbf{S}_{\mathbf{2}}$, and expresses the results of comparison in $\mathbf{D}$.
- $\mathbf{S}_{1}, \mathbf{S}_{1}+1, \mathbf{S}_{1}+2$ : Sets the hours, minutes, and seconds of the lower limit of the comparison time.
- $\mathbf{S}_{\mathbf{2}}, \mathbf{S}_{\mathbf{2}}+1, \mathbf{S}_{\mathbf{2}}+2$ : Sets the hours, minutes, and seconds of the upper limit of the comparison time.
- $\mathbf{S}, \mathbf{S}+1, \mathrm{c} 2 \mathbf{S}+2$ : The hours, minutes, and seconds of the current calendar time
- The D0 designated by the $\mathbf{S}$ listed in this program is usually obtained by comparison using the TZCP command after using the TRD command in advance to read the current calendar time. If the value of $\mathbf{S}_{1}, \mathbf{S}_{2}$, or $\mathbf{S}$ exceeds the range, this is considered an operating error, the command will not execute, and M1068=On.
- When the current time $\mathbf{S}$ is less than the lower limit value $\mathbf{S}_{1}$ and $\mathbf{S}$ is less than the upper limit value $\mathbf{S}_{\mathbf{2}}, \mathbf{D}$ will be On. When the current time $\mathbf{S}$ is greater than the lower limit value $\mathbf{S}_{1}$ and $\mathbf{S}$ is greater than the upper limit value $\mathbf{S}_{\mathbf{2}}, \mathbf{D}+2$ will be On; $\mathbf{D}+1$ will be On under other conditions.

Example - When X10=On, the TZCP command executes, and one of M10-M12 will be On. When X10=Off, the TZCP command will not execute, and M10-M12 will remain in the $\mathrm{X} 10=$ Off state.


| API | $\square$ TADD | $\mathbf{P}$ | $\boldsymbol{S}_{1}$ | $\boldsymbol{S}_{2}$ |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 162 | D | Calendar data addition |  |  |



Explanation

- $\mathbf{S}_{1}$ : time addend. $\mathbf{S}_{2}$ : time augend. $\mathbf{D}$ : time sum.
- The calendar data in hours, minutes, and seconds designated by $\mathbf{S}_{\mathbf{2}}$ is added to the calendar data in hours, minutes, and seconds designated by $\mathbf{S}_{1}$, and the result is stored as hours, minutes, and seconds in the register designated by $\mathbf{D}$.
- If the value of $\mathbf{S}_{\mathbf{1}}$ or $\mathbf{S}_{\mathbf{2}}$ exceeds the range, this is considered an operating error, the command will not execute, M1067, M1068=On, and D1067 will record the error code 0E1A(HEX).
- If the results of addition are greater than or equal to 24 hours, carry flag M1022=On, and $\mathbf{D}$ will display the results of addition minus 24 hours.
- If the results of addition are equal to 0 ( 0 hours, 0 minutes, 0 seconds), zero flag M1020=On.

Example - When $\mathrm{X} 10=$ On, the TADD command will be executed, and the calendar data in hours, minutes, and seconds designated by D0 to D2 will be added to the calendar data in hours, minutes, and seconds designated by D10 to D12, and the results are stored as a total number of hours, minutes, and seconds in the registers designated by D20 to D22.


| D0 | 8(hr) |
| :--- | :--- |
| D1 | 10(min) |
| D2 | $20(\mathrm{sec})$ |$+$| D10 | $6(\mathrm{hr})$ |
| :--- | :--- |
| D11 | $40(\mathrm{~min}$ |
| D12 | $6(\mathrm{sec}$ |$\rightarrow$| D20 | $14(\mathrm{hr})$ |
| :--- | :--- |
| D21 | $50(\mathrm{~min}$ |
| D22 | $26(\mathrm{sec}$ |

$8: 10: 20$
6:40:6
14:50:26

| API | - TSUB | P | $\mathbf{S S}_{1}$ (S2 | (D) | Calendar data subtraction |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 163 |  |  |  |  |  |


|  |  | devid |  |  |  |  | ord | devic |  |  |  | 16-bit c | mand (7 ST |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | TSUB | Continuous | TSUBP | Pulse |
| S1 |  |  |  |  |  |  |  |  | * | * | * |  | execution type |  | execution type |
| S2 |  |  |  |  |  |  |  |  | * | * | * |  |  |  |  |
| D |  |  |  |  |  |  |  |  | * | * | * | 32-bit command |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | - | - | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  | - Flag | signal: M1020 M1022 M1068 | ro flag <br> arry flag lendar err |  |

Explanation - $\mathbf{S}_{1}$ : time minuend. $\mathbf{S}_{2}$ : time augend. D: time sum.

- Subtracts the calendar data in hours, minutes, and seconds designated by $\mathbf{S}_{\mathbf{2}}$ from the calendar data in hours, minutes, and seconds designated by $\mathbf{S}_{1}$, and the result is temporarily stored as hours, minutes, and seconds in the register designated by D.
- If the value of $\mathbf{S}_{1}$ or $\mathbf{S}_{\mathbf{2}}$ exceeds the range, this is considered an operating error, the command will not execute, M1067, M1068=On, and D1067 will record the error code 0E1A(HEX).
- If subtraction results in a negative number, borrow flag M1021=On, and the result of that negative number plus 24 hours will be displayed in the register designated by D.
- If the results of subtraction are equal to 0 ( 0 hours, 0 minutes, 0 seconds), zero flag M1020=On.
- When $\mathrm{X} 10=O n$, the TADD command will be executed, and the calendar data in hours, minutes, and seconds designated by D10 to D12 will be subtracted from the calendar data in hours, minutes, and seconds designated by D0 to D2, and the results are stored as a total number of hours, minutes, and seconds in the registers designated by D20 to D22.


| D0 20(hr) |  | D10 14(hr) | $\rightarrow$ | D20 | 5(hr) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| D1 20(min) |  | D11 30(min) |  | D21 | 49(min) |
| D2 $5(\mathrm{sec})$ |  | D12 8(sec) |  | D22 | 57(sec) |
| 20:20:5 |  | 14:30:8 |  | $5: 49$ | 9:57 |


| API |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| 166 | $\square$ | TRD | $\mathbf{P}$ | (D) |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (3STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | TRD | Continuous | TRDP | Pulse |
| D |  |  |  |  |  |  |  |  | * | * | * |  | execution type |  | execution type |

Notes on operand usage:
Please refer to the function specifications table for each device in series for the scope of device usage

| 32-bit command |  |  |  |
| :---: | :---: | :---: | :---: |
| - | - | - | - |
| Flag signal: none |  |  |  |

Explanation

- $\mathbf{S}_{1}$ : time minuend. $\mathbf{S}_{2}$ : time augend. D: time sum.
- D: device used to store the current calendar time after reading.
- The EH/EH2/SV/EH3/SV2/SA/SX/SC main units have a built-in calendar clock, and the clock provides seven sets of data comprising year, week, month, day, hour, minute, and second stored in D1063 to D1069. The TRD command function allows program designers to directly read the current calendar time into the designated seven registers.
- D1063 only reads the two right digits of the Western calendar year.

Example

- When $\mathrm{XO}=\mathrm{On}$, the current calendar time is read into the designated registers D0 to D6.
- In D1064, 1 indicates Monday, 2 indicates Tuesday, and so on, with and 7 indicating Sunday.


| Special <br> D | Item | Content |  | General <br> D | Item |
| :---: | :---: | :---: | :---: | :---: | :---: |
| D1063 | Year <br> (Western) | $00-99$ | $\rightarrow$ | D0 | Year <br> (Western) |
| D1064 | Weeks | $1-7$ | $\rightarrow$ | D1 | Weeks |
| D1065 | Month | $1-12$ | $\rightarrow$ | D2 | Month |
| D1066 | Day | $1-31$ | $\rightarrow$ | D3 | Day |
| D1067 | Hour | $0-23$ | $\rightarrow$ | D4 | Hour |
| D1068 | Minute | $0-59$ | $\rightarrow$ | D5 | Minute |
| D1069 | Second | $0-59$ | $\longrightarrow$ | D6 | Second |


| API | $\square$ | GRY | $\mathbf{P}$ | (S) (D) | BIN $\rightarrow$ GRAY code transformation |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 170 | D |  | P |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (5 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | GRY | Continuous | GRYP | Pulse |
| S |  |  |  | * | * |  | * |  | * | * | * |  | execution type |  | execution type |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | 32-bit command (9 STEP) |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DGRY | Continuous execution type | DGRYP | $\begin{gathered} \text { Pulse } \\ \text { execution type } \end{gathered}$ |
|  |  |  |  |  |  |  |  |  |  |  |  | - Flag signal: none |  |  |  |

Explanation - S: source device. D: device storing GRAY code.

- Transforms the content value (BIN value) of the device designated by $\mathbf{S}$ to GRAY code, which is stored in the device designated by $\mathbf{D}$.
- The valid range of $\mathbf{S}$ is as shown below; if this range is exceeded, it will be considered an error, and the command will not execute.

16-bit command: 0-32,767

- 32-bit command: 0-2,147,483,647

Example

- When $\mathrm{X} 0=O n$, the constant K 6513 will be transformed to GRAY code and stored in D0.


| API | GBIN | P | (S) (D) | GRAY code $\rightarrow$ BIN transformation |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 171 | D | (D) |  |  |


|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command (5 STEP) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | c | D |  | Continuous | GBINP | Pulse |
| S |  |  |  | * |  |  |  |  |  |  |  |  |  |  | execution type |
| D |  |  |  |  |  |  | * | * | * | * | * | 32 -bit command (9 STEP) |  |  |  |
| Notes on operand usage: <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DGBIN | Continuous execution type | DGBINP | $\begin{gathered} \text { Pulse } \\ \text { execution type } \end{gathered}$ |
|  |  |  |  |  |  |  |  |  |  |  |  | - Flag signal: none |  |  |  |

Explanation

- S: source device used to store GRAY code. D: device used to store BIN value after transformation.
- The GRAY code corresponding to the value of the device designated by $\mathbf{S}$ is transformed into a BIN value, which is stored in the device designated by $\mathbf{D}$.
- This command will transform the value of the absolute position encoder connected with the PLCs input and (this encoder usually has an output value in the form of GRAY code) into a BIN value, which is stored in the designated register.
- The valid range of $\mathbf{S}$ is as shown below; if this range is exceeded, it will be considered an error, and the command will not execute.

16-bit command: 0-32,767

- 32-bit command: 0-2,147,483,647


## Example

- When $\mathrm{X} 20=\mathrm{On}$, the GRAY code of the absolute position encoder connected with input points X0 to X17 will be transformed into BIN value and stored in D10.



|  |  | e |  |  |  |  | Ord | devic |  |  |  | 16-bit c | mand (5 STE |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | LD\# | Continuous | - | - |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  |  |
| S2 |  |  |  | * | * | * | * | * | * | * | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: \#: \&, \|, ^ <br> Please refer to the function specifications table for each device in series for the range of device usage |  |  |  |  |  |  |  |  |  |  |  | 32-bit com | Continuous execution type | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |



- $\quad \mathbf{S}_{1}$ : data source device $1 . \mathbf{S}_{2}$ : data source device 2 .
- This command performs comparison of the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$; when the result of comparison is not 0 , this command will be activated, but this command will not be activated when the result of comparison is 0 .
- The LD\#This command can be used while directly connected with the busbar

| API No. | 16 -bit <br> commands | 32-bit <br> commands | Conditions for <br> activation |  |  |  | Conditions for <br> inactivation |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 215 | LD\& | DLD\& | $\mathbf{S}_{1}$ | $\&$ | $\mathbf{S}_{\mathbf{2}}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\&$ | $\mathbf{S}_{\mathbf{2}}$ | $=0$ |
| 216 | LD\| | DLD | $\mathbf{S}_{1}$ | I | $\mathbf{S}_{2}$ | $\neq 0$ | $\mathbf{S}_{1}$ | । | $\mathbf{S}_{\mathbf{2}}$ | $=0$ |
| 217 | LD^ $^{\wedge}$ | DLD^ $^{\wedge}$ | $\mathbf{S}_{1}$ | $\wedge$ | $\mathbf{S}_{2}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\wedge$ | $\mathbf{S}_{2}$ | $=0$ |

- \&: logical AND operation.
- I: logical OR operation.
- $\quad$ : logical XOR operation.


## Example

- When the content of C0 and C10 is subjected to the logical AND operation, and the result is not equal to $0, Y 10=0 n$.
- When the content of D200 and D300 is subjected to the logical OR operation, and the result is not equal to 0 , and $\mathrm{X} 1=\mathrm{On}, \mathrm{Y} 11=\mathrm{On}$ and remains in that state.


220

|  | Bit device |  |  | Word device |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D |
| S1 |  |  |  | * | * | * | * | * | * | * | * |
| S2 |  |  |  | * | * | * | * | * | * | * | * |

Notes on operand usage: \#: \& , |, ^
Please refer to the function specifications table for each device in series for the scope of device usage

| (9 STEP) |  |  |  |
| :---: | :---: | :---: | :---: |
| DAND\# | Continuous execution type | - | - |

Flag signal: none

## Explanation

- $\quad \mathbf{S}_{1}$ : data source device 1. $\mathbf{S}_{2}$ : data source device 2.
- This command performs comparison of the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$; when the result of comparison is not 0 , this command will be activated, but this command will not be activated when the result of comparison is 0 .
- The AND\# command is an operation command in series with the contact.

| API No. | 16 -bit <br> commands | 32-bit <br> commands | Conditions for <br> activation |  |  |  | Conditions for <br> inactivation |  |  |  |
| :---: | :--- | :--- | :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 218 | AND\& | DAND\& | $\mathbf{S}_{1}$ | $\&$ | $\mathbf{S}_{\mathbf{2}}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\&$ | $\mathbf{S}_{\mathbf{2}}$ | $=0$ |
| 219 | AND | DAND | $\mathbf{S}_{1}$ | । | $\mathbf{S}_{2}$ | $\neq 0$ | $\mathbf{S}_{1}$ | । | $\mathbf{S}_{\mathbf{2}}$ | $=0$ |
| 220 | AND^ $^{\wedge}$ | DAND^ $^{\wedge}$ | $\mathbf{S}_{1}$ | $\wedge$ | $\mathbf{S}_{2}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\wedge$ | $\mathbf{S}_{2}$ | $=0$ |

- \&: logical AND operation.
- |: logical OR operation.
- $\quad$ : logical XOR operation.


## Example

- When $\mathrm{XO}=\mathrm{On}$ and the content of CO and C 10 is subjected to the logical AND operation, and the result is not equal to $0, Y 10=O n$.
- When X1=Off and D10 and D0 is subjected to the logical OR operation, and the result is not equal to $0, \mathrm{Y} 11=$ On and remains in that state.
- When X2 $=$ On and the content of the 32-bit register D200 (D201) and 32-bit register D100 (D101) is subjected to the logical XOR operation, and the result is not equal to 0 or $\mathrm{M} 3=\mathrm{On}, \mathrm{M} 50=0 \mathrm{n}$.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit co | mand (5 STEP) |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | OR\# | Continuous | - | - |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  |  |
| S2 |  |  |  | * | * | * | * | * | * | * | * |  |  |  |  |
| Notes on operand usage: \#:\&,\|,^ <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | 32-bit command (9 STEP) |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  | DOR\# | Continuous execution type | - | - |

- $\quad \mathbf{S}_{1}$ : data source device $1 . \mathbf{S}_{2}$ : data source device 2.
- This command performs comparison of the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$; when the result of comparison is not 0 , this command will be activated, but this command will not be activated when the result of comparison is 0 .
- The OR\# command is an operation command in series with the contact.

| API No. | 16-bit <br> commands | 32-bit <br> commands | Conditions for <br> activation |  |  |  | Conditions for <br> inactivation |  |  |  |
| :---: | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 221 | OR\& | DOR\& | $\mathbf{S}_{1}$ | $\&$ | $\mathbf{S}_{2}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\&$ | $\mathbf{S}_{2}$ | $=0$ |
| 222 | OR | DOR $\mid$ | $\mathbf{S}_{1}$ | $\mid$ | $\mathbf{S}_{\mathbf{2}}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\mid$ | $\mathbf{S}_{\mathbf{2}}$ | $=0$ |
| 223 | OR^ $^{\wedge}$ | DOR $^{\wedge}$ | $\mathbf{S}_{1}$ | $\wedge$ | $\mathbf{S}_{2}$ | $\neq 0$ | $\mathbf{S}_{1}$ | $\wedge$ | $\mathbf{S}_{2}$ | $=0$ |

- \&: logical AND operation.
- $\quad$ : logical OR operation.
- $\quad \wedge$ : logical XOR operation.

Example

- When $\mathrm{X} 1=\mathrm{On}$ or the content of C 0 and C 10 is subjected to the logical AND operation, and the result is not equal to $0, \mathrm{Y} 0=0 \mathrm{On}$.
- When X2 and M30 are both equal to On, or the content of 32-bit register D10 (D11) and 32-bit register D20 (D21) is subjected to the logical OR operation, and the result is not equal to 0 , or the content of the 32 -bit counter C 235 and the 32 -bit register D200 (D201) is subjected to the logical XOR operation, and the result is not equal to $0, \mathrm{M} 60=0 \mathrm{n}$.

D LD※
(S1) (S2) Contact form compare LD*

|  | Bit device |  |  | Word device |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D |
| S1 |  |  |  | * | * | * | * | * | * | * | * |
| S2 |  |  |  | * | * | * | * | * | * | * | * |

Notes on operand usage: $※:=,>,<,<>, \leqq, \geqq$
Please refer to the function specifications table for each device in

| (9 STEP) |  |  |  |
| :---: | :---: | :---: | :---: |
| DLD※ | Continuous execution type | - | - |

Flag signal: none

## Explanation

- $\quad \mathbf{S}_{1}$ : data source device 1. $\mathbf{S}_{2}$ : data source device 2.
- This command compares the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$. Taking API $224(\mathrm{LD}=)$ as an example, this command will be activated when the result of comparison is "equal," and will not be activated when the result is "unequal."
- The LD* can be used while directly connected with the busbar

| API No. | 16-bit commands | 32-bit commands | Conditions for <br> activation | Conditions for <br> inactivation |
| :---: | :--- | :--- | :---: | :---: |
| 224 | LD $=$ | DLD $=$ | $\mathbf{S}_{1}=\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ |
| 225 | $\mathrm{LD}>$ | $\mathrm{DLD}>$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ |
| 226 | $\mathrm{LD}<$ | $\mathrm{DLD}<$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ |
| 228 | $\mathrm{LD}<>$ | $\mathrm{DLD}<>$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ |
| 229 | $\mathrm{LD}<=$ | $\mathrm{DLD}<=$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ |
| 230 | $\mathrm{LD}>=$ | $\mathrm{DLD}>=$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ |

## Example

- When the content of C 10 is equal to $\mathrm{K} 200, \mathrm{Y} 10=\mathrm{On}$.
- When the content of D200 is greater than $\mathrm{K}-30$, and $\mathrm{X} 1=\mathrm{On}, \mathrm{Y} 11=\mathrm{On}$ and remains in that state.



|  |  | dev |  |  |  |  | ord | devic |  |  |  | 16-bit com | mand (5 STE |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | AND※ | Continuous | - | - |
| S1 |  |  |  | * | * | * | * | * | * | * | * |  | execution type |  |  |
| S2 |  |  |  | * | * | * | * | * | * | * | * |  |  |  |  |
| Notes on operand usage: $※:=,>,<,<>, \leqq, \geqq$ Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | 32-bit com | Continuous execution type | - | - |

$\mathbf{S}_{1}$ : data source device $1 . \mathbf{S}_{2}$ : data source device 2 .

- This command compares the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$. Taking API 232 (AND=) as an example, when the result of comparison is equal, this command will be activated; when the result of comparison is unequal, this command will not be activated.
- The AND* command is a comparison command in series with a contact.

| API No. | 16-bit commands | 32 -bit commands | Conditions for <br> activation | Conditions for <br> inactivation |
| :---: | :--- | :--- | :---: | :---: |
| 232 | AND $=$ | DAND $=$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ |
| 233 | AND $>$ | DAND $>$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ |
| 234 | AND $<$ | DAND $<$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ |
| 236 | AND $<>$ | DAND $<>$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ |
| 237 | AND $<=$ | DAND $<=$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ |
| 238 | AND $>=$ | DAND $>=$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ |

## Example

- When $X 0=O n$ and the current value of $C 10$ is also equal to $K 200, Y 10=O n$.
- When $\mathrm{X} 1=\mathrm{Off}$ and the content of register D 0 is not equal to $\mathrm{K}-10, \mathrm{Y} 11=\mathrm{On}$ and remains in that state.
- When $\mathrm{X} 2=$ On and the content of the 32-bit register $D 0$ (D11) is less than 678,493 , or M3=On, M50=On.


API 240-
246

(S1) S2 Contact form compare OR*

|  | Bit device |  |  | Word device |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D |
| S1 |  |  |  | * | * | * | * | * | * | * | * |
| S2 |  |  |  | * | * | * | * | * | * | * | * |

Notes on operand usage: $※:=,>,<,<>, \leqq, \geqq$
Please refer to the function specifications table for each device in series for the scope of device usage


Flag signal: none

## Explanation

- $\quad \mathbf{S}_{1}$ : data source device 1. $\mathbf{S}_{2}$ : data source device 2.
- This command compares the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$. Taking API $240(\mathrm{OR}=)$ as an example, when the result of comparison is equal, this command will be activated; when the result of comparison is unequal, this command will not be activated.
- The OR* command is a compare command in parallel with a contact.

| API No. | 16-bit commands | 32-bit commands | Conditions for activation | Conditions for inactivation |
| :---: | :---: | :---: | :---: | :---: |
| 240 | OR= | DOR = | $\mathbf{S}_{1}=\mathbf{S}_{\mathbf{2}}$ | $\mathrm{S}_{1} \neq \mathrm{S}_{2}$ |
| 241 | OR > | DOR > | $\mathbf{S}_{1}>\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathrm{S}_{\mathbf{2}}$ |
| 242 | OR< | DOR < | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ | $\mathrm{S}_{1} \geqq \mathrm{~S}_{\mathbf{2}}$ |
| 244 | OR $<>$ | DOR $<>$ | $\mathrm{S}_{1} \neq \mathrm{S}_{2}$ | $\mathbf{S}_{1}=\mathbf{S}_{\mathbf{2}}$ |
| 245 | $\mathrm{OR}<=$ | DOR $<=$ | $\mathrm{S}_{\mathbf{1}} \leqq \mathrm{S}_{\mathbf{2}}$ | $\mathbf{S}_{1}>\mathbf{S}_{\mathbf{2}}$ |
| 246 | $\mathrm{OR}>=$ | DOR $>=$ | $\mathbf{S}_{1} \geqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ |

## Example

- When $\mathrm{X} 0=\mathrm{On}$ and the current value of C 10 is also equal to $\mathrm{K} 200, \mathrm{Y} 10=\mathrm{On}$.
- When $\mathrm{X} 1=\mathrm{Off}$ and the content of register D 0 is not equal to $\mathrm{K}-10, \mathrm{Y} 11=\mathrm{On}$ and remains in that state.
- When X2 =On and the content of the 32-bit register D0 (D11) is less than 678,493, or M3=On, M50=On.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit command |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D | - | - | - | - |
| S1 |  |  |  |  |  |  |  |  | * | * | * |  |  |  |  |
| S2 |  |  |  |  |  |  |  |  | * | * | * | 32-bit command (9 STEP) |  |  |  |
| Notes | r |  | the | ncti |  | $\\|^{\wedge}$ vificati | ns | ble fo |  |  | e in | FLD※ | Continuous execution type | - | - |

Please refer to the function specifications table for each device in

Explanation - $\mathbf{S}_{1}$ : data source device 1. $\mathbf{S}_{2}$ : data source device 2.

- This command compares the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$. Taking "FLD=" as an example, if the result of comparison is "equal," this command will be activated; but it will not be activated when the result is "unequal."
- The FLD* command can directly input floating point numerical values (for instance: F 1.2 ) to the $\mathbf{S}_{1}, \mathbf{S}_{\mathbf{2}}$ operands, or store floating-point numbers in register D for use in operations.
- This command can be used while directly connected with the busbar

| API No. | 32-bit commands | Conditions for <br> activation | Conditions for <br> inactivation |
| :---: | :--- | :---: | :---: |
| 275 | FLD $=$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ |
| 276 | FLD $>$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ |
| 277 | FLD $<$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ |
| 278 | FLD $<>$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ |
| 279 | FLD $<=$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ |
| 280 | FLD $>=$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ |

- When the floating point number of register D200 (D201) is less than or equal to F1.2, and X 1 activated, contact Y 21 will be activated and remain in that state.



Explanation

- $\mathbf{S}_{1}$ : data source device 1. $\mathbf{S}_{2}$ : data source device 2.
- This command compares the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$. Taking "FAND=" as an example, if the result of comparison is "equal," this command will be activated; but it will not be activated when the result is "unequal."
- The FAND* command can directly input floating point numerical values (for instance: F1.2) to the $\mathbf{S}_{\mathbf{1}}, \mathbf{S}_{\mathbf{2}}$ operands, or store floating-point numbers in register $D$ for use in operations.
- This command can be used while directly connected with the busbar

| API No. | 32-bit commands | Conditions for <br> activation | Conditions for <br> inactivation |
| :---: | :--- | :---: | :---: |
| 281 | FAND $=$ | $\mathbf{S}_{1}=\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ |
| 282 | FAND $>$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ |
| 283 | FAND $<$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ |
| 284 | FAND $<>$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ |
| 285 | FAND $<=$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ |
| 286 | FAND $>=$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ |

- When $\mathrm{X} 1=$ Off, and the floating point number in register D100 (D101) is not equal to F1.2, Y21=On and remains in that state.



|  | Bit device |  |  | Word device |  |  |  |  |  |  |  | 16-bit co | mand |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | X | Y | M | K | H | KnX | KnY | KnM | T | C | D |  |  | - | - |
| S1 |  |  |  |  |  |  |  |  | * | * | * |  |  |  |  |
| S2 |  |  |  |  |  |  |  |  | * | * | * | 32-bit command (9 STEP) |  |  |  |
| Notes on operand usage: \#: \& , \|, ^ <br> Please refer to the function specifications table for each device in series for the scope of device usage |  |  |  |  |  |  |  |  |  |  |  | FOR※ | Continuous execution type | - | - |
|  |  |  |  |  |  |  |  |  |  |  |  | Flag signal: none |  |  |  |

Explanation - $\mathbf{S}_{1}$ : data source device 1. $\mathbf{S}_{2}$ : data source device 2.

- This command compares the content of $\mathbf{S}_{1}$ and $\mathbf{S}_{2}$. Taking "FOR=" as an example, if the result of comparison is "equal," this command will be activated; but it will not be activated when the result is "unequal."
- The FOR* command can directly input floating point numerical values (for instance: F1.2) to the $\mathbf{S}_{1}, \mathbf{S}_{\mathbf{2}}$ operands, or store floating-point numbers in register $D$ for use in operations.
- This command can be used while directly connected with the busbar

| API No. | 32-bit commands | Conditions for <br> activation | Conditions for <br> inactivation |
| :---: | :--- | :---: | :---: |
| 287 | FOR $=$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ |
| 288 | FOR $>$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ |
| 289 | FOR $<$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ |
| 290 | FOR $<>$ | $\mathbf{S}_{\mathbf{1}} \neq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}=\mathbf{S}_{\mathbf{2}}$ |
| 291 | FOR $<=$ | $\mathbf{S}_{\mathbf{1}} \leqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}>\mathbf{S}_{\mathbf{2}}$ |
| 292 | FOR $>=$ | $\mathbf{S}_{\mathbf{1}} \geqq \mathbf{S}_{\mathbf{2}}$ | $\mathbf{S}_{\mathbf{1}}<\mathbf{S}_{\mathbf{2}}$ |

- When X2 and M30 are both equal to "On," or the floating point number in register D100 (D101) is greater than or equal to $\mathrm{F} 1.234, \mathrm{M} 60=\mathrm{On}$.



## 13-7 Fault Display and Treatment

$\left.$| Code | ID | Descript | Recommended Treatment |
| :---: | :---: | :--- | :--- |
| PLod | 50 | Data writing memory error | Check if there is any error in the program <br> and download the program again. |
| PLSv | 51 | Data writing memory error while <br> executing programs | Cycle the power and download the program <br> again. |
| PLdA | 52 | Error while uploading programs | Upload again. If error still exists, return to <br> the factory for repair. |
| PLFn | 53 | Command error while <br> downloading programs | Check if there is any error in the program <br> and download the program again. |
| PLor | 54 | Program exceeds memory <br> capacity or no program | Cycle the power and download the program <br> again. |
| PLSn | 56 | Command error while executing <br> programs | Check if there is any error in the program <br> and download the program again. |
| PLEd | 57 | No "END" command in the <br> program | Check if there is any error in the program <br> and download the program again. |
| PLCr | 58 | Theck if there is any error in the program <br> continuously used for more than <br> and download the program again. |  |
| 9 times |  |  |  | | Check if there is any error in the program |
| :--- |
| and download the program again. |\(\left|\begin{array}{l}Error while downloading <br>

programs\end{array} \quad \begin{array}{l}Check if there is any error in the program <br>

and download the program again.\end{array}\right|\)| PLC scan time exceeds the |
| :--- |
| maximum allowable time |$\quad$| Check if the source code is correct and |
| :--- |
| download the program again. | \right\rvert\, | PLSF |
| :--- |
| 50 |

[This page intentionally left blank]

## Appendix A. Revision History

| Drive <br> Firmware <br> Version | Issued <br> Edition | Revision History | Issued Date |
| :---: | :---: | :---: | :---: |
| V1.01 | 00 | Newly established. | November, |
| 2022 |  |  |  |

[This page intentionally left blank]


[^0]:    Explanation
    (S): Encoding source device.

    D : Device that saves the encoding result.

[^1]:    Explanation

[^2]:    Explanation

    - $\quad \mathbf{S}$ : the designated source (binary floating point number).

    D: the ASIN value result.

[^3]:    Explanation

    - S: the designated source (binary floating point number).

    D: the COSH value result.

